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Australian National  
Fabrication Facility

# *Silicon-based Quantum Computing: From laboratory to industrial manufacture*

Andrew Dzurak

UNSW - Sydney

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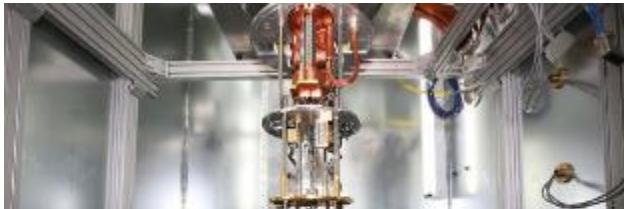
## Google Buys a Quantum Computer

By Quentin Hardy | May 16, 2013 5:00 am

Google and a corporation associated with NASA are forming a laboratory to study artificial intelligence by means of computers based on quantum physics. Their quantum computer, which will be thousands of times faster than existing supercomputers, is due to be use in the third quarter of this year.

A quantum computer deve

Credit Kim Stallknech



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### TECHNOLOGY



IBM's quantum computer gets powerful upgrade

Friday, May 19, 2017 - 01:55

The New York Times | <https://nyti.ms/2ferL3u>

TECHNOLOGY

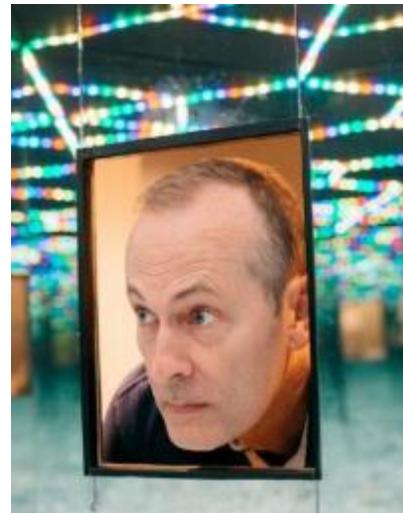
## Microsoft Spends Big to Build a Computer Out of Science Fiction

By JOHN MARKOFF NOV. 20, 2016

SAN FRANCISCO — Microsoft is putting its considerable financial and engineering muscle into the experimental field of quantum computing as it works to build a machine that could tackle problems beyond the reach of today's digital computers.

Todd Holmdahl will direct Microsoft's quantum computing efforts.

Credit Ian C. Bates for The New York Times



# Commercial Investments in QC

## *Superconducting QC*



Microsoft

*Topological QC*



*Superconducting QC  
& Silicon QC*



UNSW  
AUSTRALIA

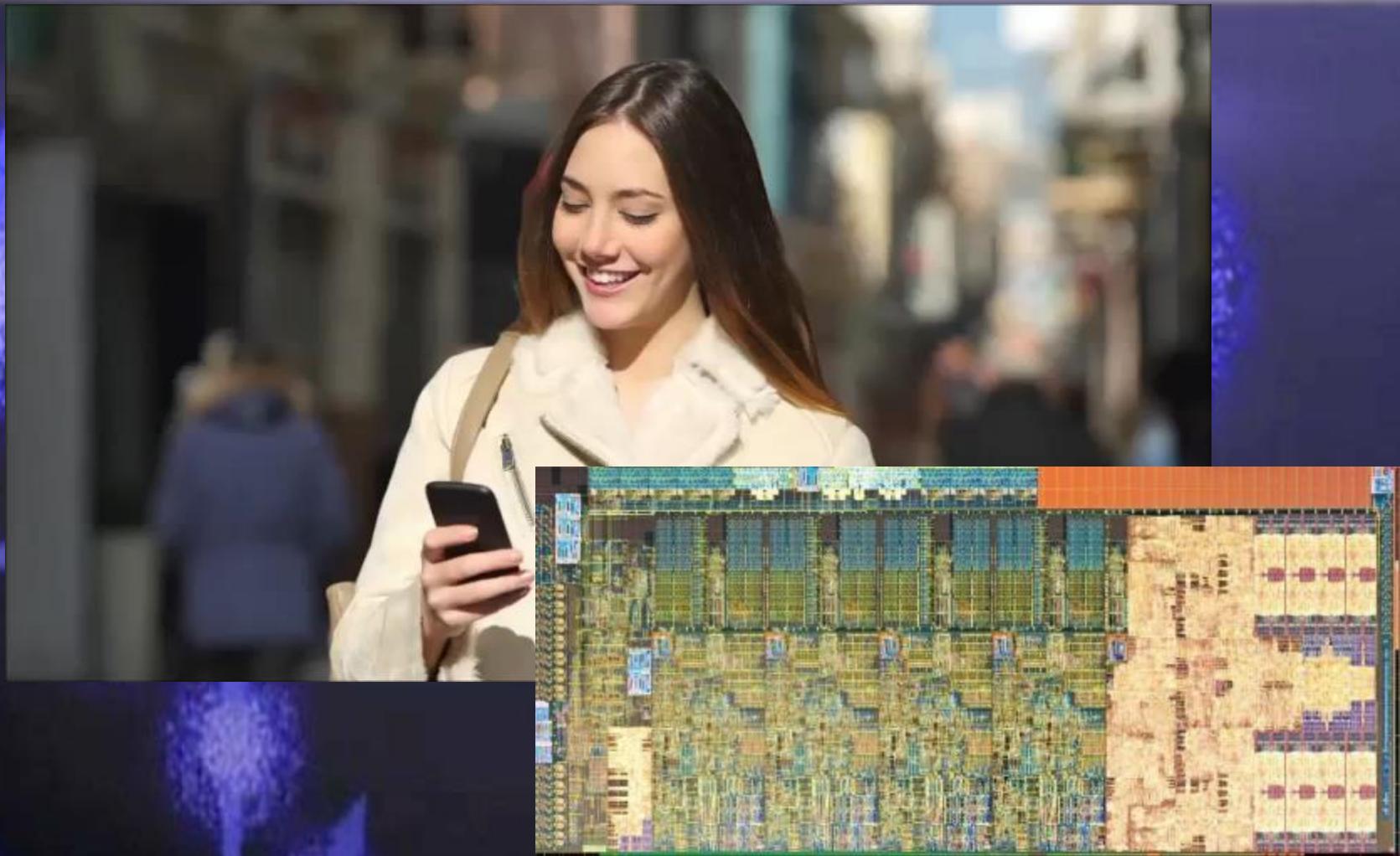


Australian Government



Commonwealth Bank

# *Silicon Quantum Computing*



# Spin Qubits in Silicon

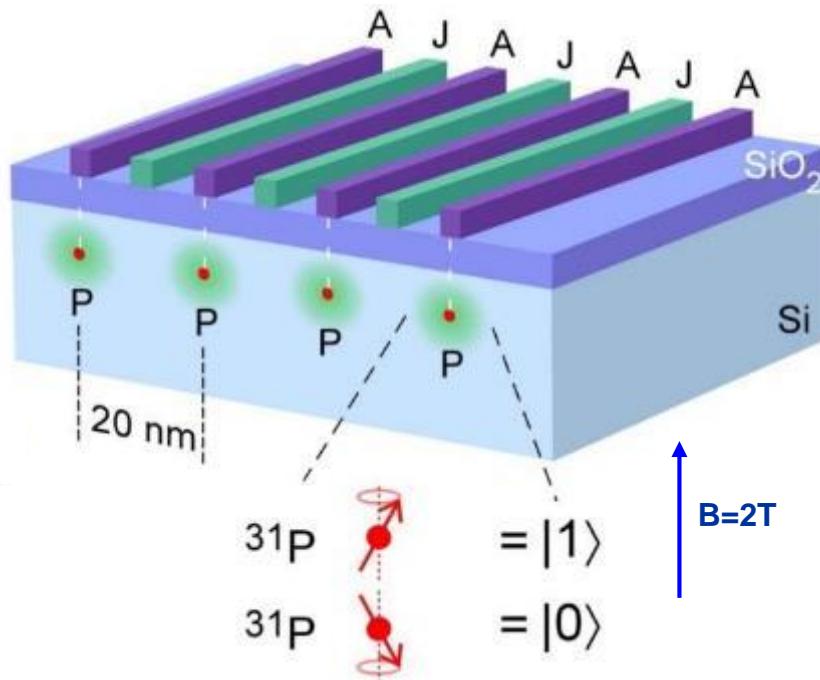
NATURE | VOL 393 | 14 MAY 1998

133

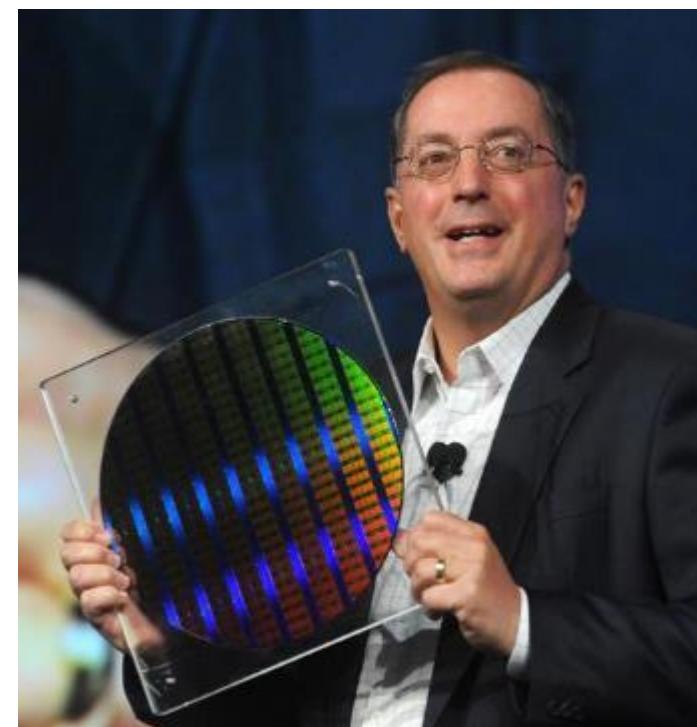
## A silicon-based nuclear spin quantum computer

B. E. Kane

Semiconductor Nanofabrication Facility, School of Physics, University of New South Wales, Sydney 2052, Australia



- Long Coherence Times in Silicon at 1K:
  - Nuclear – mins
  - Electron – ms-s
- Scalable
- Industry “Compatible”

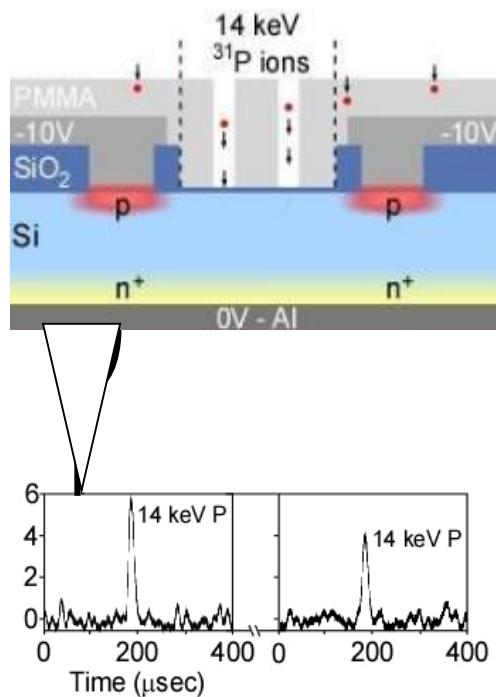


B.E. Kane, *Nature* 393, 133 (1998)

# Silicon Qubits: *Single-Atom* Nanotechnologies

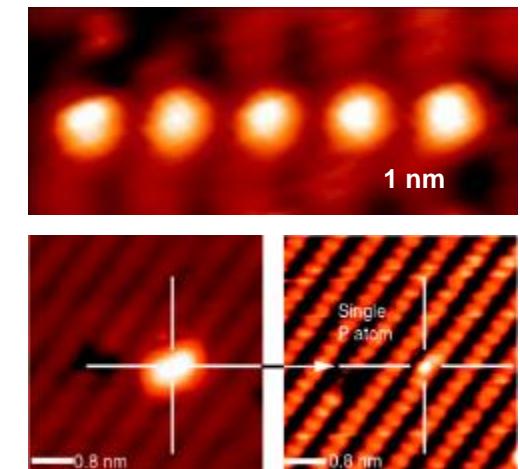
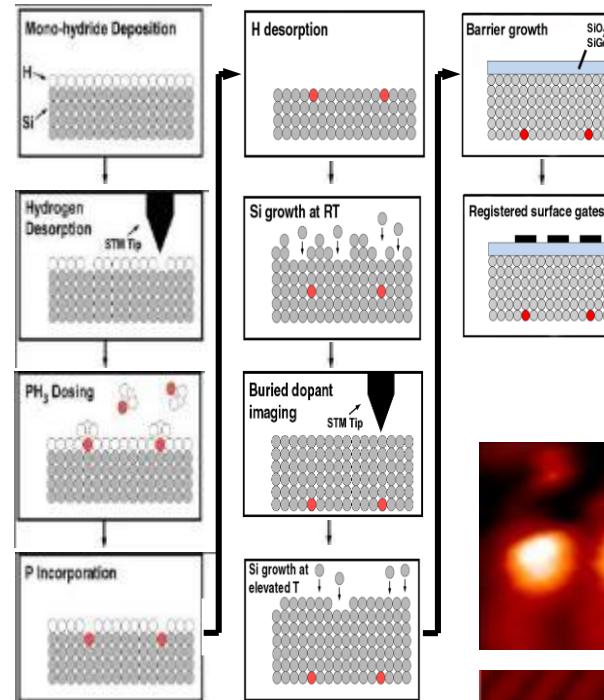
## Top-Down

Jamieson, Yang,  
Hopf, Hearne,  
Pakes, Prawer,  
Mitic, Gauja,  
Andresen, Hudson,  
Dzurak and Clark,  
**Appl. Phys. Lett.**  
**86**, 202101 (2005)

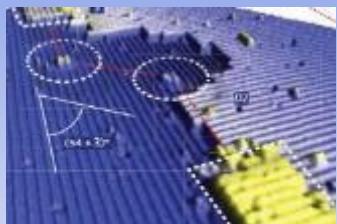


## Bottom-Up

O'Brien, Schofield,  
Simmons, Clark,  
Dzurak, Curson,  
Kane, McAlpine,  
Hawley and Brown,  
**Phys. Rev. B** **64**,  
R161401 (2001)



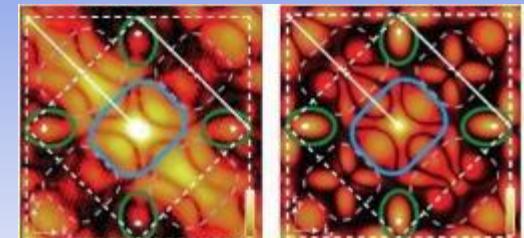
# Atomically Precise *STM-fabricated* Devices



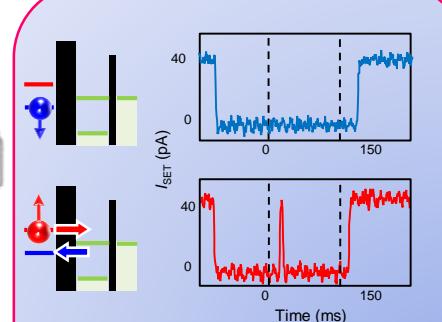
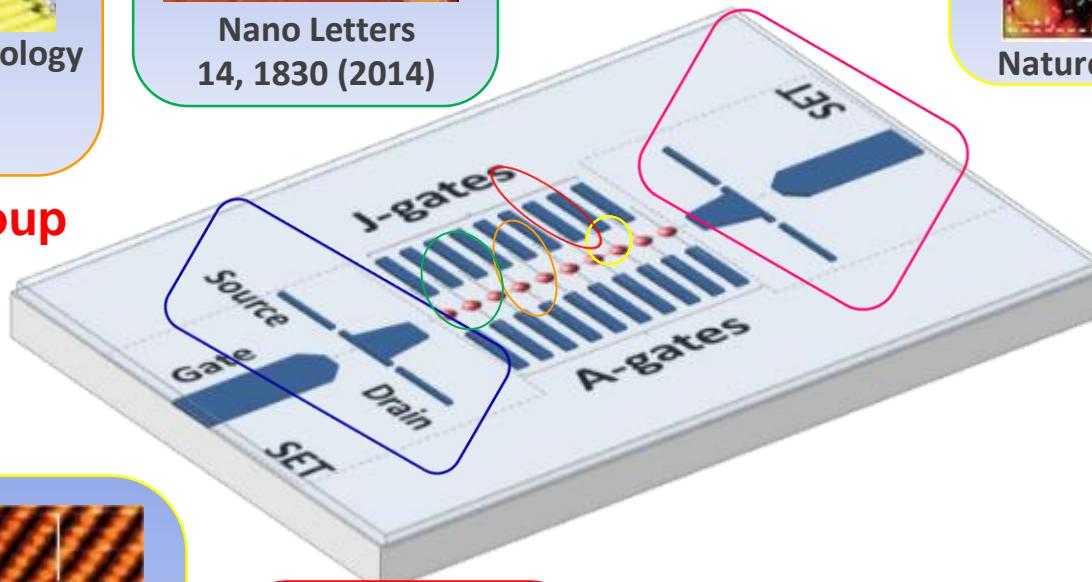
Nature Nanotechnology  
9, 430 (2014)



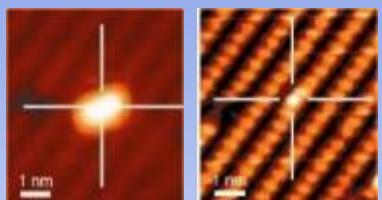
Nano Letters  
14, 1830 (2014)



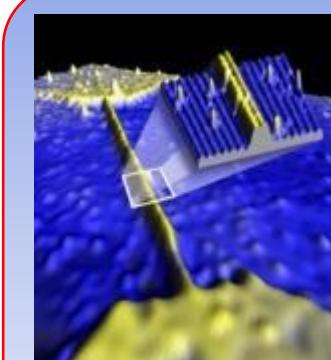
Nature Materials 13, 605 (2014)



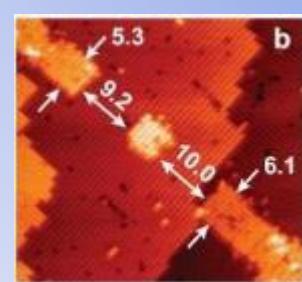
Nature Communications  
4, 2017 (2013)



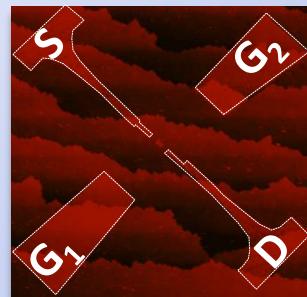
Nature Nanotechnology  
7, 242 (2012)



Science  
335 64 (2012)



Nature  
Nanotechnology  
5, 502 (2010)



Nano Letters  
11, 4376 (2011)

# *Ion-Implanted* Single-Atom Qubits

**Ion Implantation:**

**Prof. David Jamieson**

**U. Melbourne**



**Si Device Engineering:**

**Prof. Andrew Dzurak**

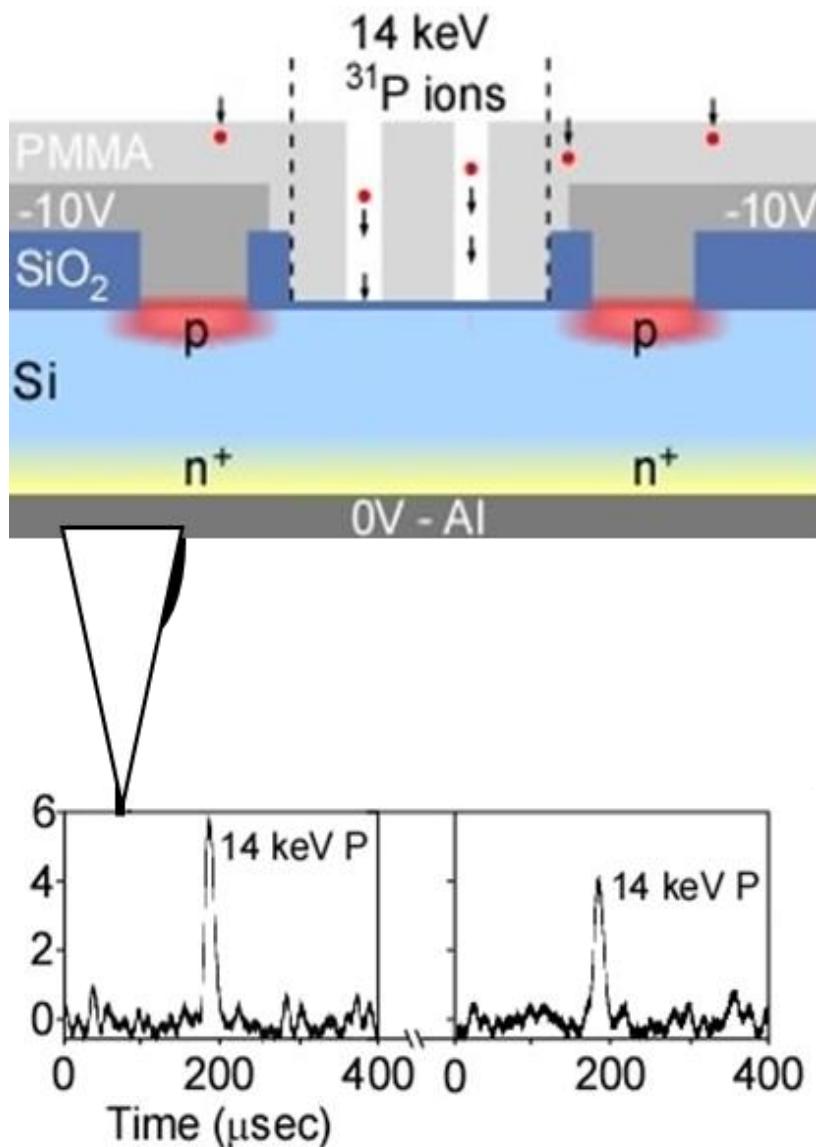
**UNSW**



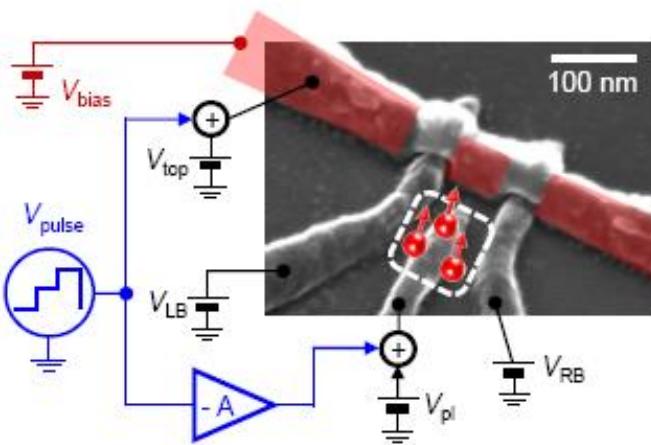
**Quantum Operations:**

**Prof. Andrea Morello**

**UNSW**



# Si:P e-Spin Qubit: *Single-Shot Readout*



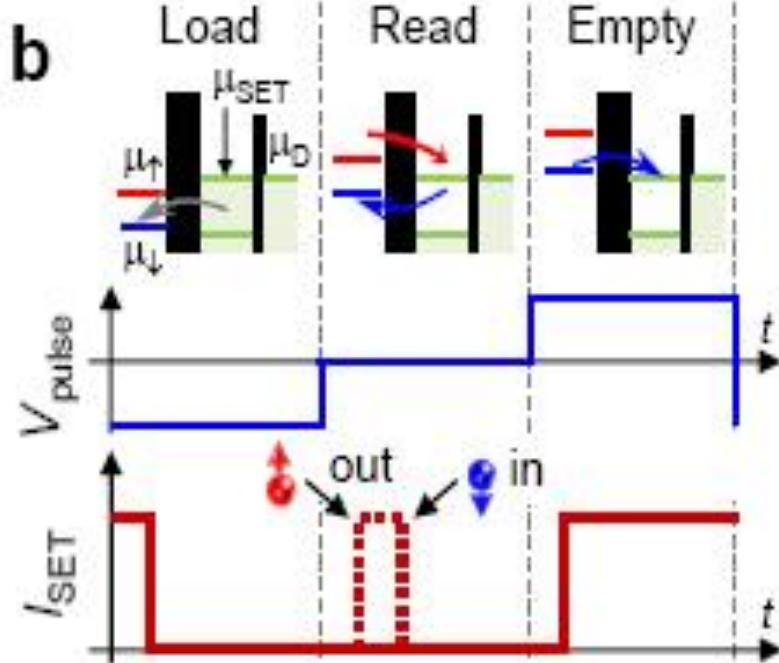
A. Morello et al., *Nature* **467**, 687 (2010)

nature

LETTERS

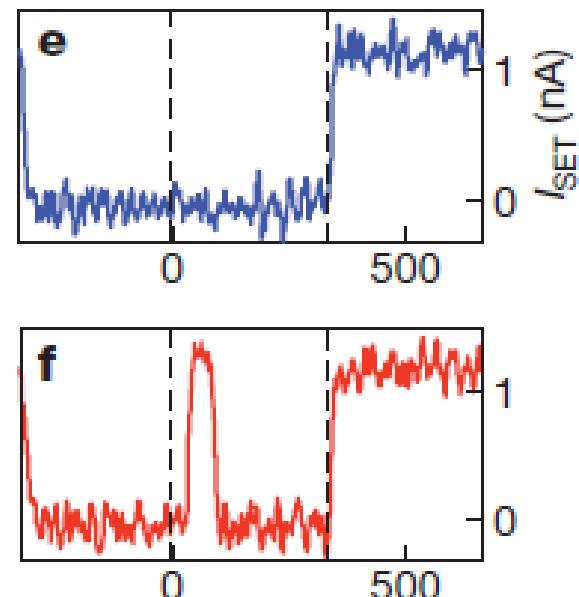
## Single-shot readout of an electron spin in silicon

Andrea Morello<sup>1</sup>, Jarryd J. Pla<sup>1</sup>, Floris A. Zwanenburg<sup>1</sup>, Kok W. Chan<sup>1</sup>, Kuan Y. Tan<sup>1</sup>, Hans Huebl<sup>1†</sup>, Mikko Möttönen<sup>1,3,4</sup>, Christopher D. Nugroho<sup>1†</sup>, Changyi Yang<sup>2</sup>, Jessica A. van Donkelaar<sup>2</sup>, Andrew D. C. Alves<sup>2</sup>, David N. Jamieson<sup>2</sup>, Christopher C. Escott<sup>1</sup>, Lloyd C. L. Hollenberg<sup>2</sup>, Robert G. Clark<sup>1†</sup> & Andrew S. Dzurak<sup>1</sup>



$T_{1e} = 6\text{s}$  (at 1.5T)

Fidelity > 90%

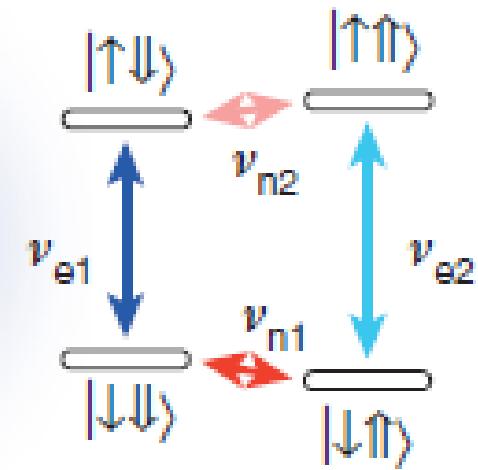
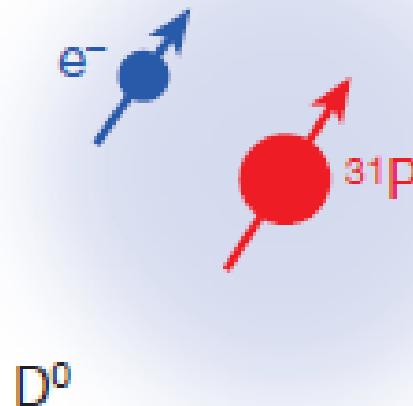
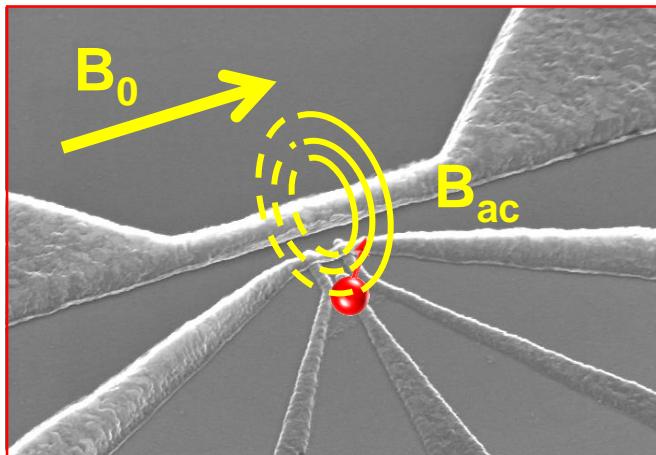


# Qubit Gate Operations: P-Donor ESR & NMR

On-chip microwave transmission line:

J.P. Dehollain *et al.*, *Nanotechnology* **24**, 015202 (2013)

**$^{31}\text{P:Si}$**   
**P-Donor**  
**Electron/Nuclear**  
**Spin Levels**



$$H = g\mu_B B_0 S_z - \gamma_n B_0 I_z + A \mathbf{I} \cdot \mathbf{S}$$

$\downarrow$  = Electron Spin,  $\mathbf{S}$

$\Downarrow$  = Nuclear Spin,  $\mathbf{I}$

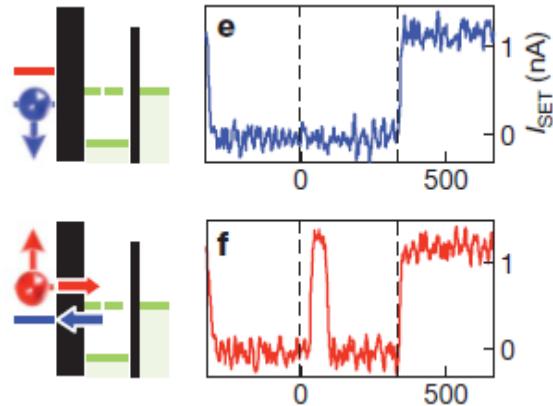
# Si:P Donor Electron & Nuclear Spin Qubits

A. Morello et al., *Nature* **467**, 687 (2010)

$$T_{1e} = 6\text{s} \text{ (at } 1.5\text{T)}$$

## Single-shot readout of an electron spin in silicon

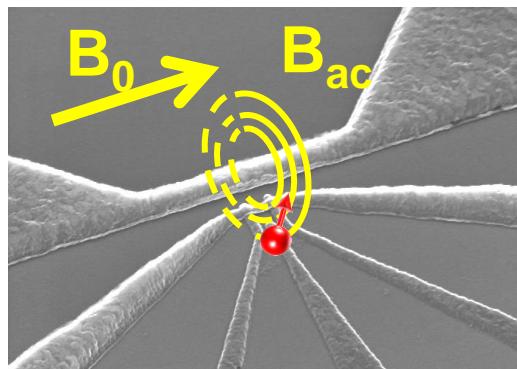
Andrea Morello<sup>1</sup>, Jarryd J. Pla<sup>1</sup>, Floris A. Zwanenburg<sup>1</sup>, Kok W. Chan<sup>1</sup>, Kuan Y. Tan<sup>1</sup>, Hans Huebl<sup>1†</sup>, Mikko Möttönen<sup>1,3,4</sup>, Christopher D. Nugroho<sup>1†</sup>, Changyi Yang<sup>2</sup>, Jessica A. van Donkelaar<sup>2</sup>, Andrew D. C. Alves<sup>2</sup>, David N. Jamieson<sup>2</sup>, Christopher C. Escott<sup>1</sup>, Lloyd C. L. Hollenberg<sup>2</sup>, Robert G. Clark<sup>1†</sup> & Andrew S. Dzurak<sup>1</sup>



## LETTER

J.J. Pla et al., *Nature* **489**, 541 (2012)

doi:10.1038/nature11449



## A single-atom electron spin qubit in silicon

Jarryd J. Pla<sup>1</sup>, Kuan Y. Tan<sup>1†</sup>, Juan P. Dehollain<sup>1</sup>, Wee H. Lim<sup>1</sup>, John J. L. Morton<sup>2†</sup>, David N. Jamieson<sup>3</sup>, Andrew S. Dzurak<sup>1</sup> & Andrea Morello<sup>1</sup>

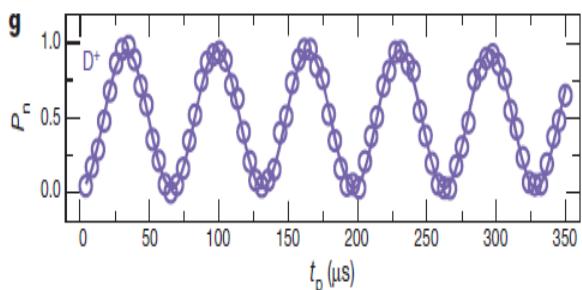
## LETTER

J.J. Pla et al., *Nature* **496**, 334 (2013)

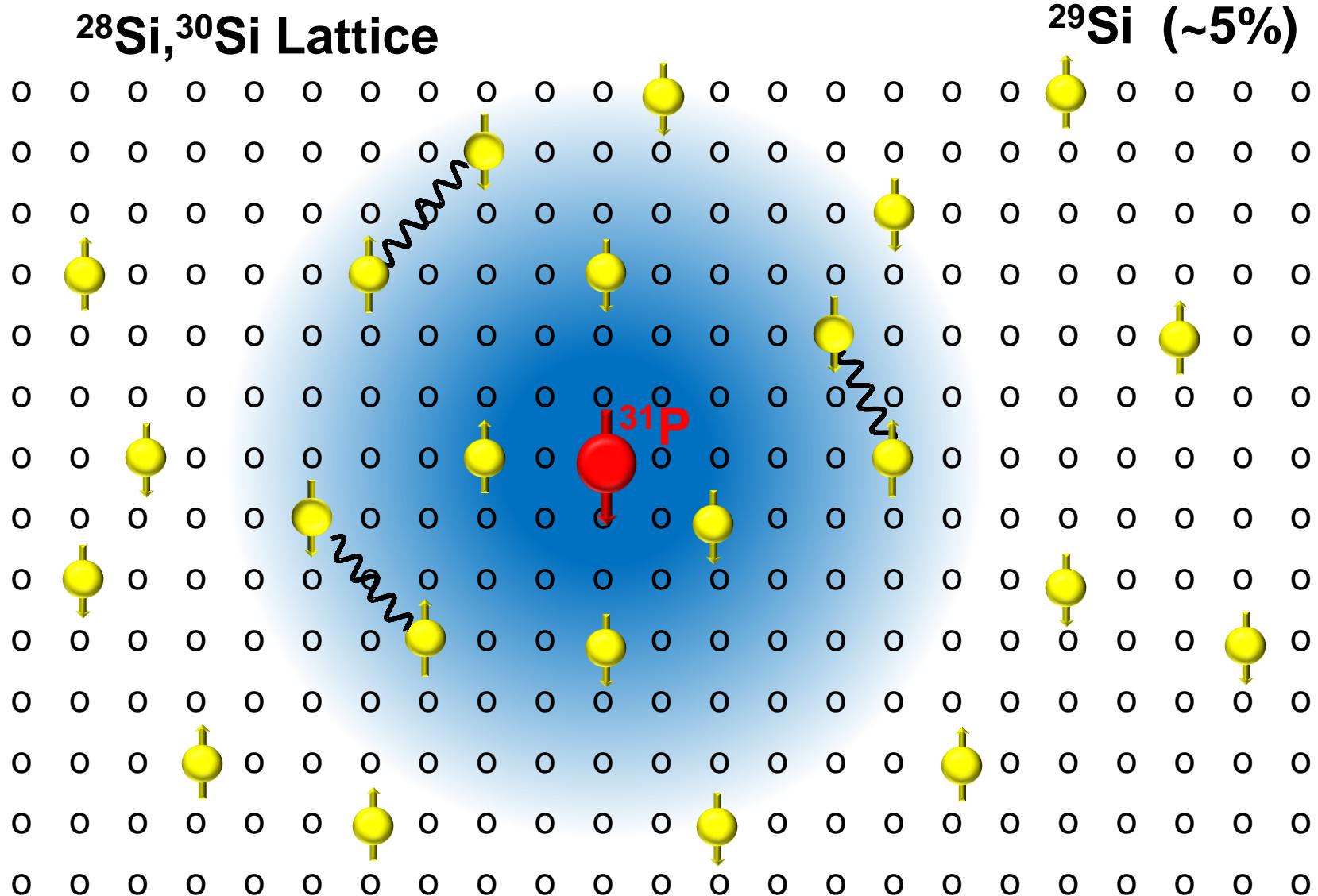
doi:10.1038/nature12011

## High-fidelity readout and control of a nuclear spin qubit in silicon

Jarryd J. Pla<sup>1</sup>, Kuan Y. Tan<sup>1†</sup>, Juan P. Dehollain<sup>1</sup>, Wee H. Lim<sup>1†</sup>, John J. L. Morton<sup>2</sup>, Floris A. Zwanenburg<sup>1†</sup>, David N. Jamieson<sup>3</sup>, Andrew S. Dzurak<sup>1</sup> & Andrea Morello<sup>1</sup>



# Lattice Nuclear Spin Noise in *natSi*



# Si:P Donors: Electron & Nuclear Spin Qubits in $^{28}\text{Si}$

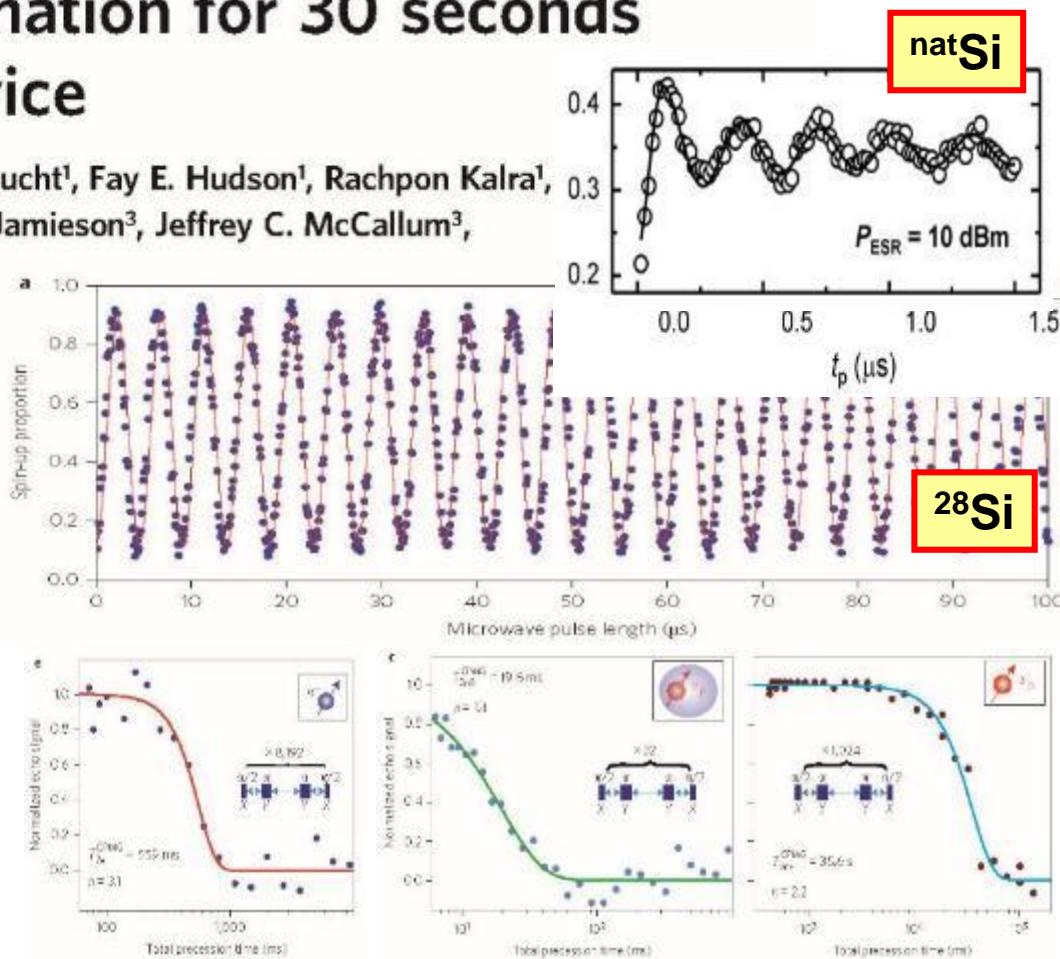
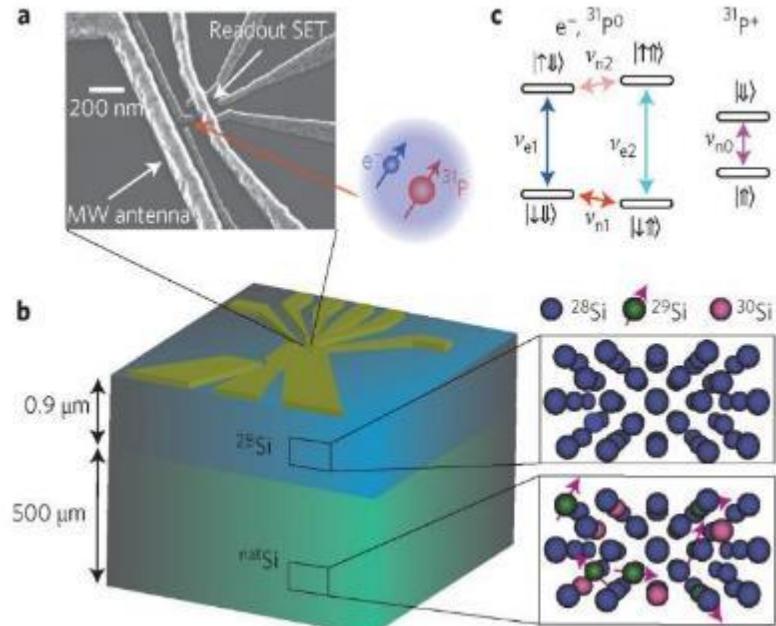
LETTERS

PUBLISHED ONLINE: 12 OCTOBER 2014 | DOI: 10.1038/NNANO.2014.211

nature  
nanotechnology

## Storing quantum information for 30 seconds in a nanoelectronic device

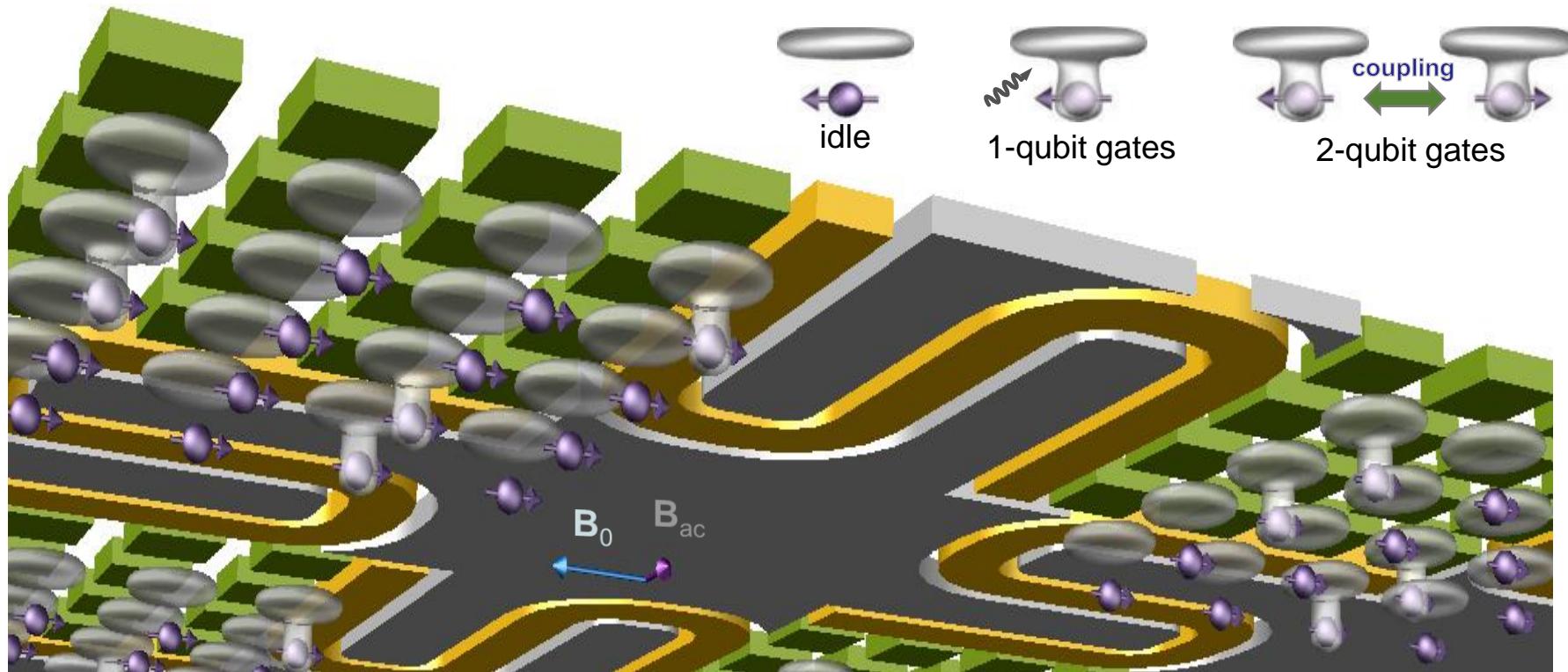
Juha T. Muhonen<sup>1\*</sup>, Juan P. Dehollain<sup>1</sup>, Arne Laucht<sup>1</sup>, Fay E. Hudson<sup>1</sup>, Rachpon Kalra<sup>1</sup>, Takeharu Sekiguchi<sup>2</sup>, Kohei M. Itoh<sup>2</sup>, David N. Jamieson<sup>3</sup>, Jeffrey C. McCallum<sup>3</sup>, Andrew S. Dzurak<sup>1</sup> and Andrea Morello<sup>1\*</sup>



# Implanted P-atom Qubits: Scalability Pathways

Morello Group

“Flip-Flop Qubits”



G. Tosi et al., *Nature Communications* (2017)

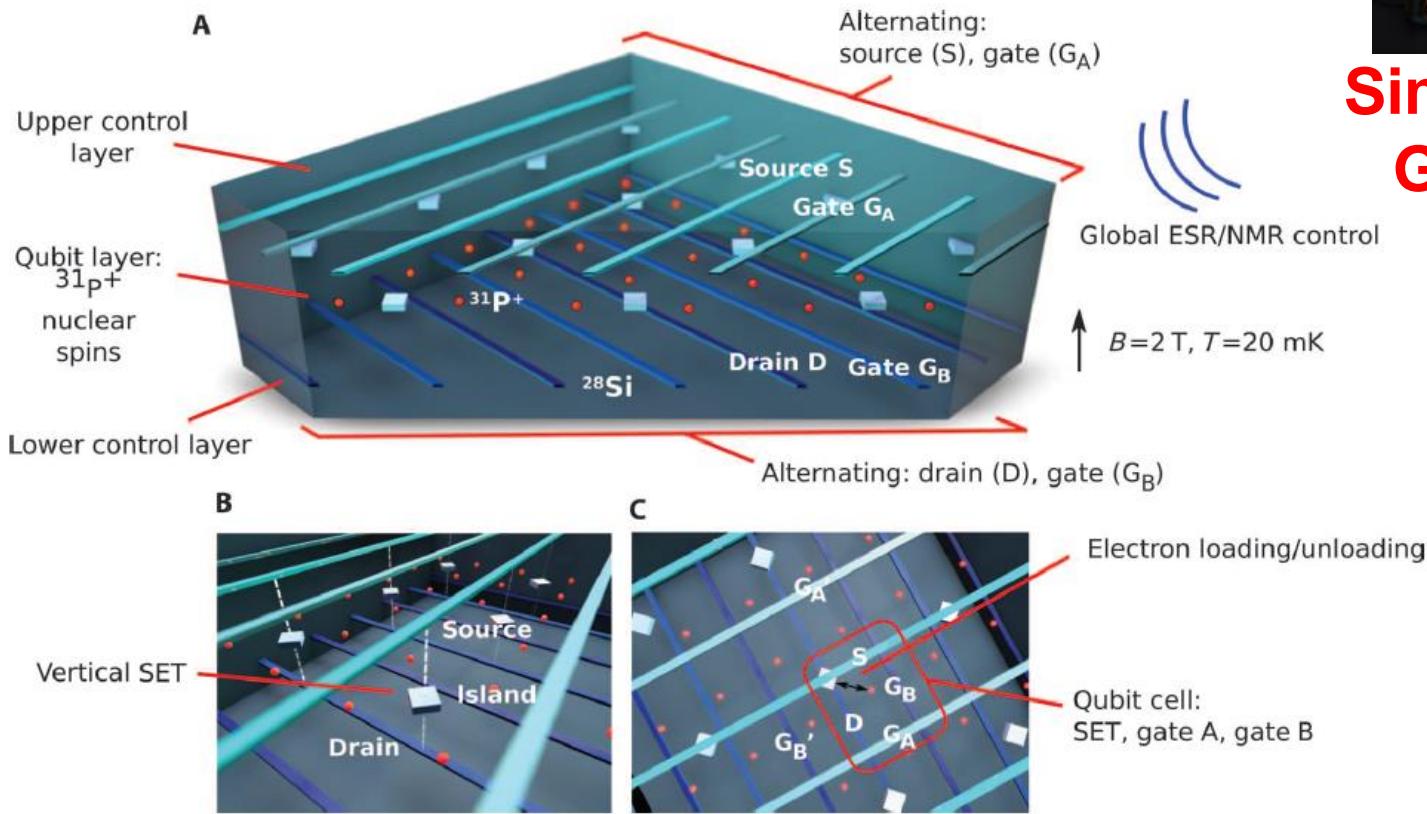
# STM-positioned P-atom Qubits: Scalability Pathways

## A surface code quantum computer in silicon

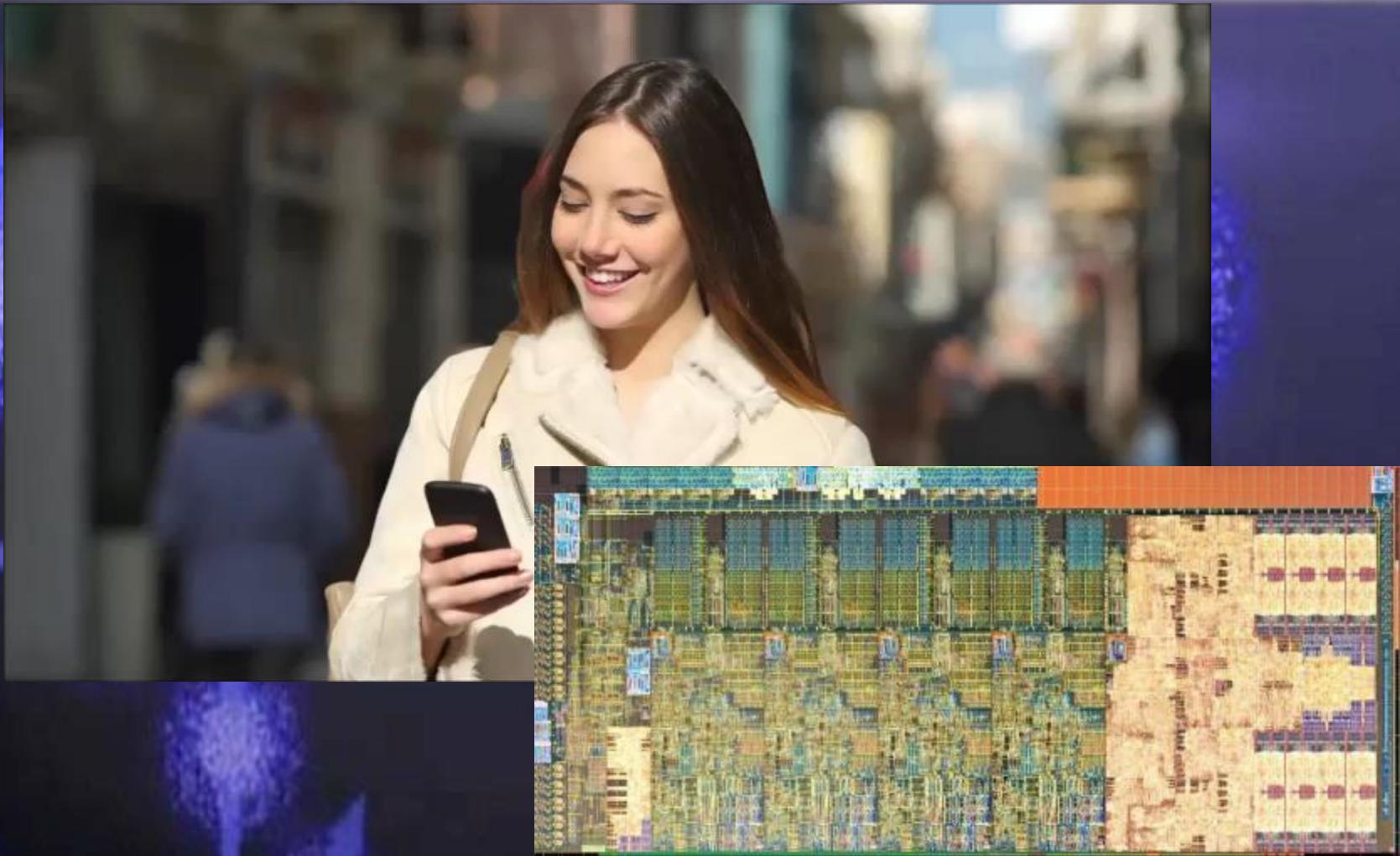
Charles D. Hill,<sup>1</sup> Eldad Peretz,<sup>2</sup> Samuel J. Hile,<sup>2</sup> Matthew G. House,<sup>2</sup> Martin Fuechsle,<sup>2</sup> Sven Rogge,<sup>2</sup> Michelle Y. Simmons,<sup>2</sup> Lloyd C. L. Hollenberg<sup>1\*</sup>



**Simmons  
Group**



# *Silicon CMOS mass production?*



# 1998: An important year for spin qubits ...

NATURE | VOL 393 | 14 MAY 1998

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## A silicon-based nuclear spin quantum computer

B. E. Kane

Semiconductor Nanofabrication Facility, School of Physics, University of New South Wales, Sydney 2052, Australia

PHYSICAL REVIEW A

VOLUME 57, NUMBER 1

JANUARY 1998

## Quantum computation with quantum dots

Daniel Loss<sup>1,2,\*</sup> and David P. DiVincenzo<sup>1,3,†</sup>

# Electron Spin *Qubits* based on *Quantum Dots*

PHYSICAL REVIEW A

VOLUME 57, NUMBER 1

JANUARY 1998

## Quantum computation with quantum dots

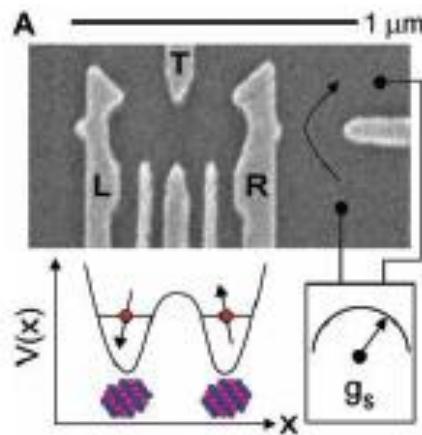
Daniel Loss<sup>1,2,\*</sup> and David P. DiVincenzo<sup>1,3,†</sup>

Coherent  
Coupled E  
Semiconducto  
Quantum Dots

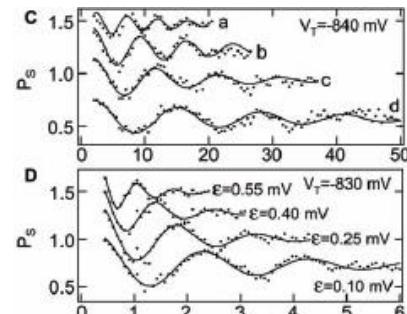
## Coherence times in GaAs limited by nuclear spin bath

Vol 442 | 7 August 2006 | doi:10.1038/nature05065

J. R. Petta,<sup>1</sup> A. C. Johnson,<sup>1</sup> J. M. Taylor,<sup>1</sup> E. A. Laird,<sup>1</sup> A. Yacoby,<sup>2</sup>  
M. D. Lukin,<sup>1</sup> C. M. Marcus,<sup>1</sup> M. P. Hanson,<sup>3</sup> A. C. Gossard<sup>3</sup>

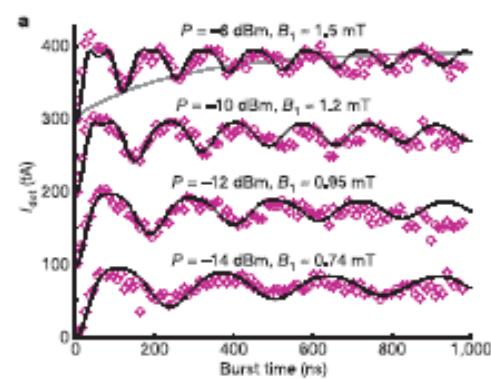
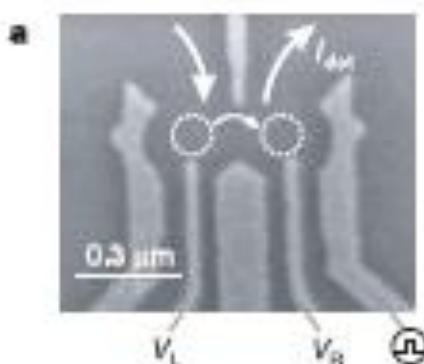


30 SEPTEMBER 2005 VOL 309 SCIENCE www.sciencemag.org



## Driven coherent oscillations of a single electron spin in a quantum dot

F. H. L. Koppens<sup>1</sup>, C. Buizert<sup>1</sup>, K. J. Tielrooij<sup>1</sup>, I. T. Vink<sup>1</sup>, K. C. Nowack<sup>1</sup>, T. Meunier<sup>1</sup>, L. P. Kouwenhoven<sup>1</sup>& L. M. K. Vandersypen<sup>1</sup>



# Si Q-Dot Qubits: Si/SiGe Heterostructures - I

LETTER

344 | NATURE | VOL 481 | 19 JANUARY 2012

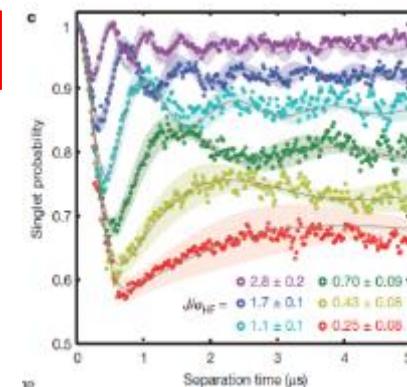
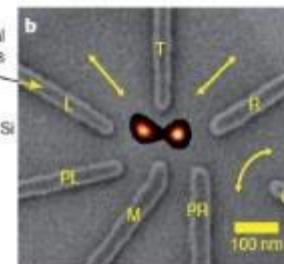
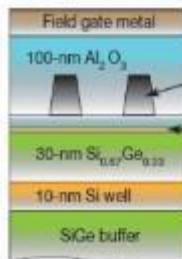
natSi

doi:10.1038/nature10707

$T_2^* = 360 \text{ ns}$

## Coherent singlet-triplet oscillations in a silicon-based double quantum dot

B. M. Maune<sup>1</sup>, M. G. Borselli<sup>1</sup>, B. Huang<sup>1</sup>, T. D. Ladd<sup>1</sup>, P. W. Deelman<sup>1</sup>, K. S. Holabird<sup>1</sup>, A. A. Kiselev<sup>1</sup>, R. S. Ross<sup>1</sup>, A. E. Schmitz<sup>1</sup>, M. Sokolich<sup>1</sup>, C. A. Watson<sup>1</sup>, M. F. Gyure<sup>1</sup> & A. T. Hunter<sup>1</sup>



RESEARCH ARTICLE

Eng et al. Sci. Adv. 2015;1:e1500214 29 May 2015

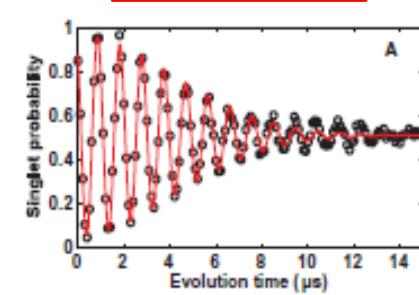
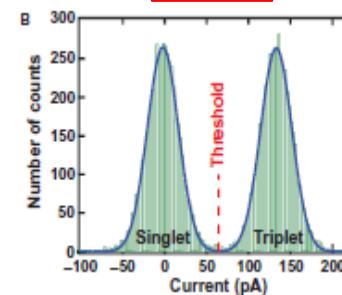
<sup>28</sup>Si

$T_2^* = 6 \mu\text{s}$

CONDENSED MATTER PHYSICS

## Isotopically enhanced triple-quantum-dot qubit

Kevin Eng, Thaddeus D. Ladd, Aaron Smith, Matthew G. Borselli, Andrey A. Kiselev, Bryan H. Fong, Kevin S. Holabird, Thomas M. Hazard, Biqin Huang, Peter W. Deelman, Ivan Milosavljevic, Adele E. Schmitz, Richard S. Ross, Mark F. Gyure, Andrew T. Hunter\*



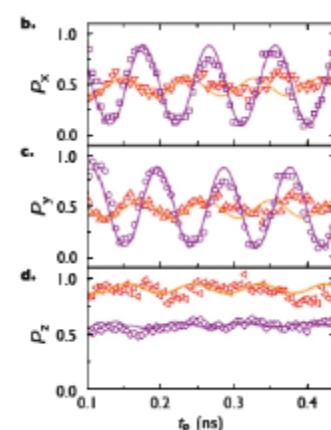
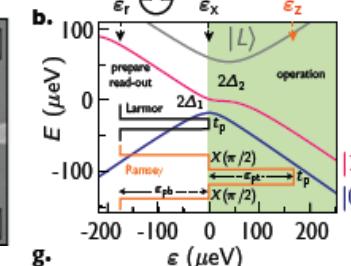
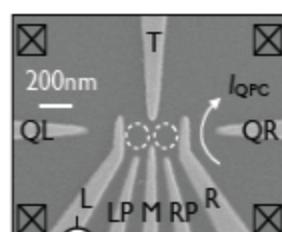
## Quantum control and process tomography of a semiconductor quantum dot hybrid qubit

Dohun Kim,<sup>1</sup> Zhan Shi,<sup>1</sup> C. B. Simmons,<sup>1</sup> D. R. Ward,<sup>1</sup> J. R. Prance,<sup>1</sup> Teck Seng Koh,<sup>1</sup> John King Gamble,<sup>2</sup> D. E. Savage,<sup>3</sup> M. G. Lagally,<sup>3</sup> Mark Friesen,<sup>1</sup> S. N. Coppersmith,<sup>1</sup> and M. A. Eriksson<sup>1</sup>

Nature 511, 70 (2014)

natSi

$T_2^* = 2\text{-}10 \text{ ns}$ ;  $T_\pi \sim 50 \text{ ps}$ ;  $F_C = 85\text{-}94\%$



# Si Q-Dot Qubits: Si/SiGe Heterostructures - II

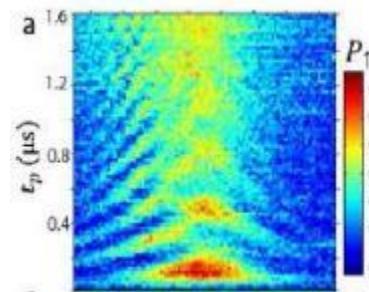
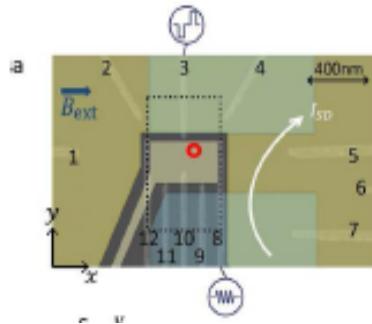
*Nature Nanotechnology* 9, 666 (2014)

natSi

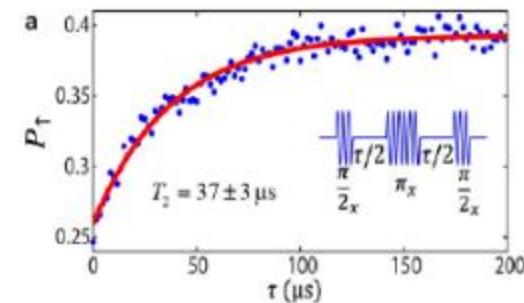
EDSR -  $\mu$ Magnet

## Electrical control of a long-lived spin qubit in a Si/SiGe quantum dot

E. Kawakami<sup>1†</sup>, P. Scarlino<sup>1†</sup>, D. R. Ward<sup>2</sup>, F. R. Braakman<sup>1,3</sup>, D. E. Savage<sup>2</sup>, M. G. Lagally<sup>2</sup>, Mark Friesen<sup>2</sup>, S. N. Coppersmith<sup>2</sup>, M. A. Eriksson<sup>2</sup>, and L. M. K. Vandersypen<sup>1\*</sup>



$T_2^* = 1 \mu\text{s}; T_2 = 37 \mu\text{s}$



$F_C = 99\%$  via RBM - *PNAS* 113, 11738 (2016)

*Science Advances* 2, e1600694 (2016)

## A fault-tolerant addressable spin qubit in a natural silicon quantum dot

K. Takeda,<sup>1</sup> J. Kamioka,<sup>2</sup> T. Otsuka,<sup>1</sup> J. Yoneda,<sup>1</sup> T. Nakajima,<sup>1</sup>

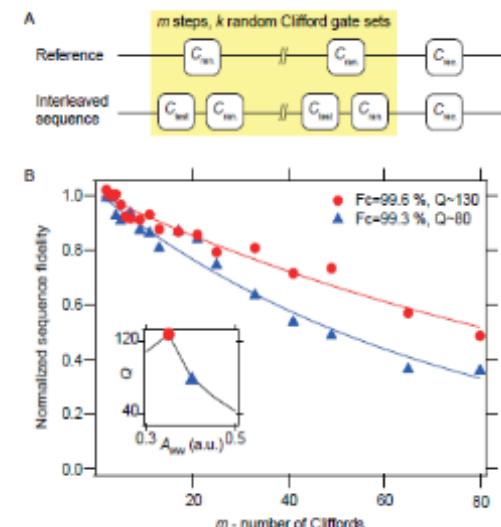
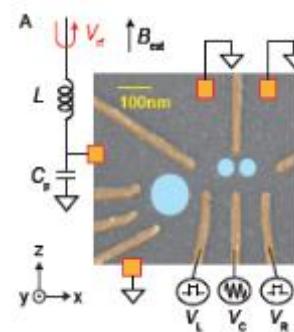
M.R. Delbecq,<sup>1</sup> S. Amaha,<sup>1</sup> G. Allison,<sup>1</sup> T. Kodera,<sup>2</sup> S. Oda,<sup>2</sup> and S. Tarucha<sup>1,3</sup>

natSi

EDSR -  $\mu$ Magnet

$T_2^* \sim 2 \mu\text{s}$

$F_C = 99.6\%$  via RBM



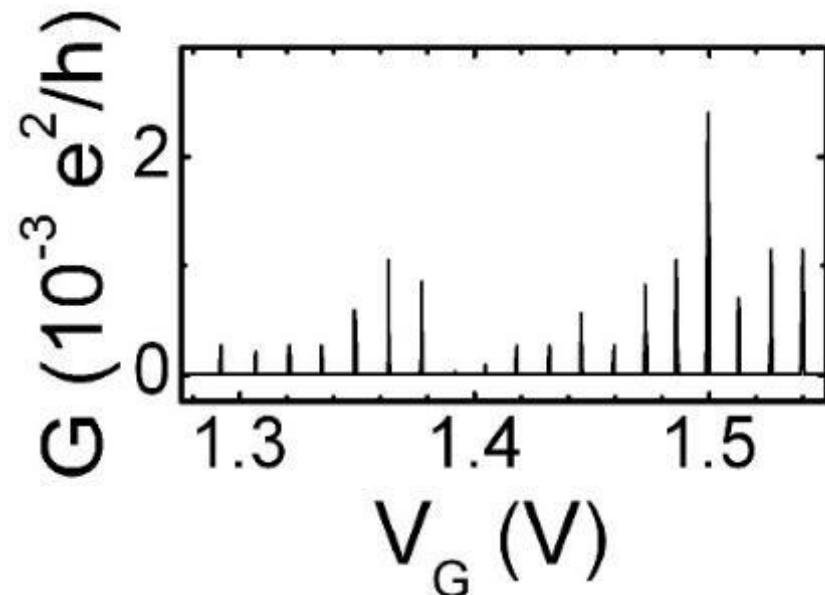
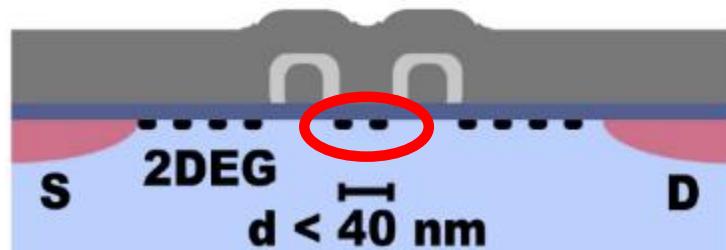
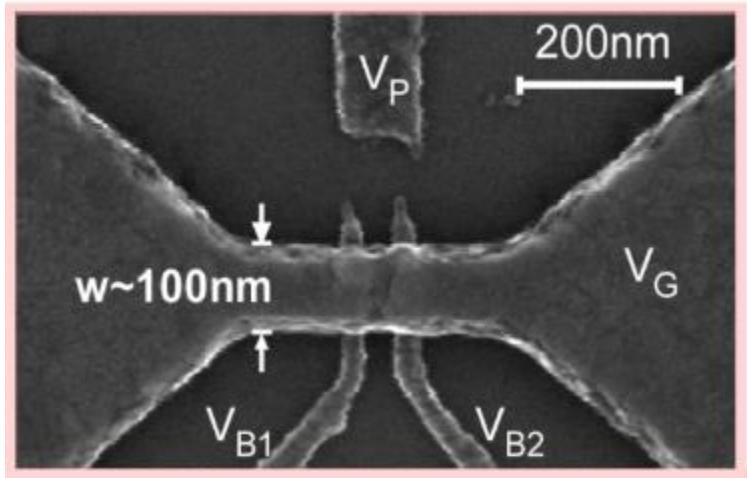
# Silicon-MOS Quantum Dots

NANO  
LETTERS

2007  
Vol. 7, No. 7  
2051–2055

## Gate-Defined Quantum Dots in Intrinsic Silicon

Susan J. Angus,\* Andrew J. Ferguson, Andrew S. Dzurak, and Robert G. Clark



# <sup>28</sup>SiMOS Q-Dots: 1-Qubit Gate, Fidelity > 99%

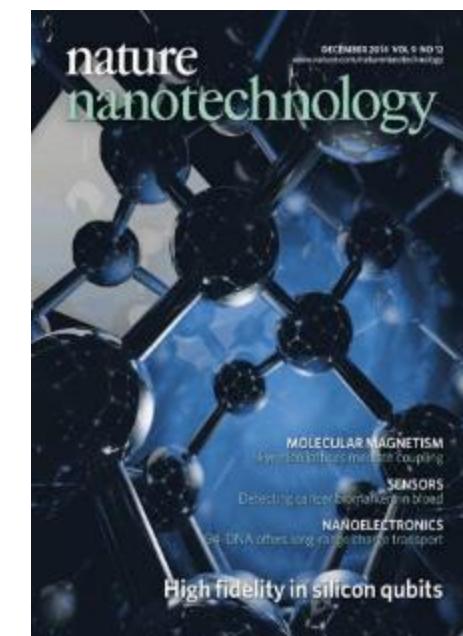
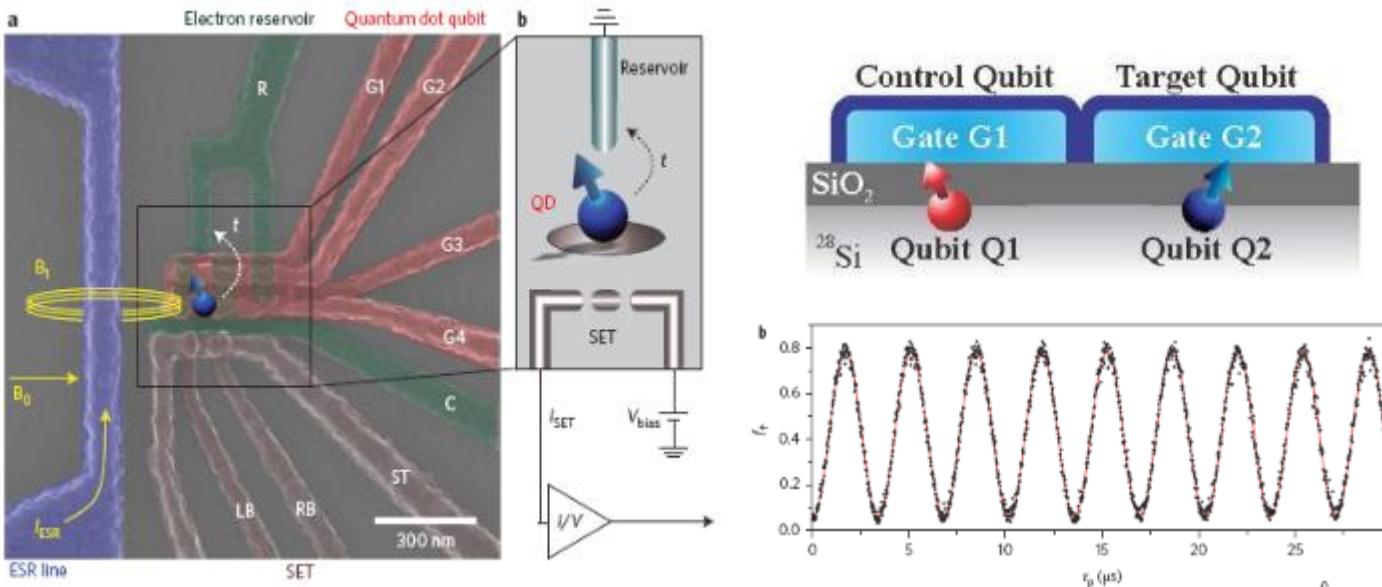
nature  
nanotechnology

LETTERS

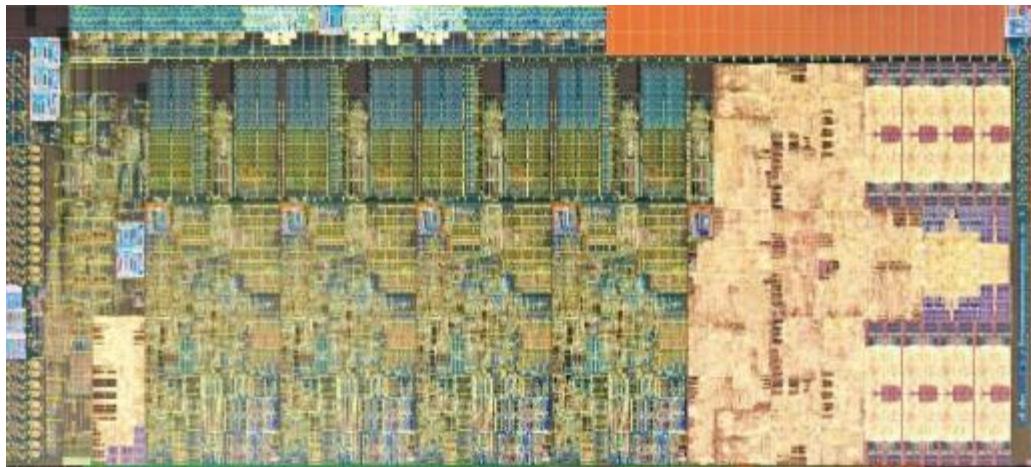
PUBLISHED ONLINE: 12 OCTOBER 2014 | DOI: 10.1038/NNANO.2014.216

## An addressable quantum dot qubit with fault-tolerant control-fidelity

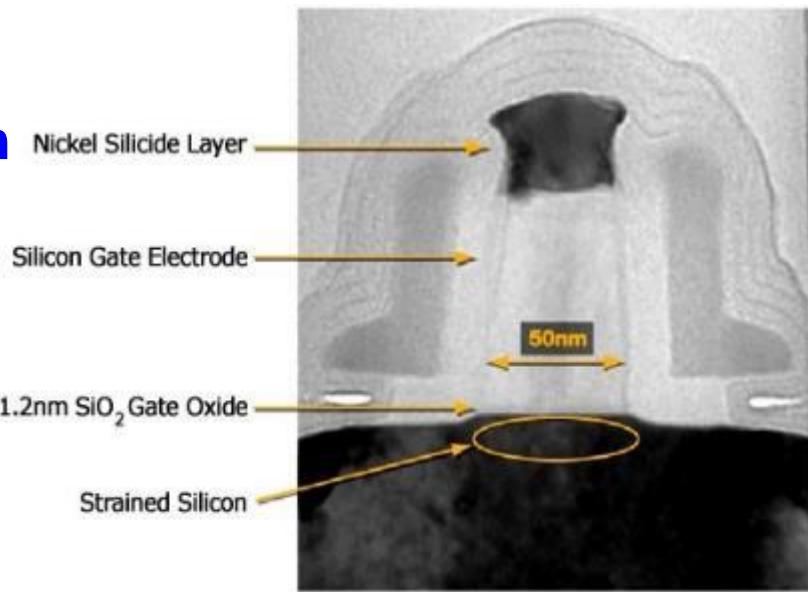
M. Veldhorst<sup>1\*</sup>, J. C. C. Hwang<sup>1</sup>, C. H. Yang<sup>1</sup>, A. W. Leenstra<sup>2</sup>, B. de Ronde<sup>2</sup>, J. P. Dehollain<sup>1</sup>, J. T. Muhonen<sup>1</sup>, F. E. Hudson<sup>1</sup>, K. M. Itoh<sup>3</sup>, A. Morello<sup>1</sup> and A. S. Dzurak<sup>1\*</sup>



# SiMOS Q-Dot Qubits: *CMOS Compatibility*



Intel Pentium  
Silicon  
MOSFET  
Transistor  
65nm Node  
(2005)



Silicon MOS  
Single-electron  
Qubit (2014)



# SiMOS Q-Dot: Large & Tunable Valley Splitting, $\Delta E_v$

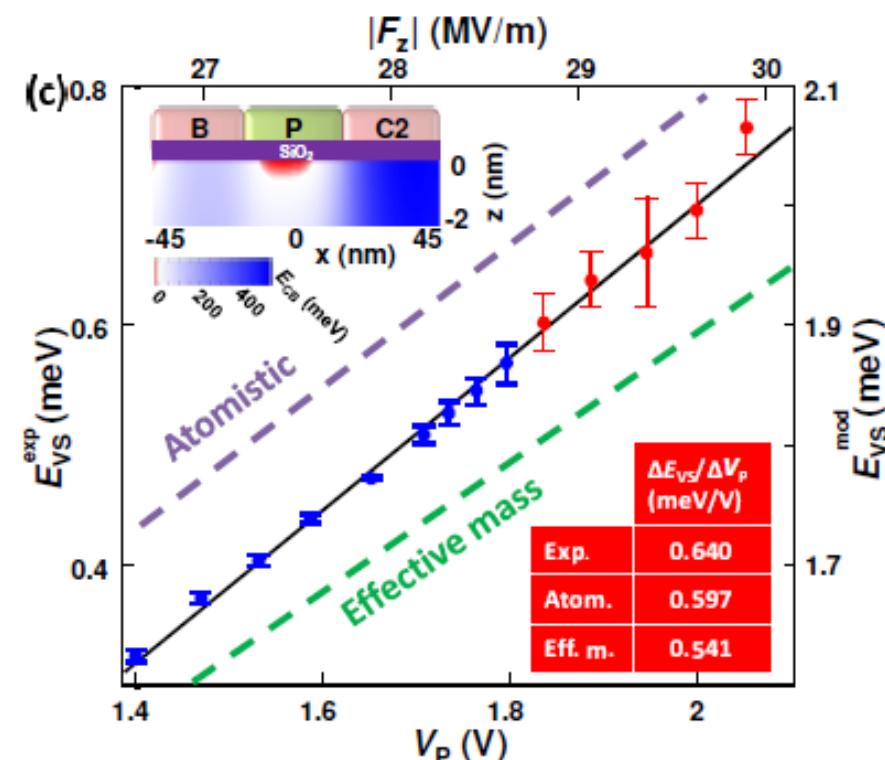
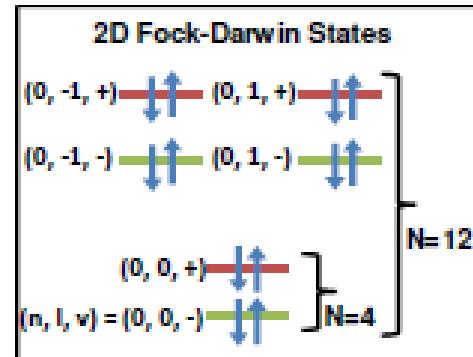
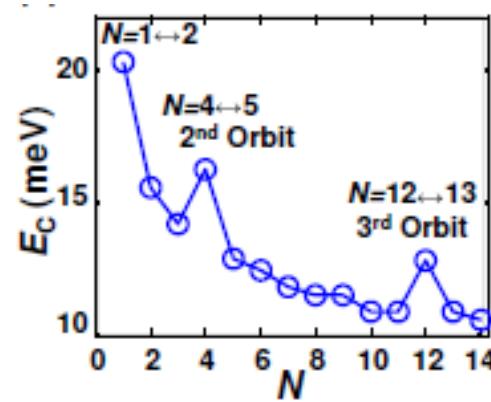
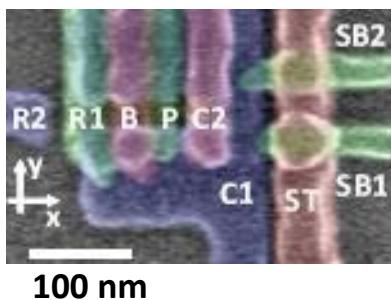
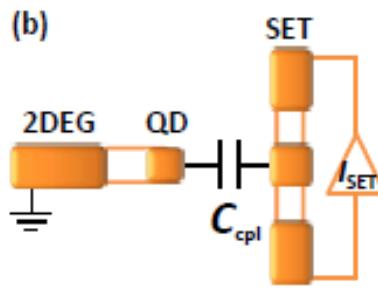
ARTICLE

Received 11 Jan 2013 | Accepted 27 May 2013 | Published 27 Jun 2013



## Spin-valley lifetimes in a silicon quantum dot with tunable valley splitting

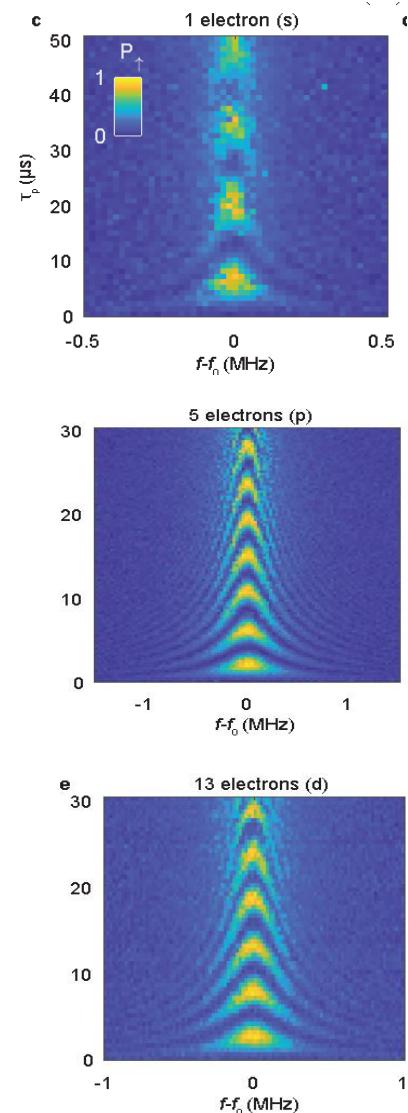
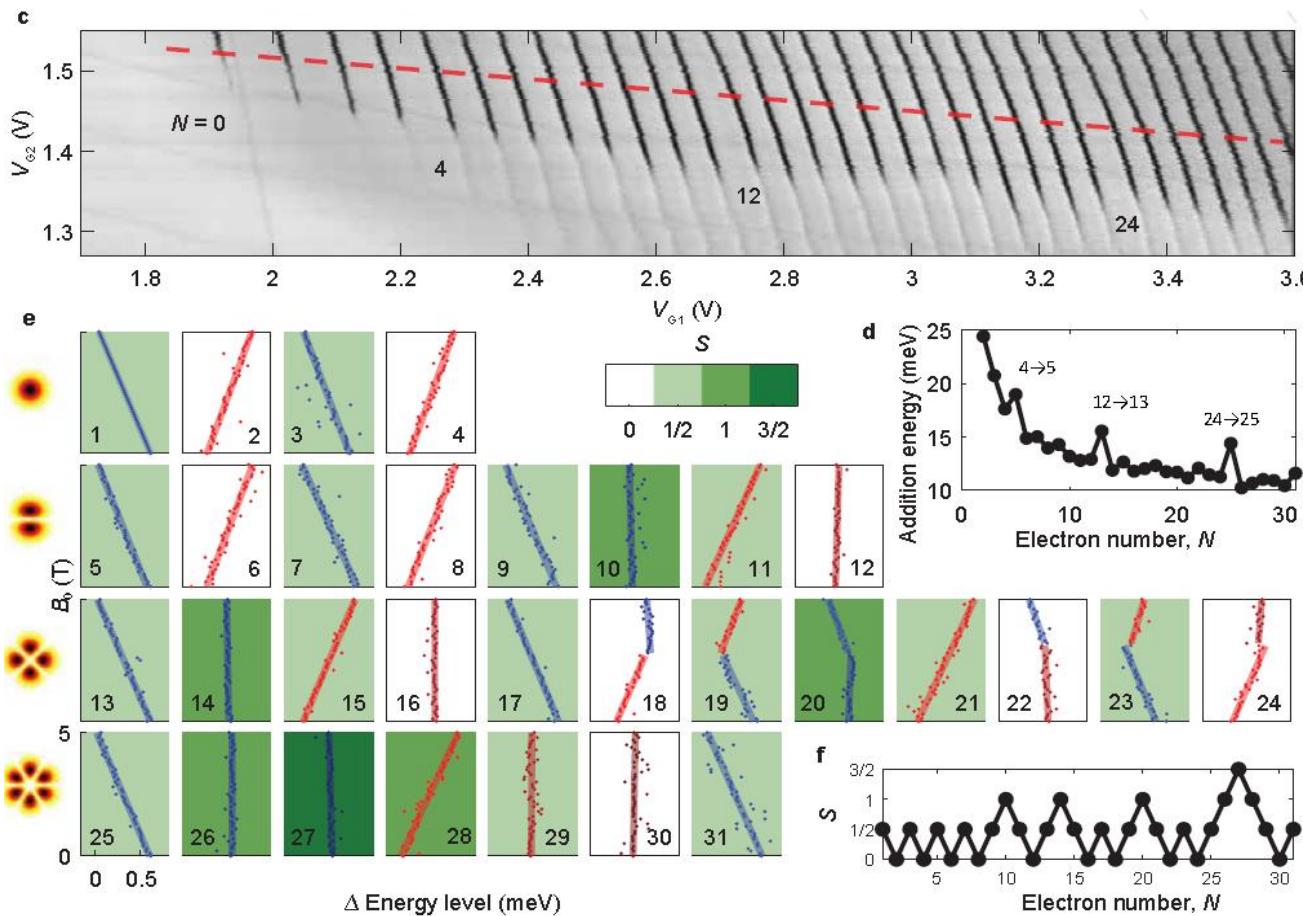
C.H. Yang<sup>1</sup>, A. Rossi<sup>1</sup>, R. Ruskov<sup>2</sup>, N.S. Lai<sup>1</sup>, F.A. Mohiyaddin<sup>1</sup>, S. Lee<sup>3</sup>, C. Tahan<sup>2</sup>, G. Klimeck<sup>3</sup>, A. Morello<sup>1</sup> & A.S. Dzurak<sup>1</sup>



# SiMOS Q-Dot = Artificial Atom: Monovalent Qubits

Coherent spin control of s-, p-, d- and f-electrons in a silicon quantum dot

R. C. C. Leon,<sup>1,\*</sup> C. H. Yang,<sup>1</sup> J. C. C. Hwang,<sup>1,†</sup> J. Camirand Lemyre,<sup>2</sup> T. Tanttu,<sup>1</sup> W. Huang,<sup>1</sup> K. W. Chan,<sup>1</sup> K. Y. Tan,<sup>3</sup> F. E. Hudson,<sup>1</sup> K. M. Itoh,<sup>4</sup> A. Morello,<sup>1</sup> A. Laucht,<sup>1</sup> M. Pioro-Ladrière,<sup>2,5</sup> A. Saraiva,<sup>1,‡</sup> and A. S. Dzurak<sup>1,§</sup>



# SiMOS: pMOS Hole QDs

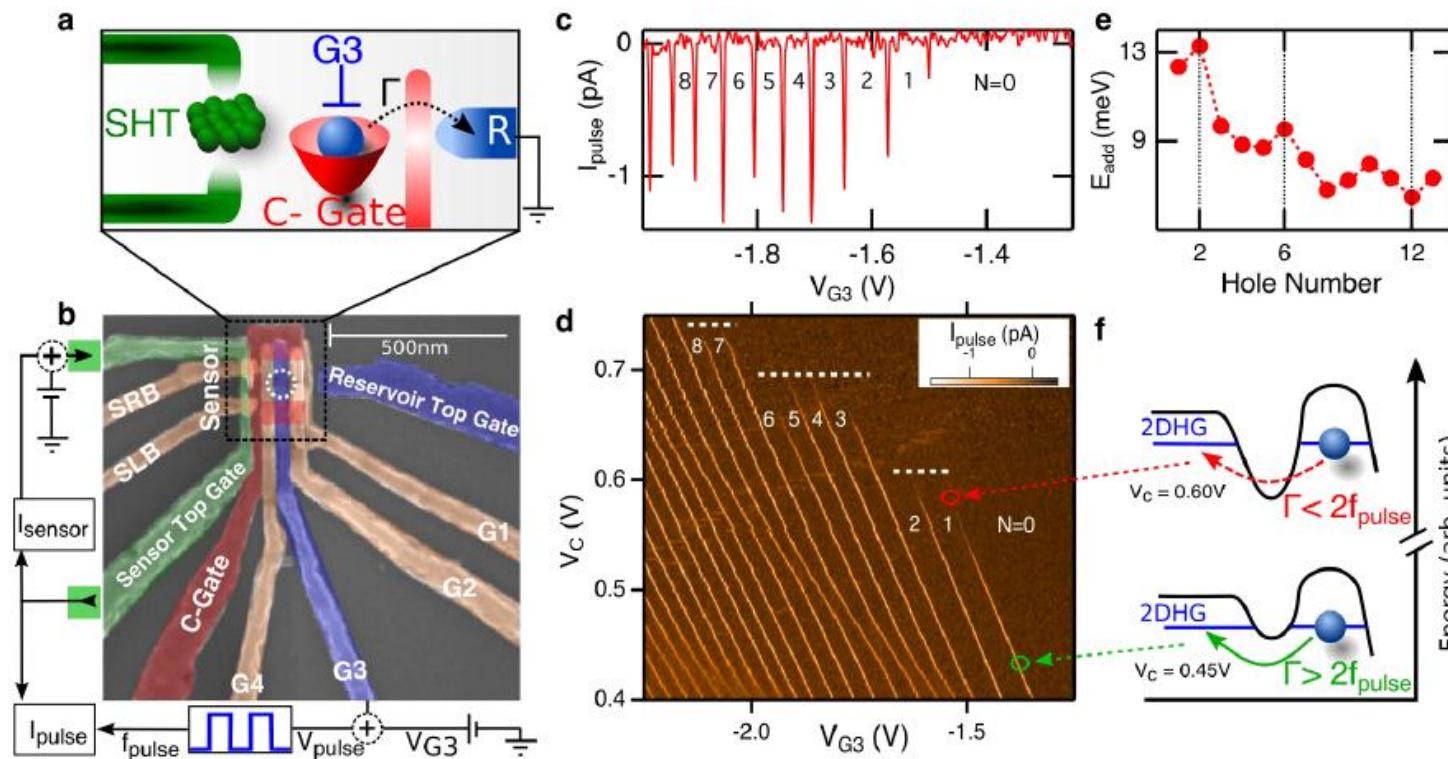
Spin filling and orbital structure of the first six holes in a silicon metal-oxide-semiconductor quantum dot

S. D. Liles<sup>1</sup>, R. Li<sup>1,3</sup>, C. H. Yang<sup>2</sup>, F. E. Hudson<sup>2</sup>, M. Veldhorst<sup>2,3</sup>, A. S. Dzurak<sup>2</sup>, A. R. Hamilton<sup>1</sup>

<sup>1</sup> School of Physics, University of New South Wales, Sydney NSW 2052, Australia

<sup>2</sup> Centre for Quantum Computation and Communication Technology, School of Electrical Engineering and Telecommunications, The University of New South Wales, Sydney NSW 2052, Australia

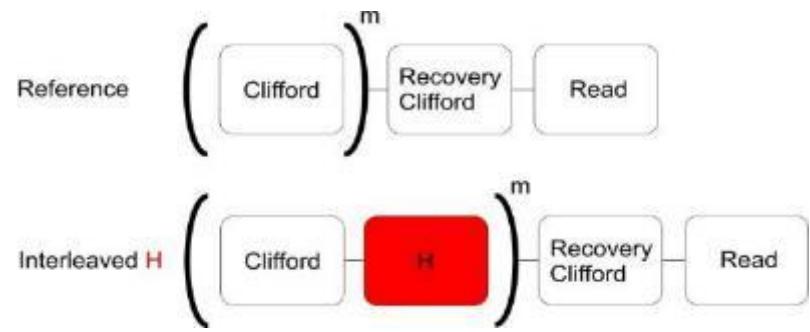
<sup>3</sup> QuTech and Kavli Institute of Nanoscience, TU Delft, 2600 GA Delft, The Netherlands



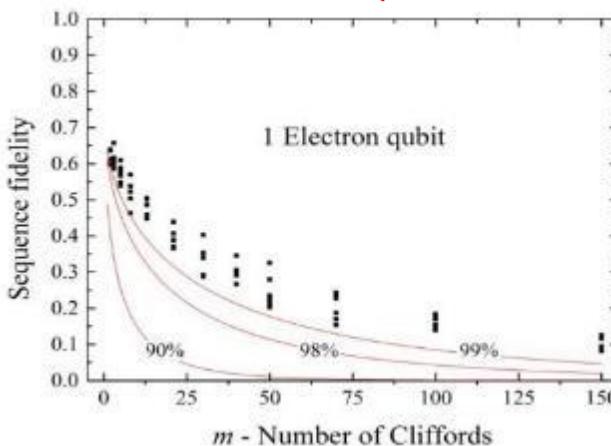
# SiMOS Dot Qubits in $^{28}\text{Si}$ : 1-Qubit Randomized Benchmarking

Gate	1 electron qubit		3 electron qubit	
	Fidelity (%)	$\sigma$	Fidelity (%)	$\sigma$
Reference	99.57	0.03	98.12	0.15
Average	99.59		98.15	
I	99.83	0.09	97.9	0.5
X	99.83	0.09	98.97	0.4
Y	99.99	0.07	99.34	0.3
X/2	99.57	0.09	98.15	0.4
-X/2	99.53	0.09	97.76	0.3
Y/2	99.67	0.09	98.71	0.5
-Y/2	99.23	0.1	96.90	0.5
$\alpha$	0.61		0.67	
$g^z - g_0$	-2.5E-3	7E-5	-8.8E-3	4E-5
$f_{\text{ESR}}$	39.141	1E-3	39.016	1E-3
$T_2^*$ (ms)	0.12		0.07	
$T_2$ (ms)	28		2.8	

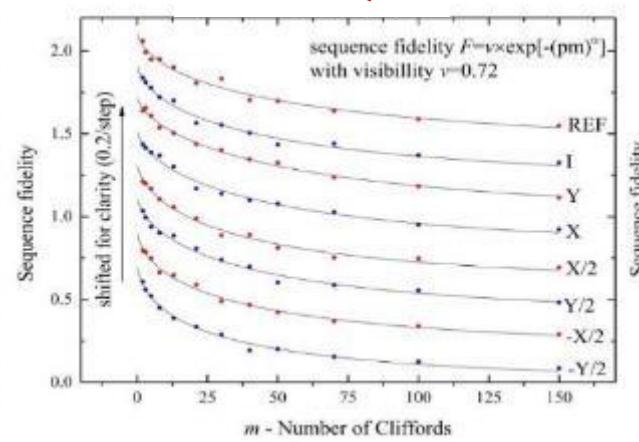
- All 1e 1-qubit gate fidelities > 99%
- Average 1e 1-qubit gate fidelity = 99.6%



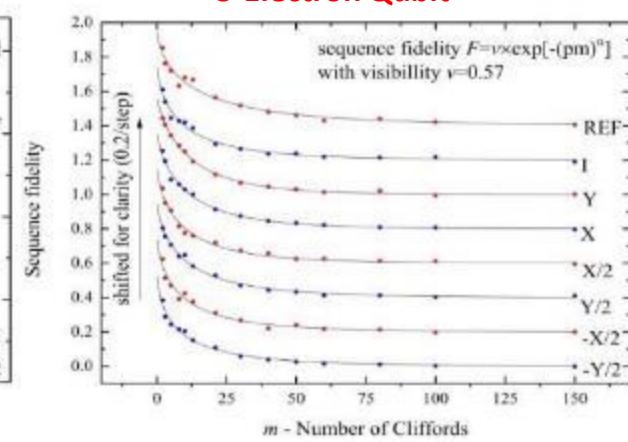
1-Electron Qubit



1-Electron Qubit



3-Electron Qubit



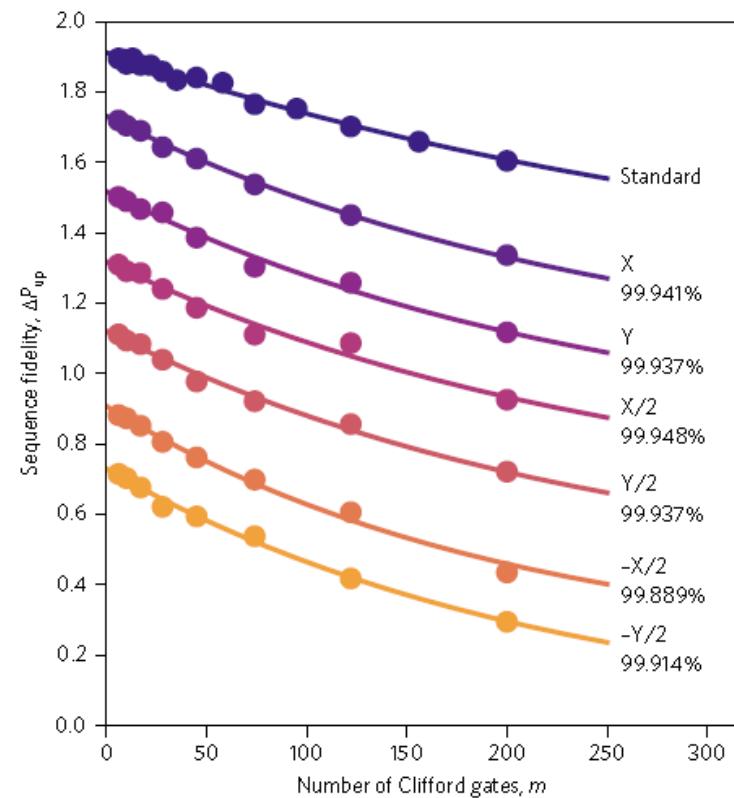
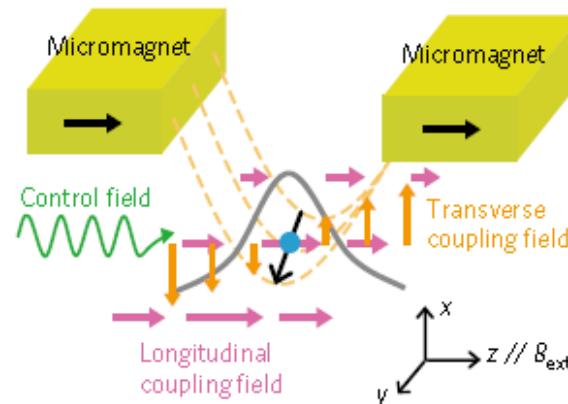
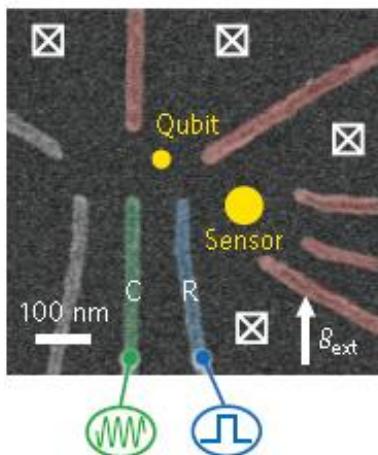
# Si/SiGe Dot Qubits in $^{28}\text{Si}$ : 1-Qubit Randomized Benchmarking

A quantum-dot spin qubit with coherence limited by charge noise and fidelity higher than 99.9%

EDSR

$^{28}\text{Si}$

Jun Yoneda <sup>1,2\*</sup>, Kenta Takeda  <sup>1,2</sup>, Tomohiro Otsuka <sup>1,2,3</sup>, Takashi Nakajima <sup>1,2</sup>, Matthieu R. Delbecq <sup>1,2</sup>, Giles Allison <sup>1</sup>, Takumu Honda <sup>4</sup>, Tetsuo Kodera <sup>4</sup>, Shunri Oda  <sup>4</sup>, Yusuke Hoshi <sup>5</sup>, Noritaka Usami <sup>6</sup>, Kohei M. Itoh <sup>7</sup> and Seigo Tarucha <sup>1,2\*</sup>



# 1-Qubit Gate: GRAPE Optimized Pulses, $F_{1Q} = 99.96\%$

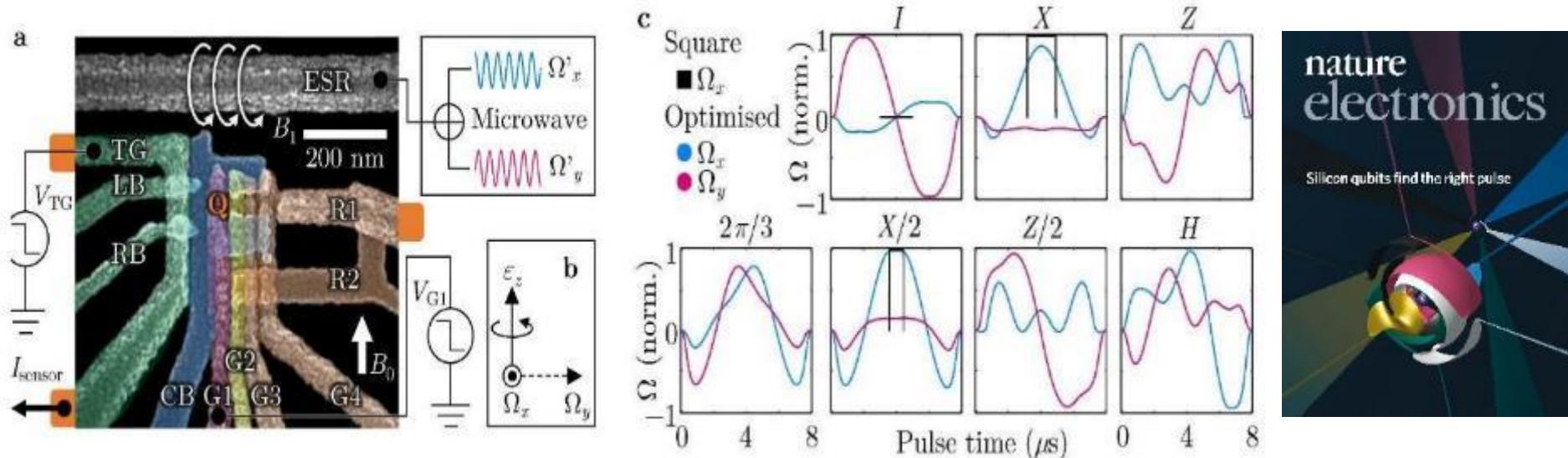
nature  
electronics

ARTICLES

<https://doi.org/10.1038/s41928-019-0234-1>

## Silicon qubit fidelities approaching incoherent noise limits via pulse engineering

C. H. Yang<sup>1\*</sup>, K. W. Chan<sup>1</sup>, R. Harper<sup>2</sup>, W. Huang<sup>1</sup>, T. Evans<sup>1,2</sup>, J. C. C. Hwang<sup>1</sup>, B. Hensen<sup>1</sup>, A. Laucht<sup>1</sup>, T. Tanttu<sup>1</sup>, F. E. Hudson<sup>1</sup>, S. T. Flammia<sup>2</sup>, K. M. Itoh<sup>3</sup>, A. Morello<sup>1</sup>, S. D. Bartlett<sup>2\*</sup> and A. S. Dzurak<sup>1\*</sup>



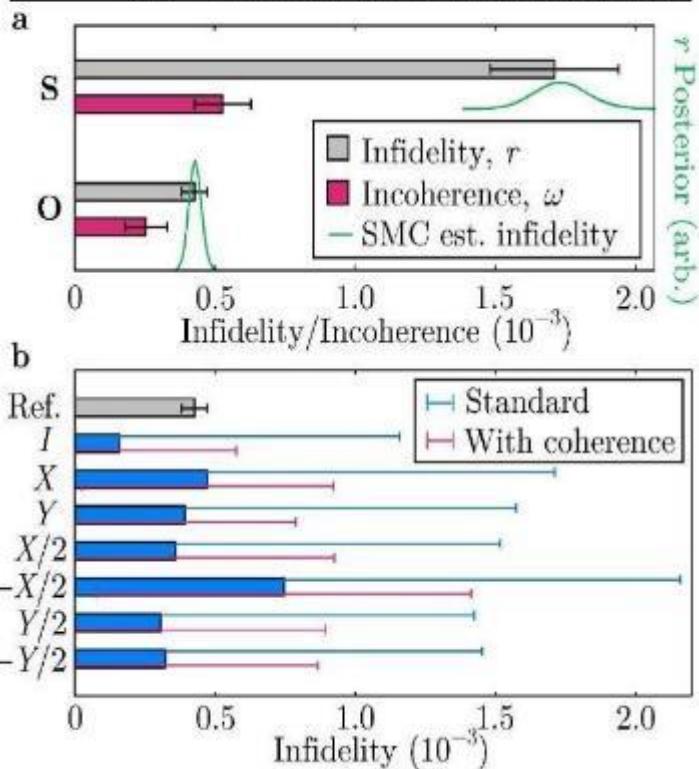
THE UNIVERSITY OF  
SYDNEY

Yang *et al.*, *Nature Electron.* **2**, 151 (2019)



# Silicon-MOS Q-Dot 1-Qubit Gate: $F_{1Q} = 99.96\%$

RB schemes	N – Square pulse with <i>no</i> calibrations
S	Square pulse with calibrations
O	Optimised pulse with calibrations



QUANTUM COMPUTING

## Designer pulses for better qubit gate operations

Error rates in silicon qubits can be improved by benchmarking and optimizing pulse engineering techniques.

Joseph Emerson

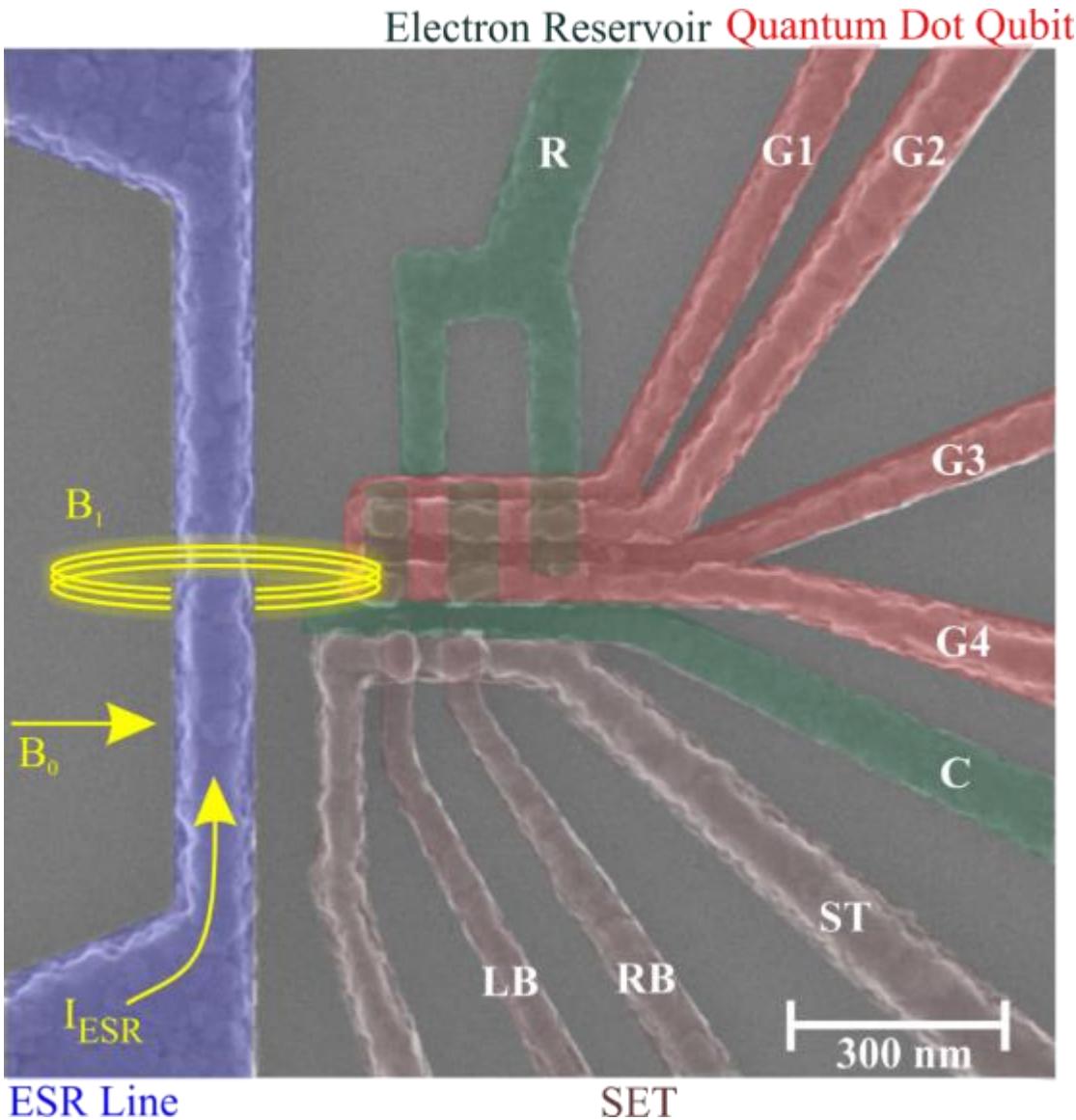


THE UNIVERSITY OF  
SYDNEY

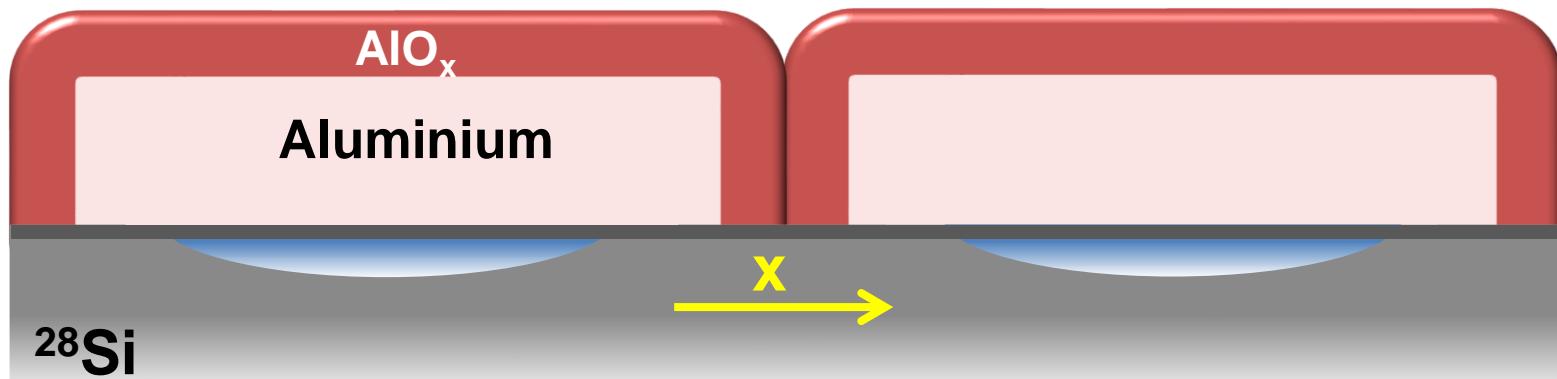
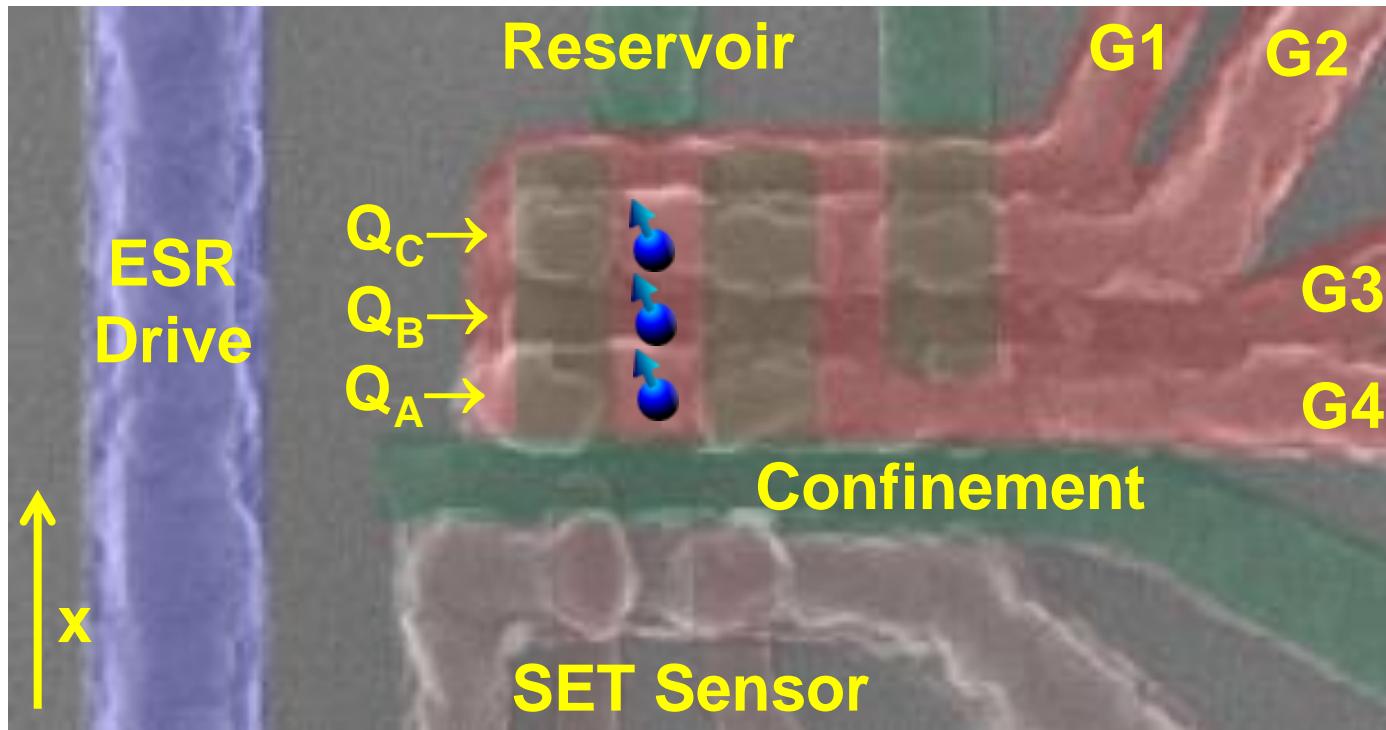
Yang *et al.*, *Nature Electron.* **2**, 151 (2019)

UNSW  
SYDNEY

# $^{28}\text{Si}$ -MOS Dots: *Multi-Qubit Devices*

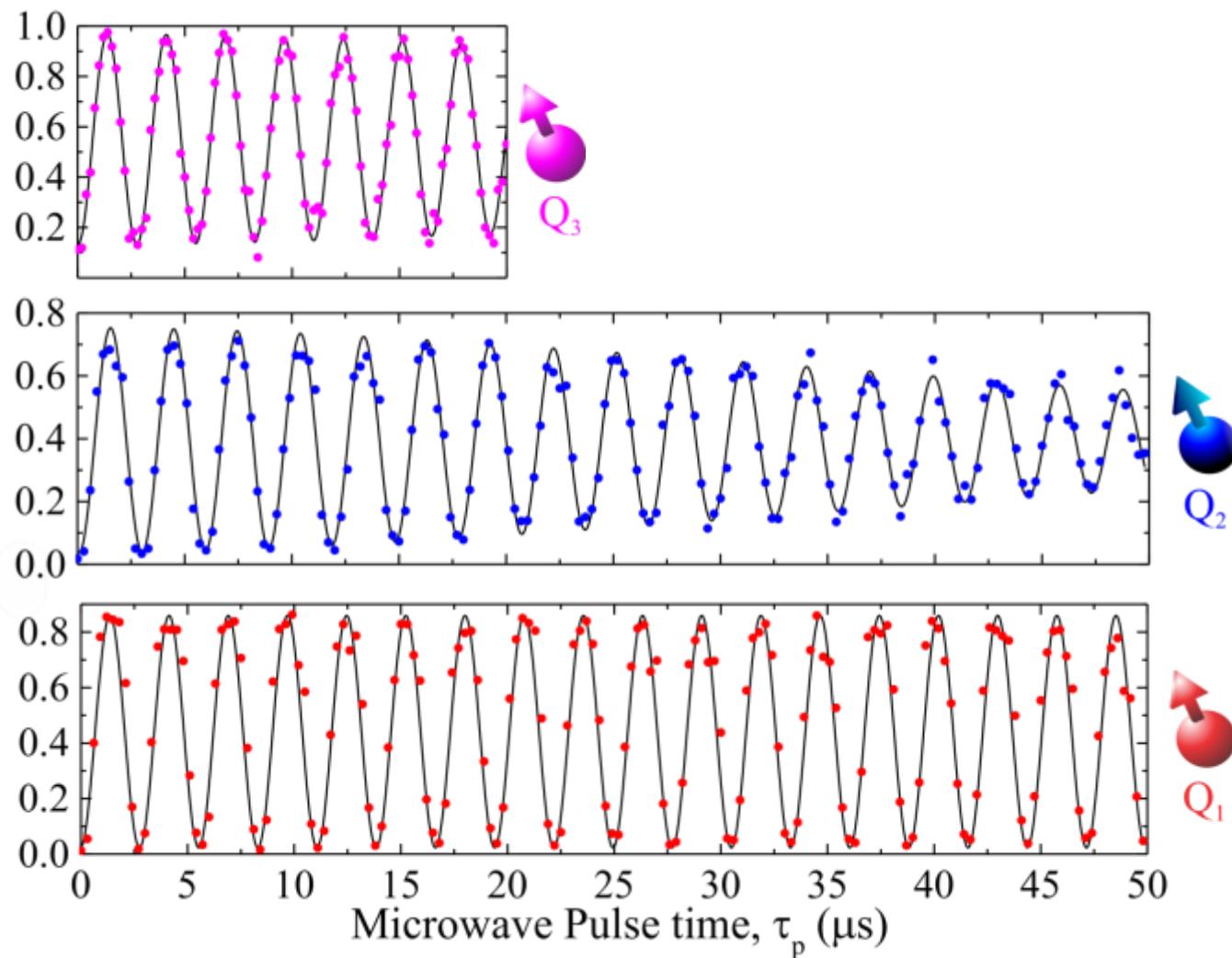
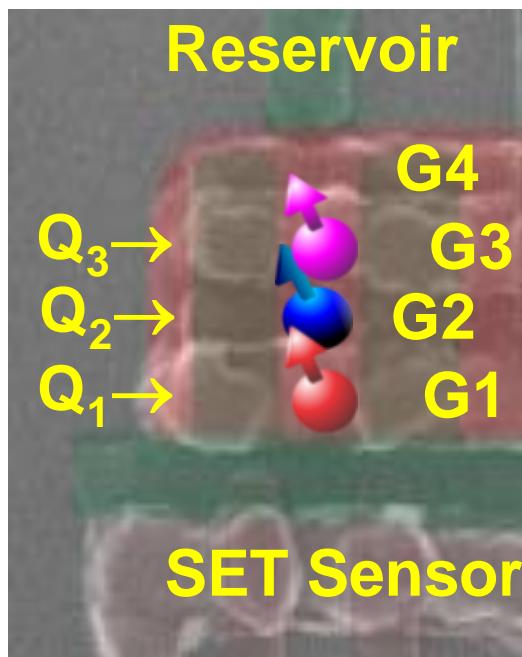


# $^{28}\text{Si}$ -MOS Dots: *Multi-Qubit Devices*



**1 Gate  $\Rightarrow$  1 Qubit**

# $^{28}\text{Si}$ -MOS Dots: *Operation of 3 Independent Qubits*



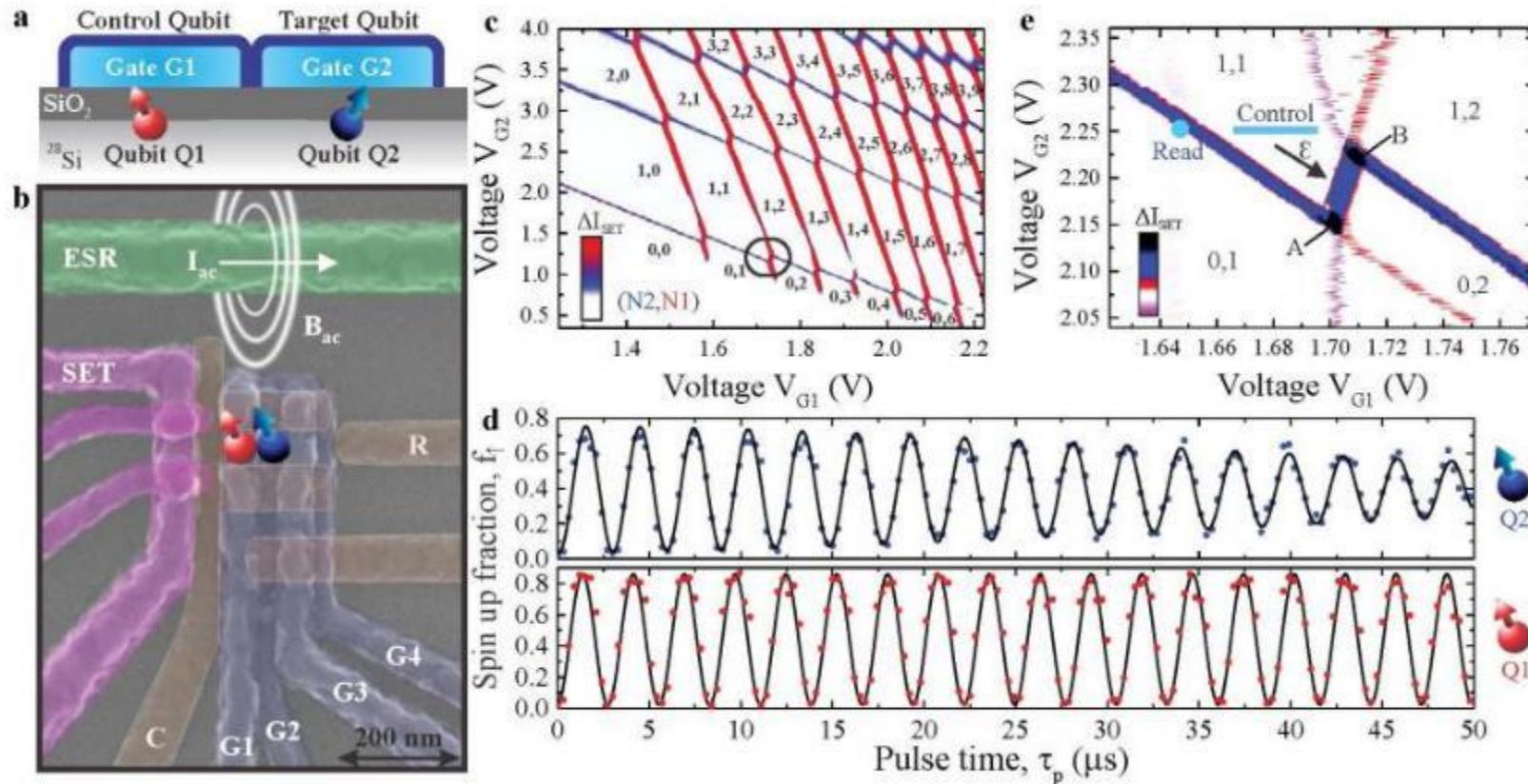
# CMOS Qubits: *First 2-Qubit Logic in Silicon*

## LETTER

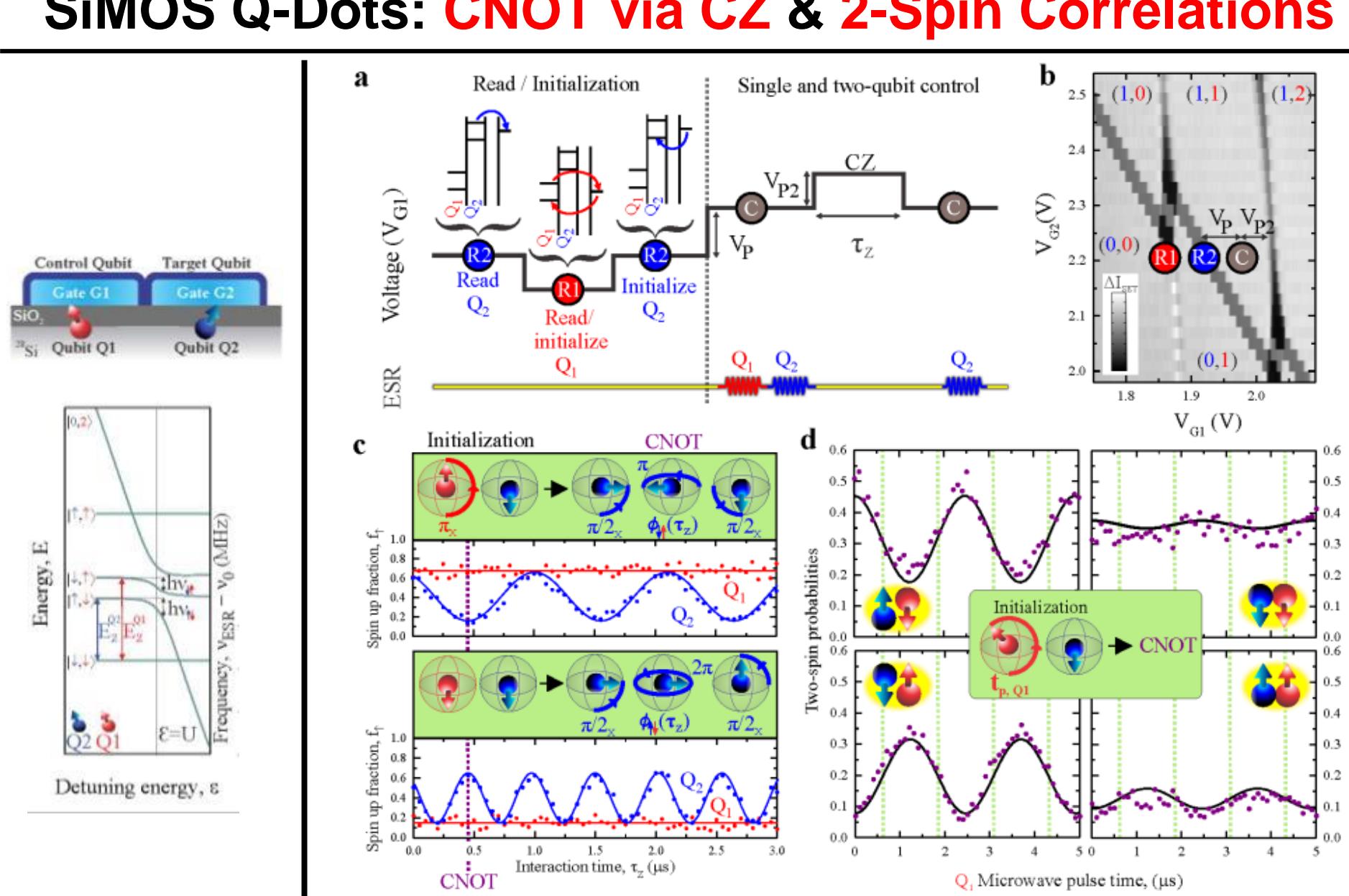
doi:10.1038/nature15263

### A two-qubit logic gate in silicon

M. Veldhorst<sup>1</sup>, C. H. Yang<sup>1</sup>, J. C. C. Hwang<sup>1</sup>, W. Huang<sup>1</sup>, J. P. Dehollain<sup>1</sup>, J. T. Muhonen<sup>1</sup>, S. Simmons<sup>1</sup>, A. Laucht<sup>1</sup>, F. E. Hudson<sup>1</sup>, K. M. Itoh<sup>2</sup>, A. Morello<sup>1</sup> & A. S. Dzurak<sup>1</sup>



# SiMOS Q-Dots: CNOT via CZ & 2-Spin Correlations

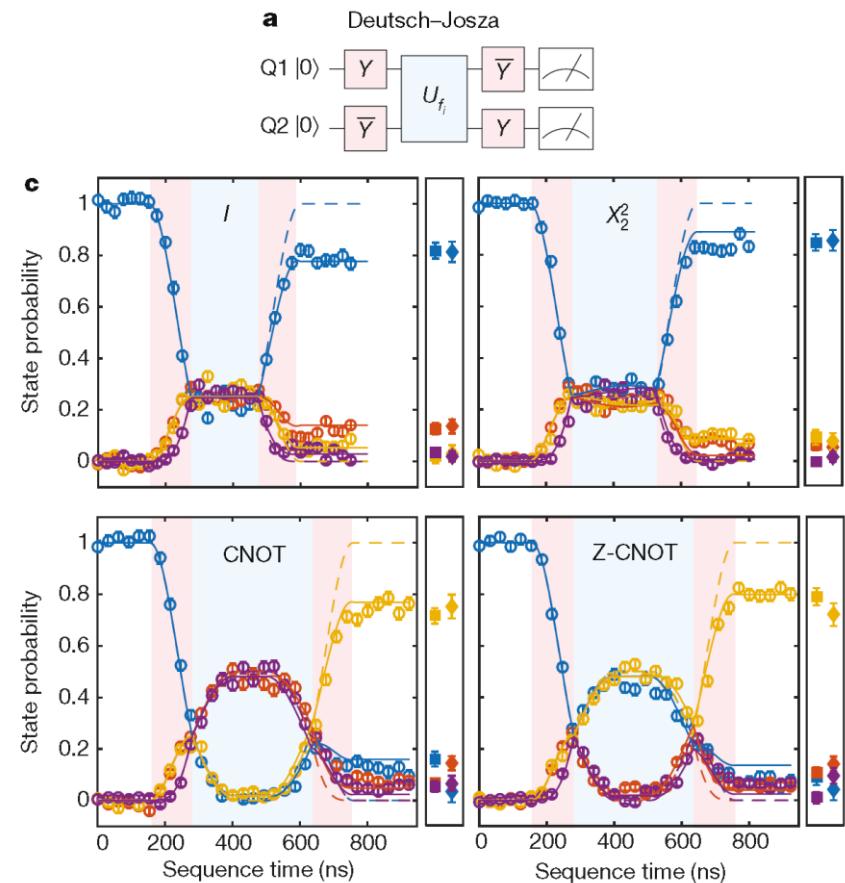
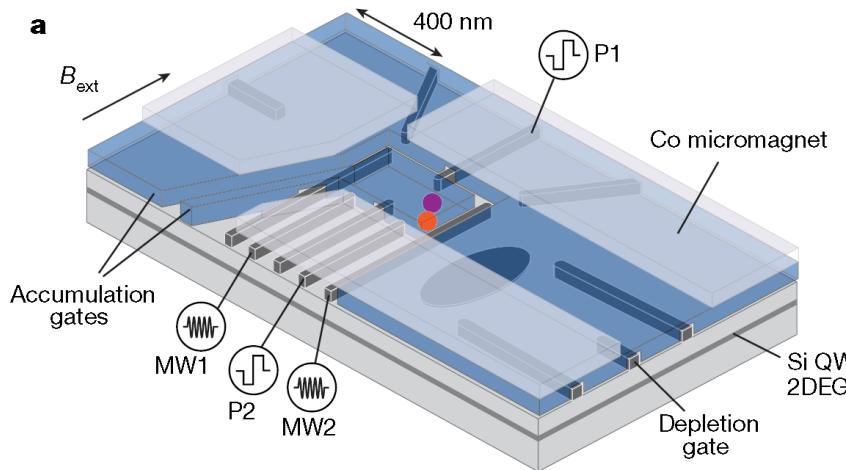


# 2-Qubit Algorithms: Si/SiGe Double-QD

## A programmable two-qubit quantum processor in silicon

T. F. Watson<sup>1</sup>, S. G. J. Philips<sup>1</sup>, E. Kawakami<sup>1</sup>, D. R. Ward<sup>2</sup>, P. Scarlino<sup>1</sup>, M. Veldhorst<sup>1</sup>, D. E. Savage<sup>2</sup>, M. G. Lagally<sup>2</sup>, Mark Friesen<sup>2</sup>, S. N. Coppersmith<sup>2</sup>, M. A. Eriksson<sup>2</sup> & L. M. K. Vandersypen<sup>1</sup>

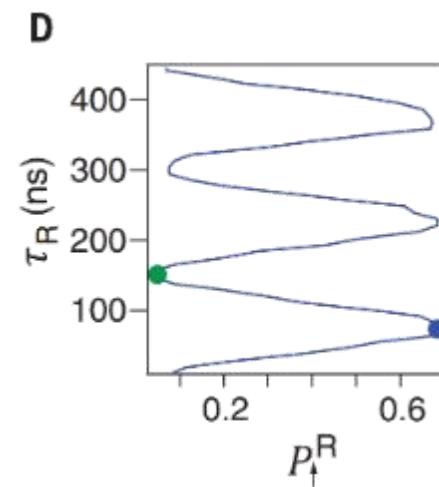
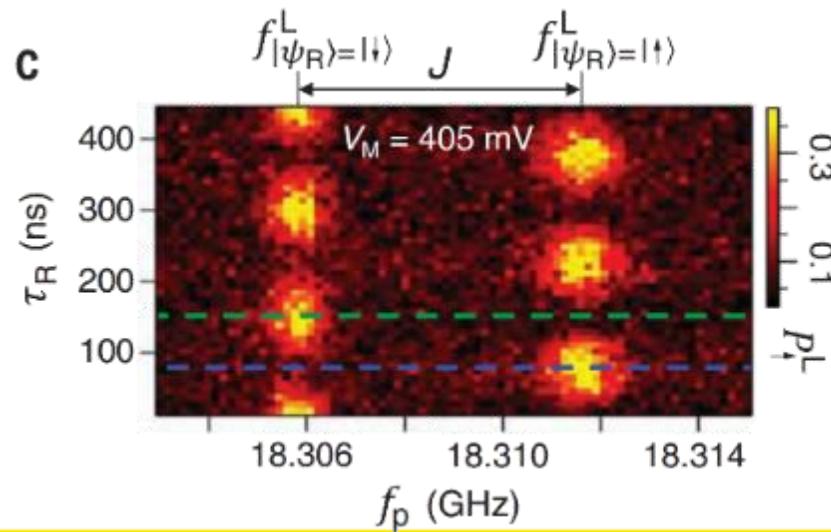
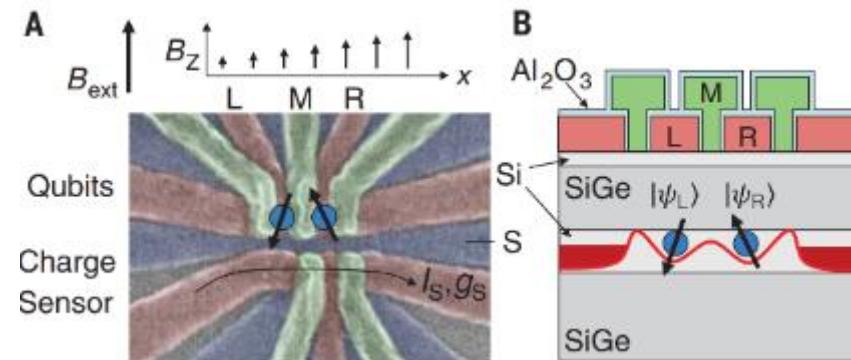
EDSR -  $\mu$ Magnet      <sup>nat</sup>Si  
Bell State F = 89%



# Si/SiGe two-qubit gates: Resonant CNOT

Petta group (Princeton) – Si/SiGe QDs:

- Resonantly driven rather than waiting for phase accumulation
- Exchange gate to manipulate exchange coupling
- Measured Bell states ( $F = 78\%$ ) and demonstrated entanglement



# <sup>28</sup>SiMOS QDs: Two-Qubit Fidelity via RB

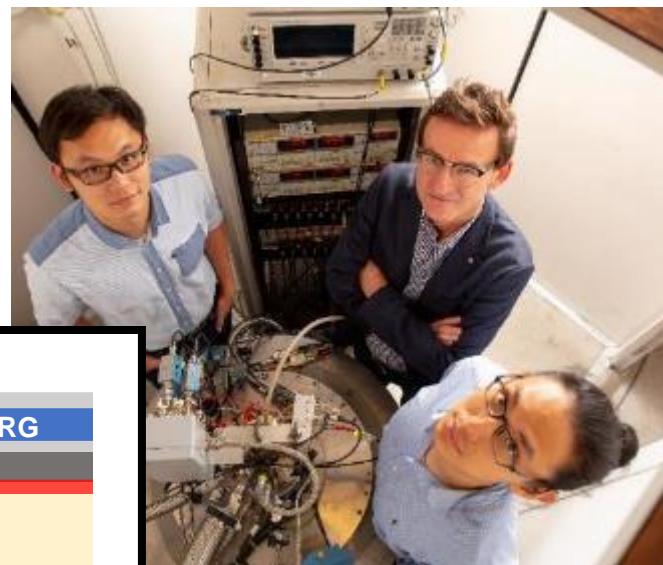
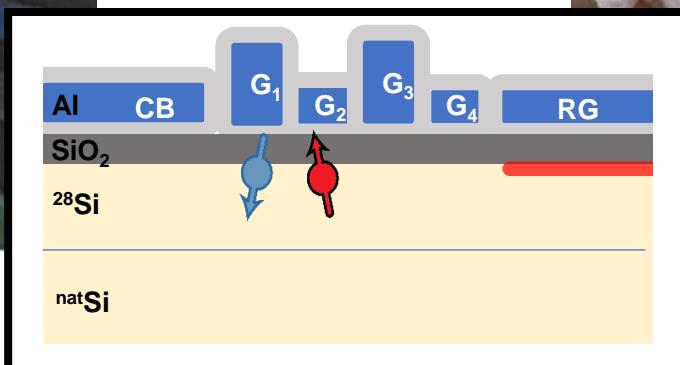
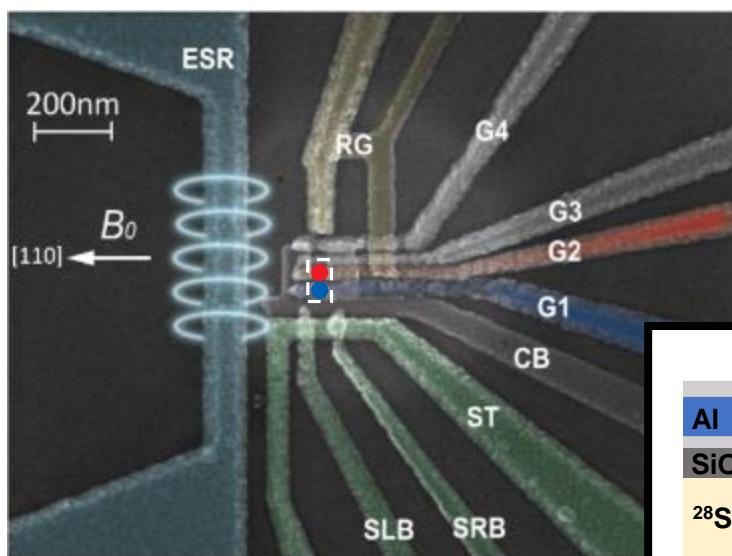
LETTER

nature  
International journal of science

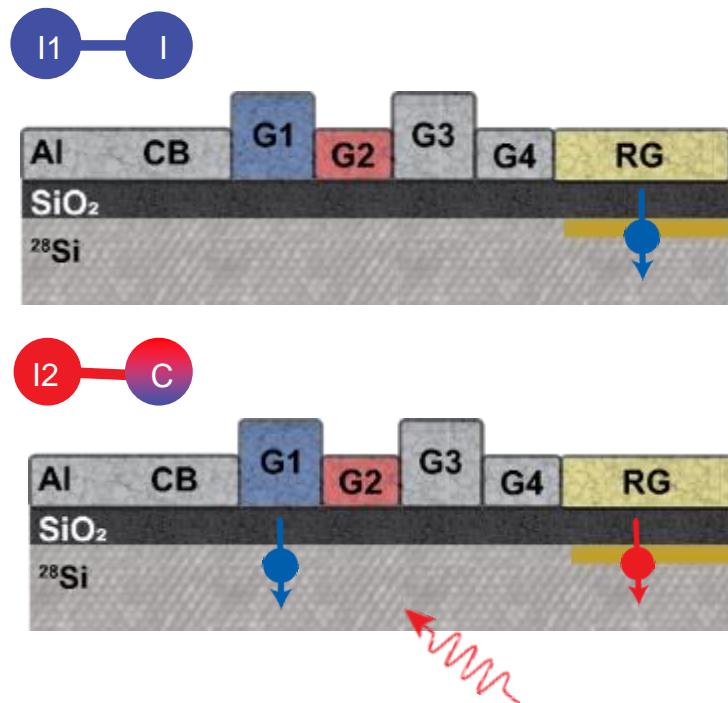
<https://doi.org/10.1038/s41586-019-1197-0>

## Fidelity benchmarks for two-qubit gates in silicon

W. Huang<sup>1\*</sup>, C. H. Yang<sup>1</sup>, K. W. Chan<sup>1</sup>, T. Tanttu<sup>1</sup>, B. Hensen<sup>1</sup>, R. C. C. Leon<sup>1</sup>, M. A. Fogarty<sup>1,2</sup>, J. C. C. Hwang<sup>1</sup>, F. E. Hudson<sup>1</sup>, K. M. Itoh<sup>3</sup>, A. Morello<sup>1</sup>, A. Laucht<sup>1</sup> & A. S. Dzurak<sup>1,\*</sup>



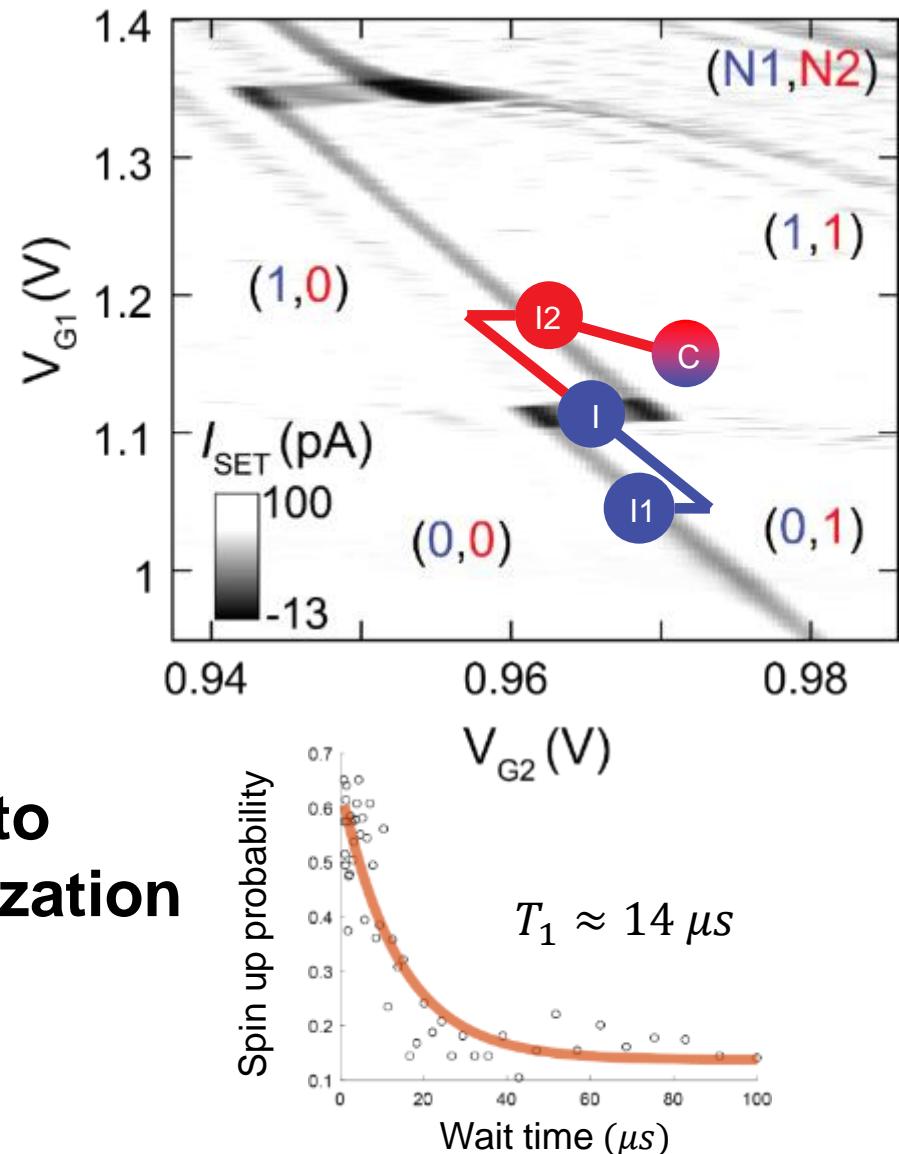
# Initialization process for two qubits



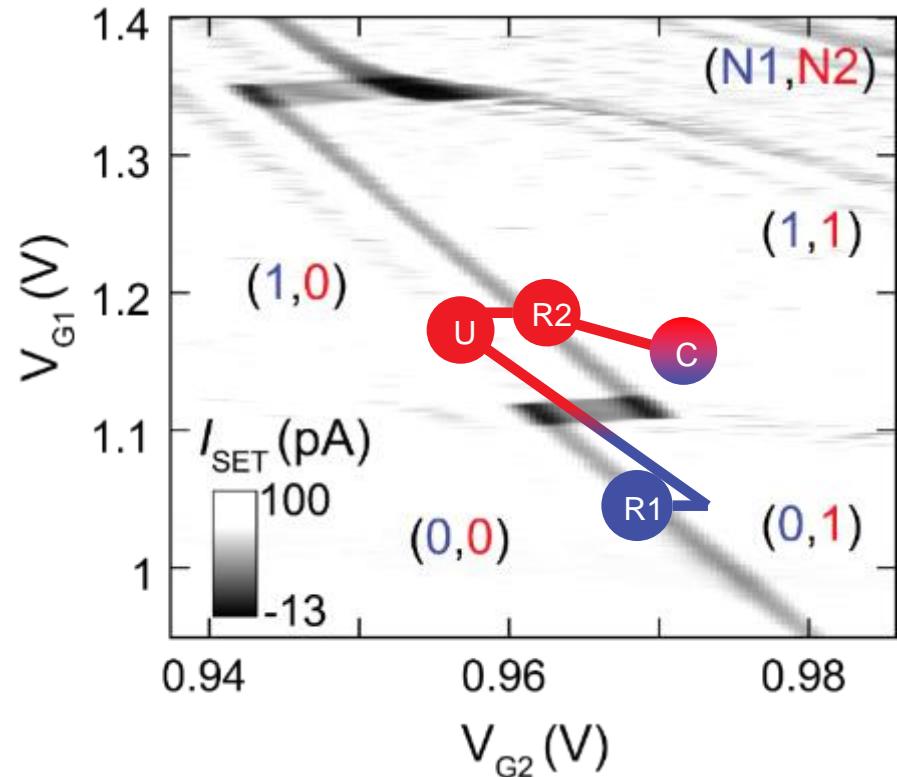
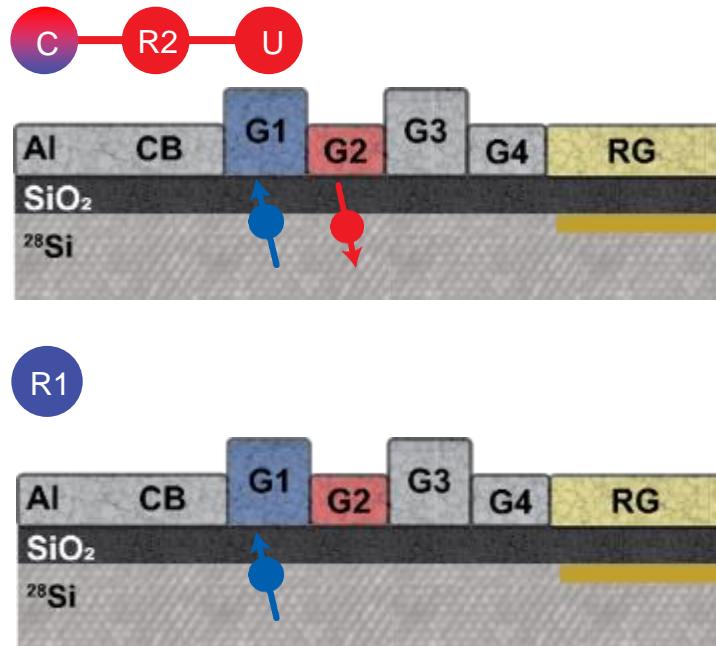
- Hot-spot relaxation close to **(0,1)-(1,0)** improves initialization fidelity of Q1

Watson et al., Nature 555, 633 (2018)

Zhao et al., arXiv:1707.05217

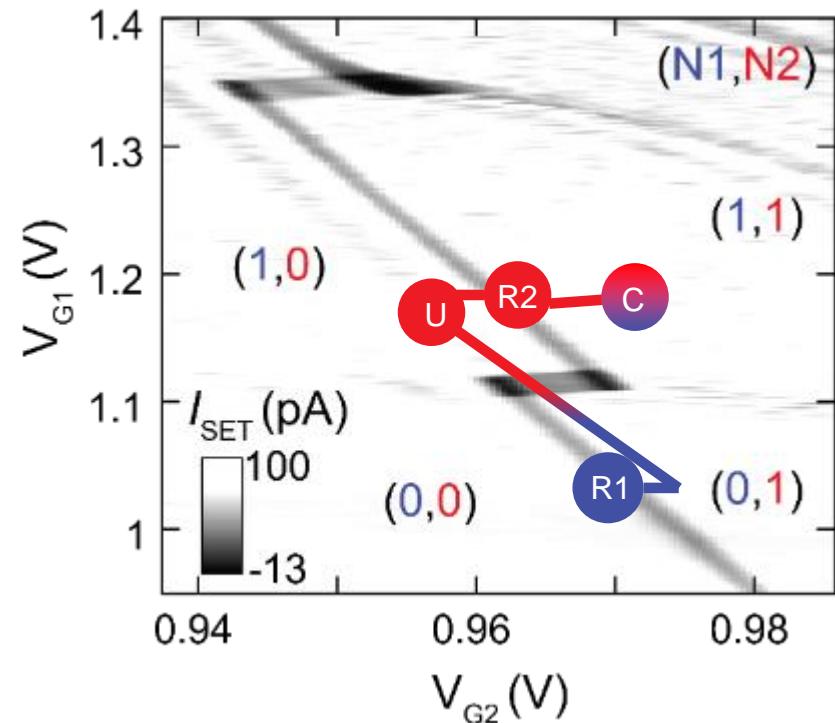
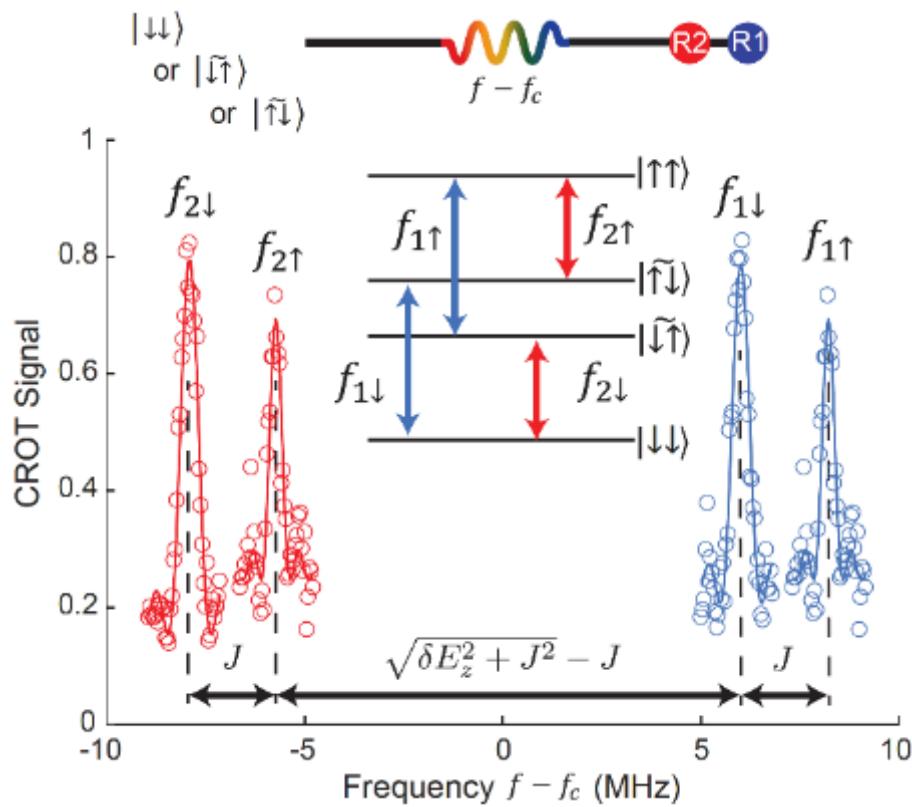


# Readout process for two qubits



- Shutting Q1 from D1 to D2 is advantageous over reading out Q1 at the (1,0)-(0,0) transition directly, due to the slow tunneling rate from D1 to the reservoir.

# Conditional control

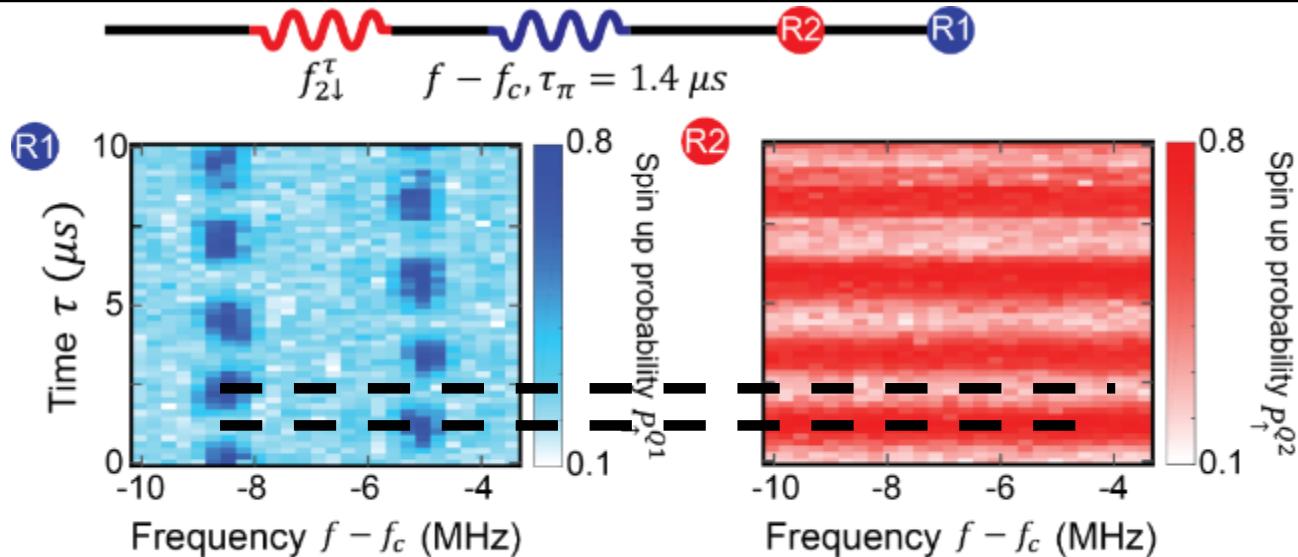


- Lines clearly split by  $\delta E_z$  and  $J$  → conditional and selective addressing

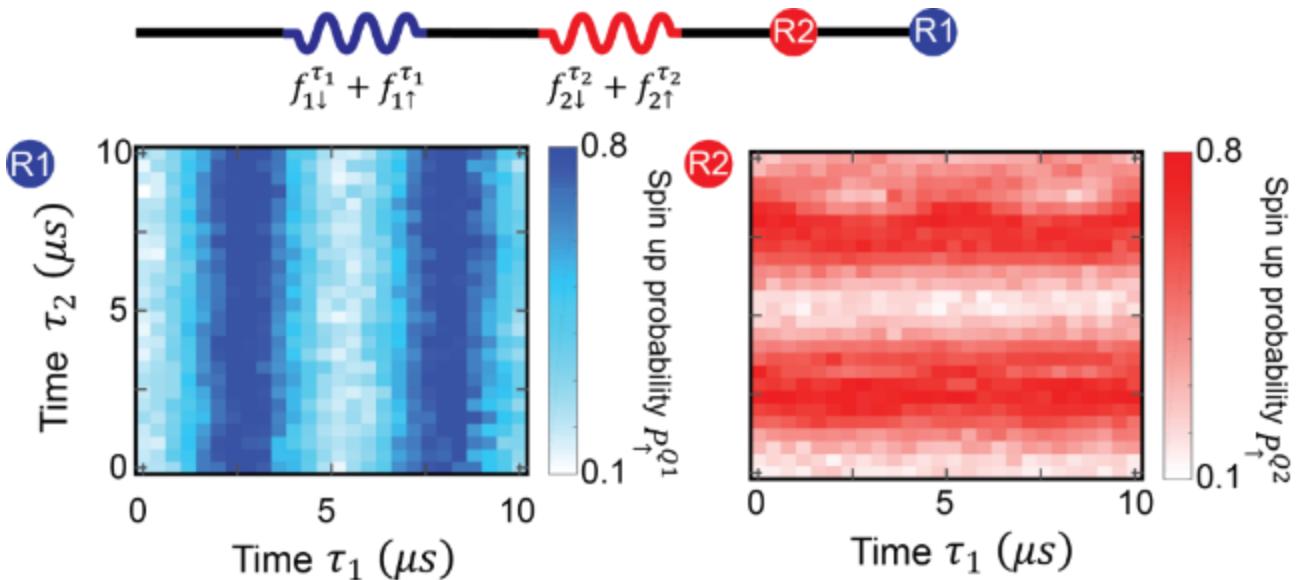
$\overline{E_z}$	39.17 GHz
$\delta E_z$	13.72 MHz
$J$	2.51 MHz
$\Omega_R$	0.35-0.55 MHz

# Conditional rotations

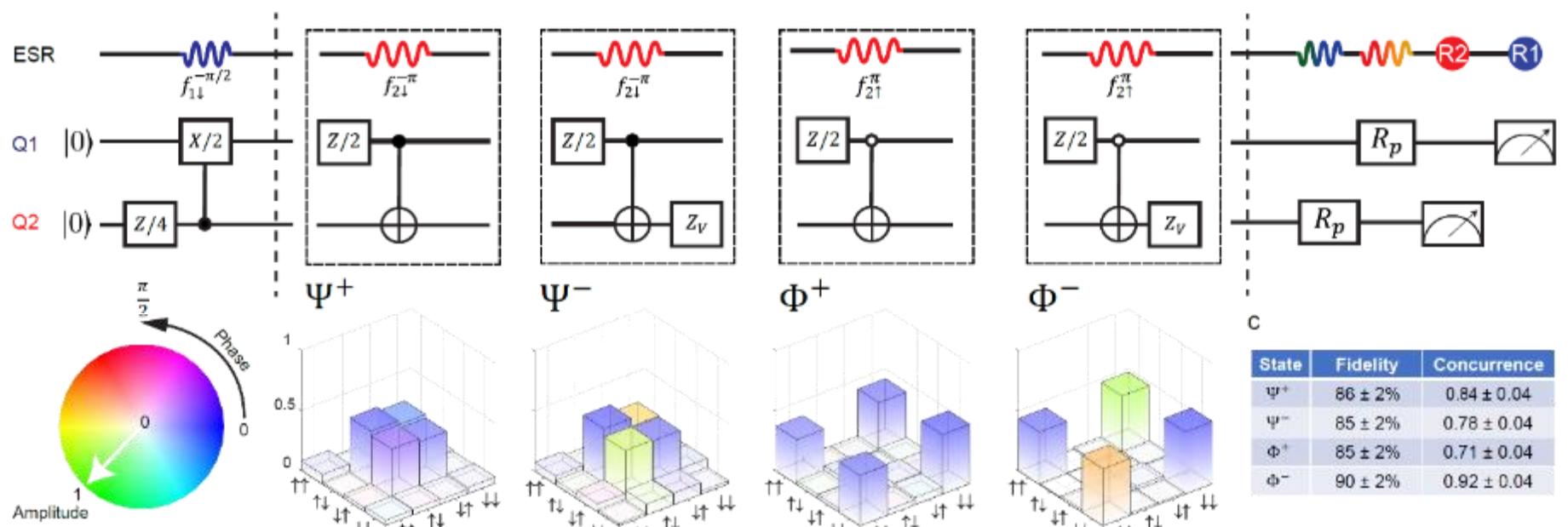
- Every MW pulse is a conditional qubit gate



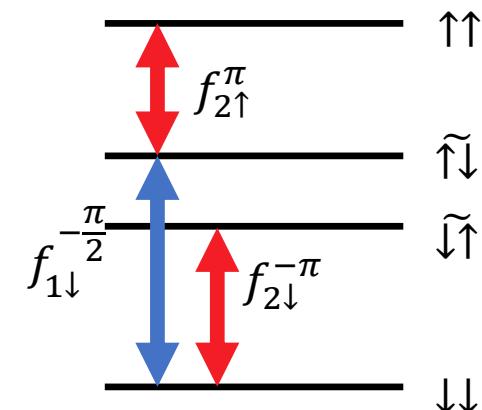
- Single qubit gates require 2 MW pulses (either serial or parallel)



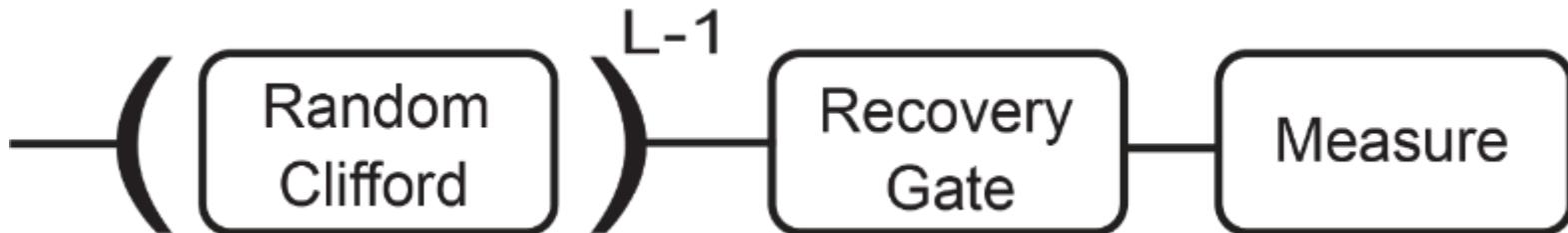
# Bell state tomography



- Projected to  $X, -X, Y, Z$  axis
- 8960 single shots
- Corrected for readout error

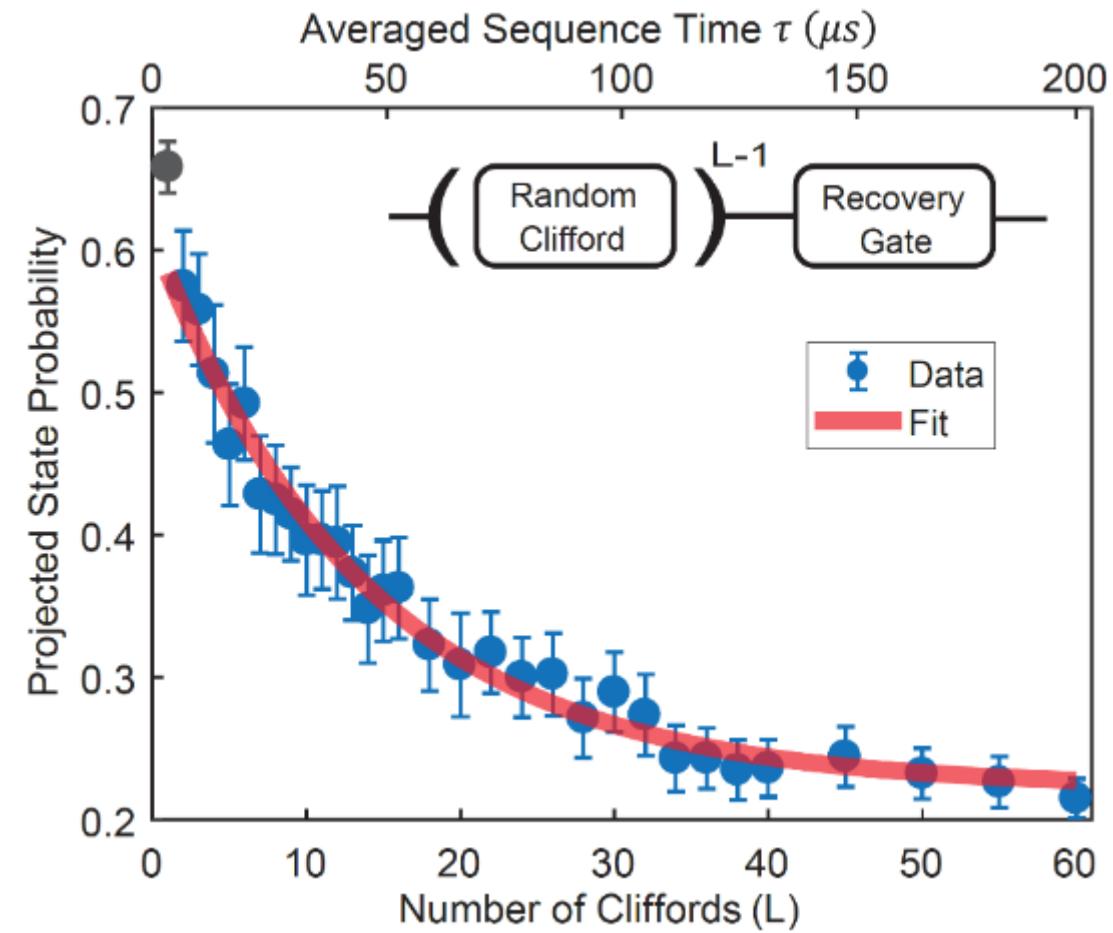


# Two-qubit randomized benchmarking



- Single qubit : 24 Clifford gates
- Two qubit : 11520 Clifford gates
- Project to  $|\uparrow\uparrow\rangle$  final state.
- $P = A \left( \frac{1}{4} + \frac{4}{3} F_{Clifford} \right)^L + B$
- SPAM errors absorbed by parameters  $A$  and  $B$

# 2-Qubit Randomized Benchmarking in Silicon



- 51 random Clifford sequences per  $L$  data point

$$P = A \left( \frac{1}{4} + \frac{4}{3} F_{\text{Clifford}} \right)^L + B$$

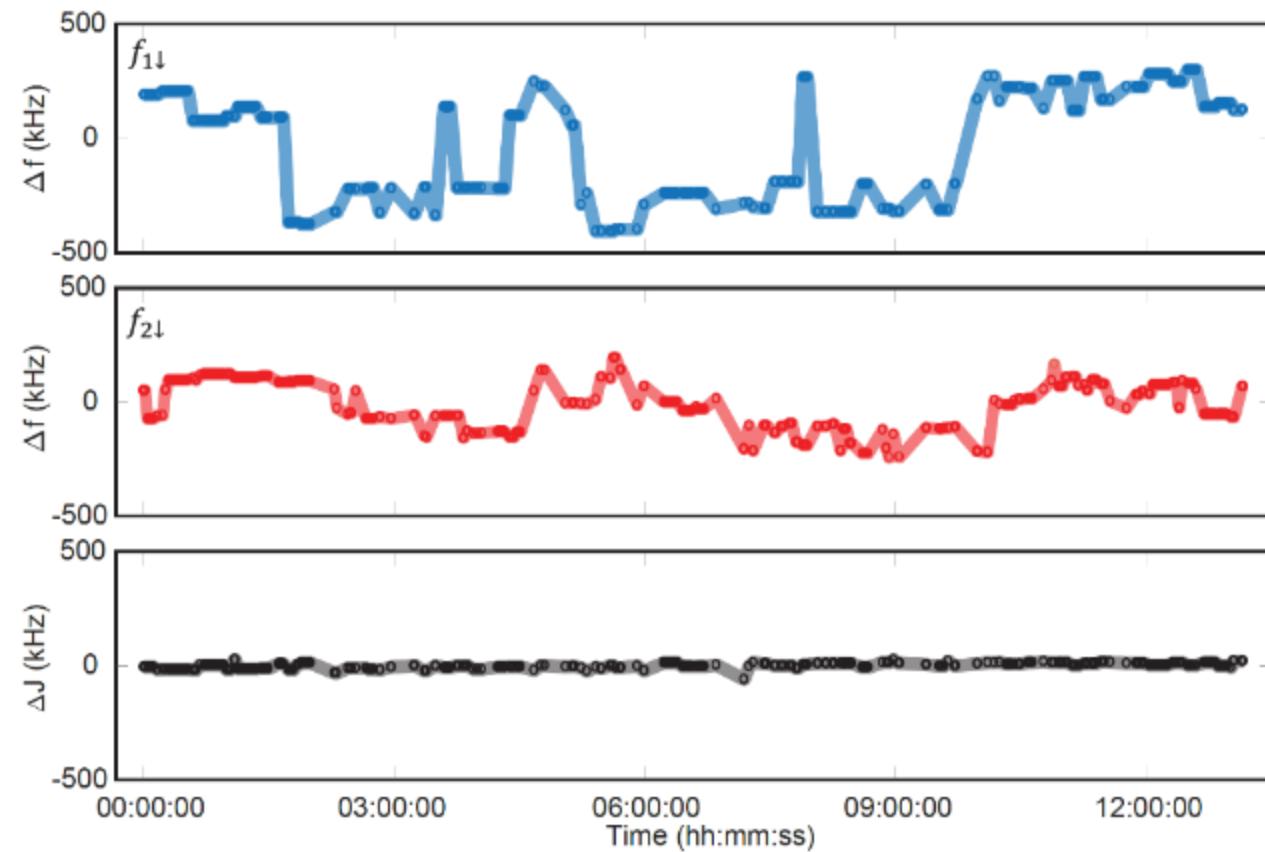
- Gate fidelities:

$$F_{\text{Clifford}} = 94.7 \pm 0.8 \%$$

$$F_{\text{primitive}} = 98.0 \pm 0.3 \%$$

$$F_{\pi/2}^{\text{cond}} = 99.0 \pm 0.2 \%$$

# ESR Frequency Tracking: *12 hours*



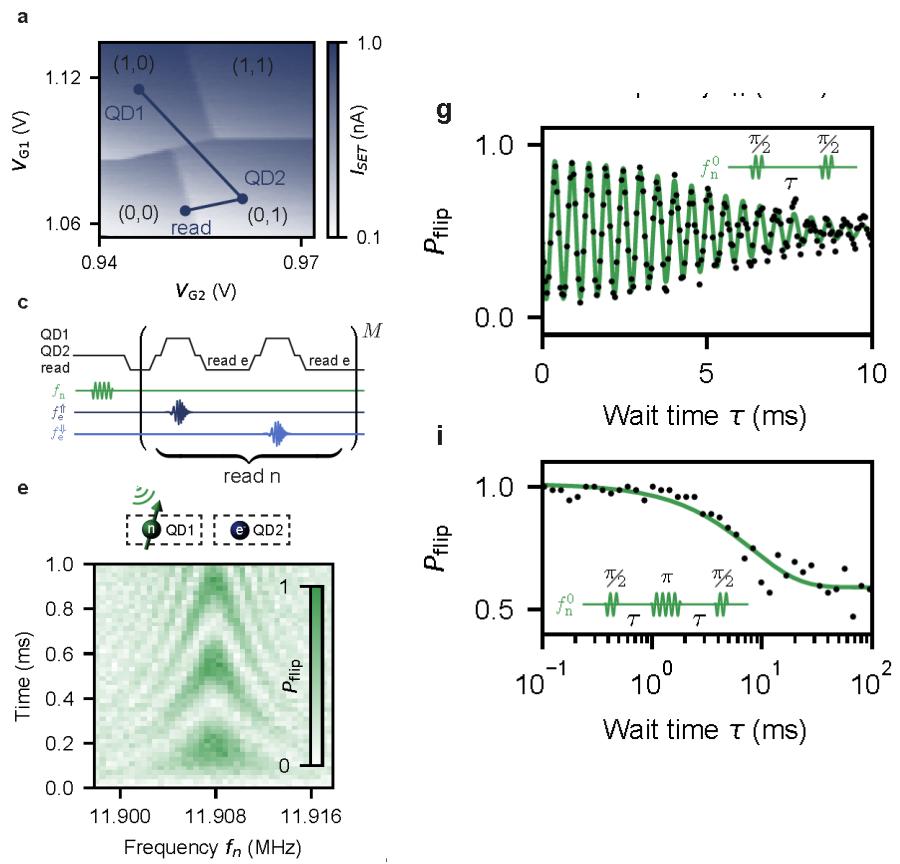
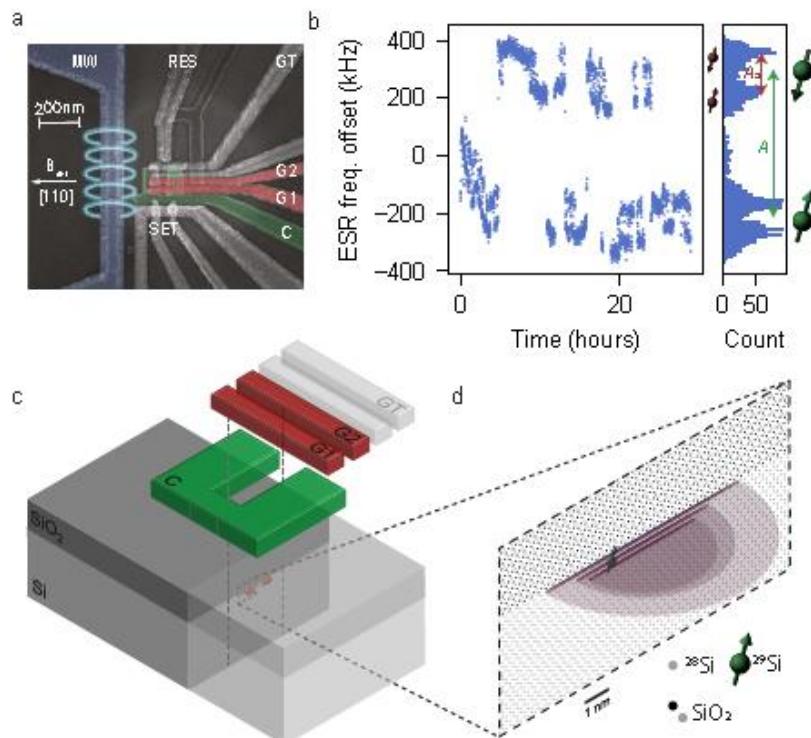
- Frequency jumps of up to 600 kHz for Q1
- Much smaller jumps for Q2
- Q1 and Q2 jumps are uncorrelated → No  $B_0$  or MW source noise
- $J$  very stable + SET charge sensor stable → Not charge noise

→ Residual  $^{29}\text{Si}$  nuclei couple locally to the qubits

# $^{29}\text{Si}$ Nuclear Spin Qubit: *Possible Quantum Memory*

## A silicon quantum-dot-coupled nuclear spin qubit

Bas Hensen<sup>\*,1</sup>, Wister Huang<sup>\*,1</sup>, Chih-Hwan Yang<sup>1</sup>, Kok Wai Chan<sup>1</sup>, Jun Yoneda<sup>1</sup>, Tuomo Tanttu<sup>1</sup>, Fay E. Hudson<sup>1</sup>, Arne Laucht<sup>1</sup>, Kohei M. Itoh<sup>2</sup>, Andrea Morello<sup>1</sup>, Andrew S. Dzurak<sup>1</sup>



# *Scalability* of Silicon Spin Qubits

# Medium-term Vision: Error-corrected 1D Architecture



PHYSICAL REVIEW X 8, 021058 (2018)

UNSW

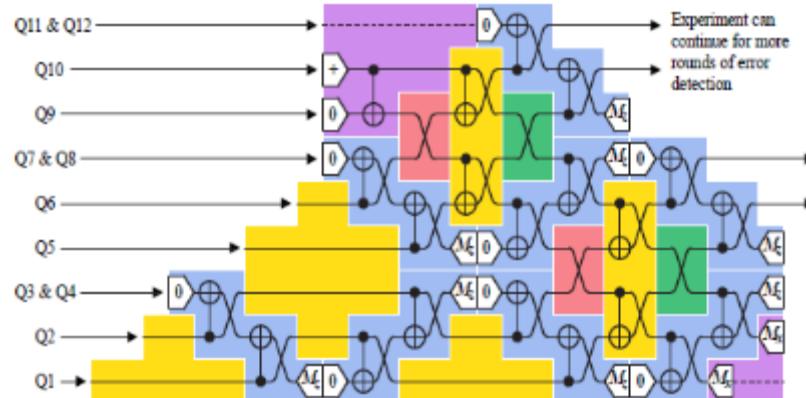
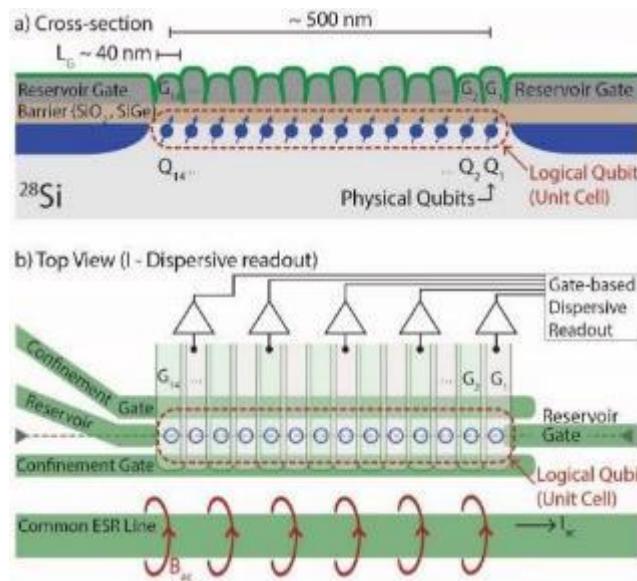


## Logical Qubit in a Linear Array of Semiconductor Quantum Dots

Cody Jones,<sup>1,\*</sup> Michael A. Fogarty,<sup>2</sup> Andrea Morello,<sup>2</sup> Mark F. Gyure,<sup>1</sup> Andrew S. Dzurak,<sup>2</sup> and Thaddeus D. Ladd<sup>1</sup>

<sup>1</sup>HRL Laboratories, LLC, 3011 Malibu Canyon Road, Malibu, California 90265, USA

<sup>2</sup>Centre for Quantum Computation and Communication Technology, School of Electrical Engineering and Telecommunications, The University of New South Wales, Sydney, New South Wales 2052, Australia



# Key Components for *Parity Readout* in SiMOS QDs



Article | OPEN | Published: 30 October 2018

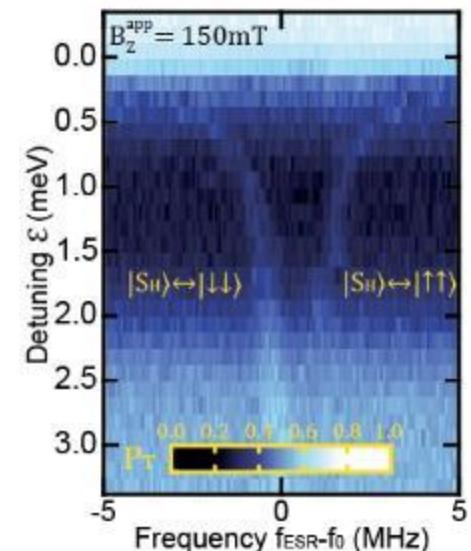
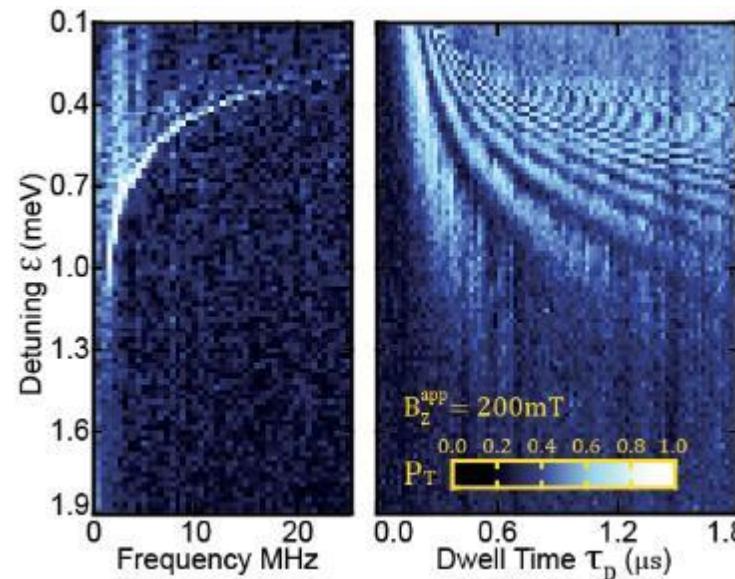
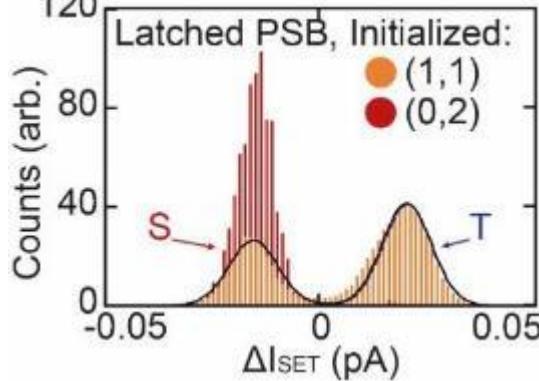
Integrated silicon qubit platform with single-spin addressability, exchange control and single-shot singlet-triplet readout

## Requirements achieved:

- ✓ Single-shot spin-blockade readout
- ✓ Coherent control of spin-pairs in singlet-triplet basis
- ✓ Individual ESR spin addressability

M. A. Fogarty , K. W. Chan, B. Hensen , W. Huang, T. Tanttu, C. H. Yang, A. Laucht, M. Veldhorst, F. E. Hudson, K. M. Itoh, D. Culcer, T. D. Ladd, A. Morello & A. S. Dzurak

Nature Communications 9, Article number: 4370 (2018) | D



# Fault-tolerant Scalability: Surface Codes

PHYSICAL REVIEW A 86, 032324 (2012)

## Surface codes: Towards practical large-scale quantum computation

Austin G. Fowler

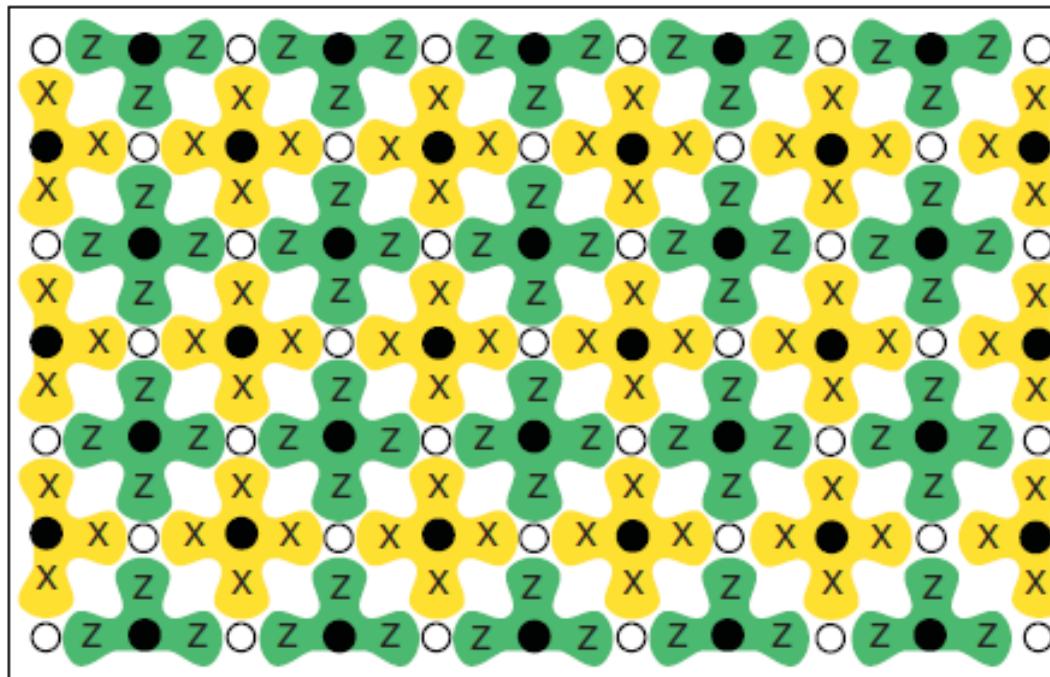
*Centre for Quantum Computation and Communication Technology, School of Physics, The University of Melbourne, Victoria 3010, Australia*

Matteo Mariantoni, John M. Martinis, and Andrew N. Cleland

*Department of Physics, University of California, Santa Barbara, California 93106-9530, USA*

*and California Nanosystems Institute, University of California, Santa Barbara, California 93106-9530, USA*

(Received 2 August 2012; published 18 September 2012)



# QD Qubits: Scalability Concepts

ARTICLE

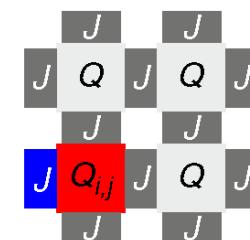
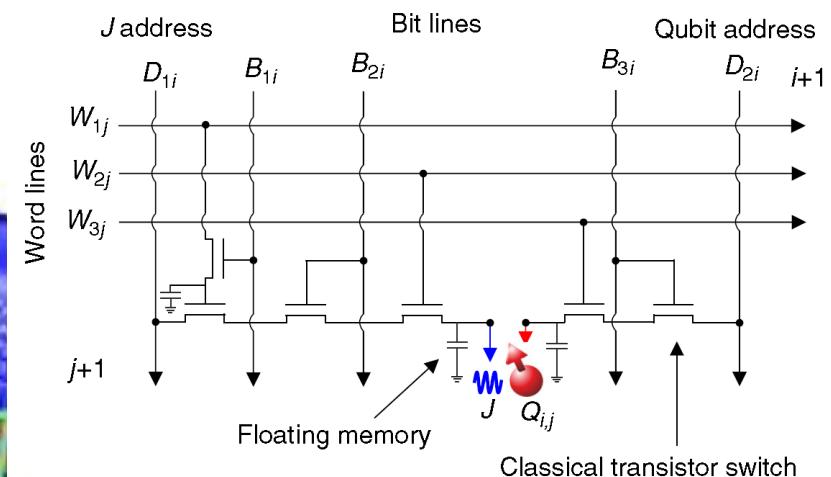
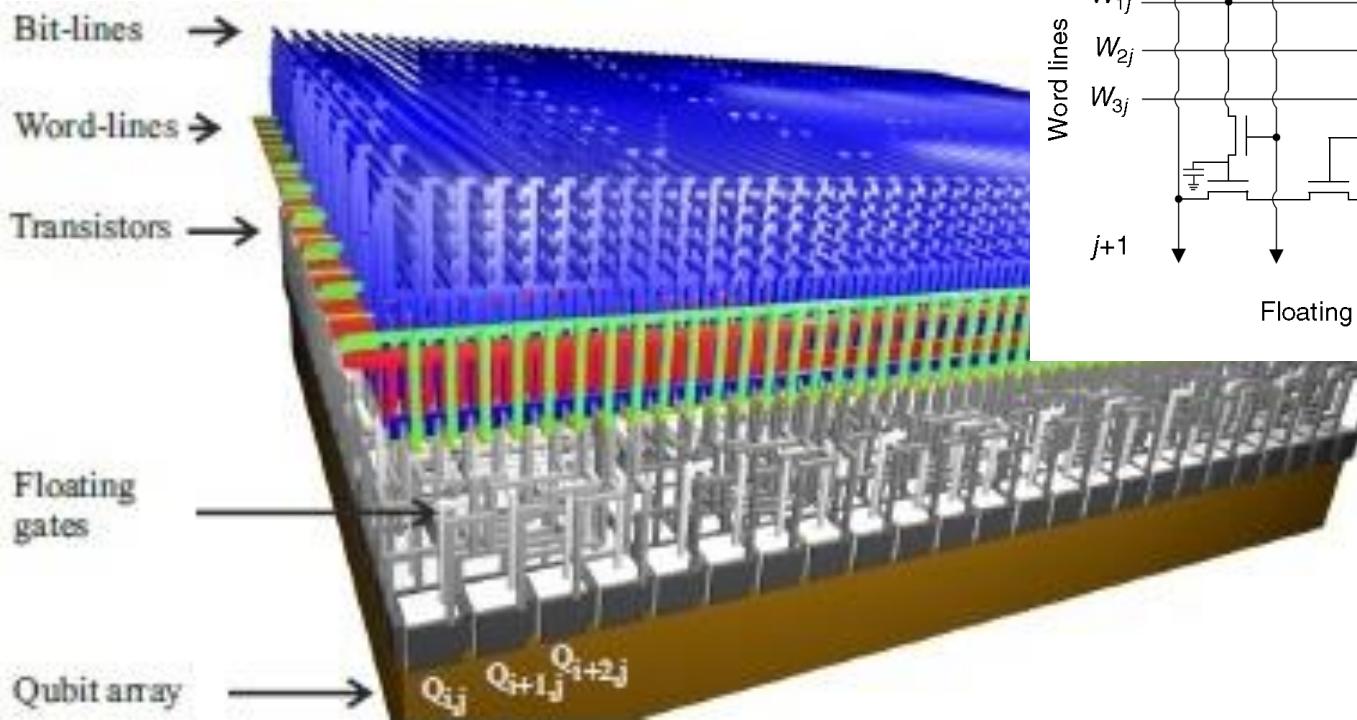
DOI: 10.1038/s41467-017-01905-6

OPEN



## Silicon CMOS architecture for a spin-based quantum computer

M. Veldhorst<sup>1,2</sup>, H.G.J. Eenink<sup>1,2</sup>, C.H. Yang<sup>2</sup> & A.S. Dzurak<sup>2</sup>



# Industrial Manufacture: *Uniformity, Integration*

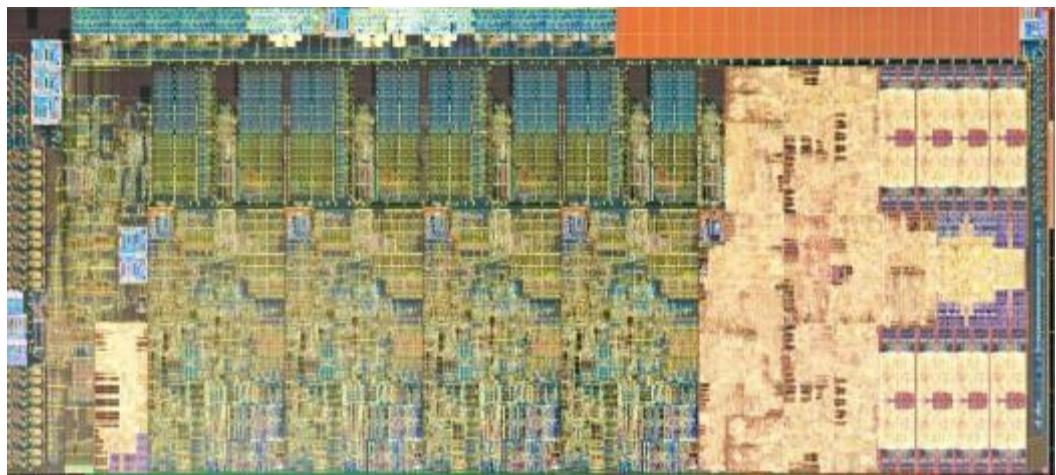
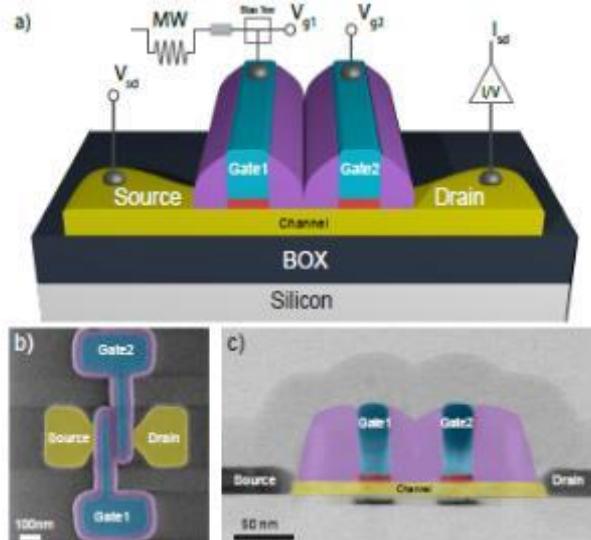
## ARTICLE

Received 28 Jul 2016 | Accepted 14 Oct 2016 | Published 24 Nov 2016

## A CMOS silicon spin qubit



R. Maurand<sup>1,2</sup>, X. Jehl<sup>1,2</sup>, D. Kotekar-Patil<sup>1,2</sup>, A. Corna<sup>1,2</sup>, H. Bohuslavskyi<sup>1,2</sup>, R. Laviéville<sup>1,3</sup>, L. Hutin<sup>1,3</sup>, S. Barraud<sup>1,3</sup>, M. Vinet<sup>1,3</sup>, M. Sanquer<sup>1,2</sup> & S. De Franceschi<sup>1,2</sup>



# Scalability: Reducing wiring complexity

## Dispersive Readout of a Few-Electron Double Quantum Dot with Fast rf Gate Sensors

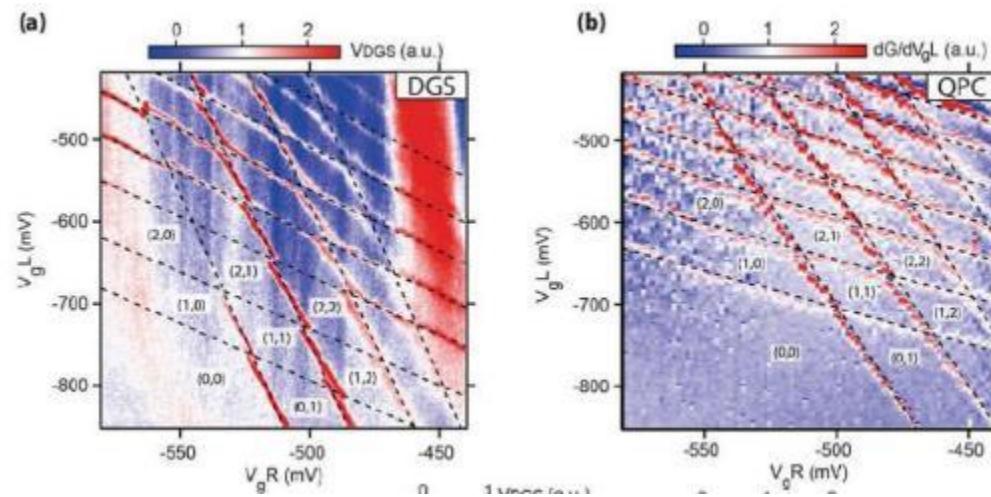
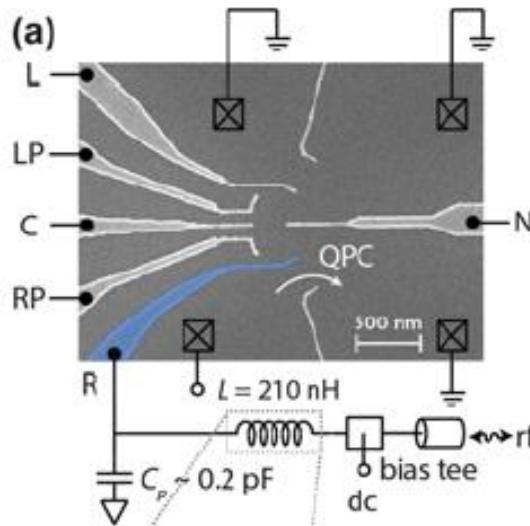
J. I. Colless,<sup>1</sup> A. C. Mahoney,<sup>1</sup> J. M. Homibrook,<sup>1</sup> A. C. Doherty,<sup>1</sup> H. Lu,<sup>2</sup> A. C. Gossard,<sup>2</sup> and D. J. Reilly<sup>1,\*</sup>

<sup>1</sup>*ARC Centre of Excellence for Engineered Quantum Systems, School of Physics, The University of Sydney, Sydney, New South Wales 2006, Australia*

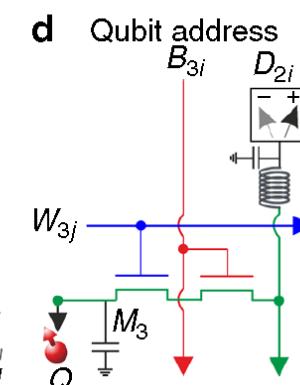
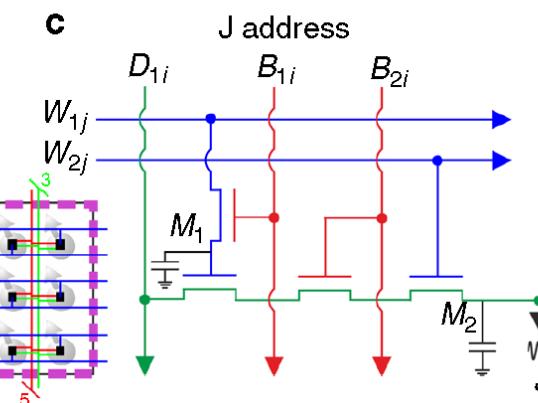
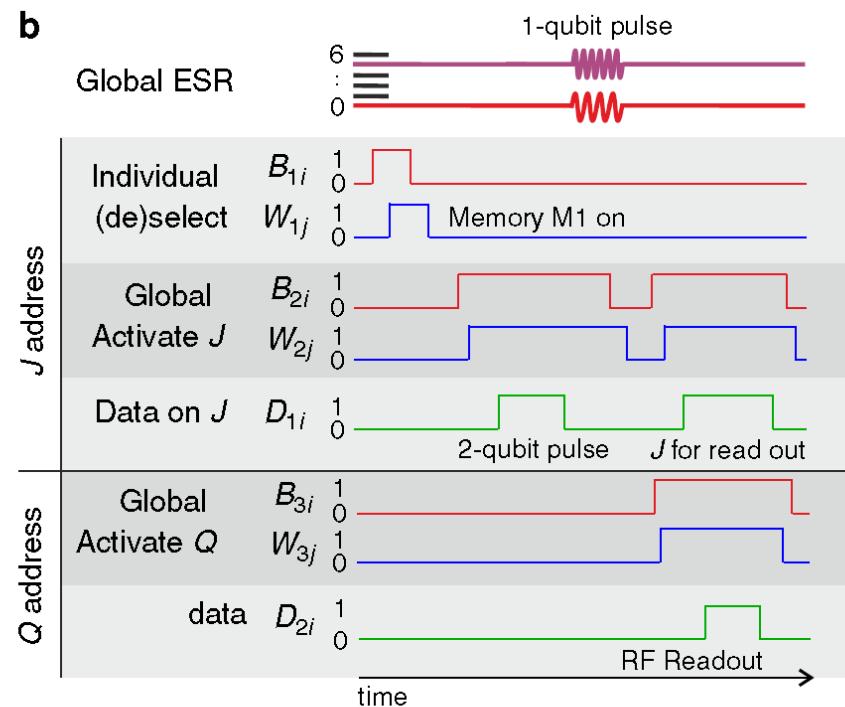
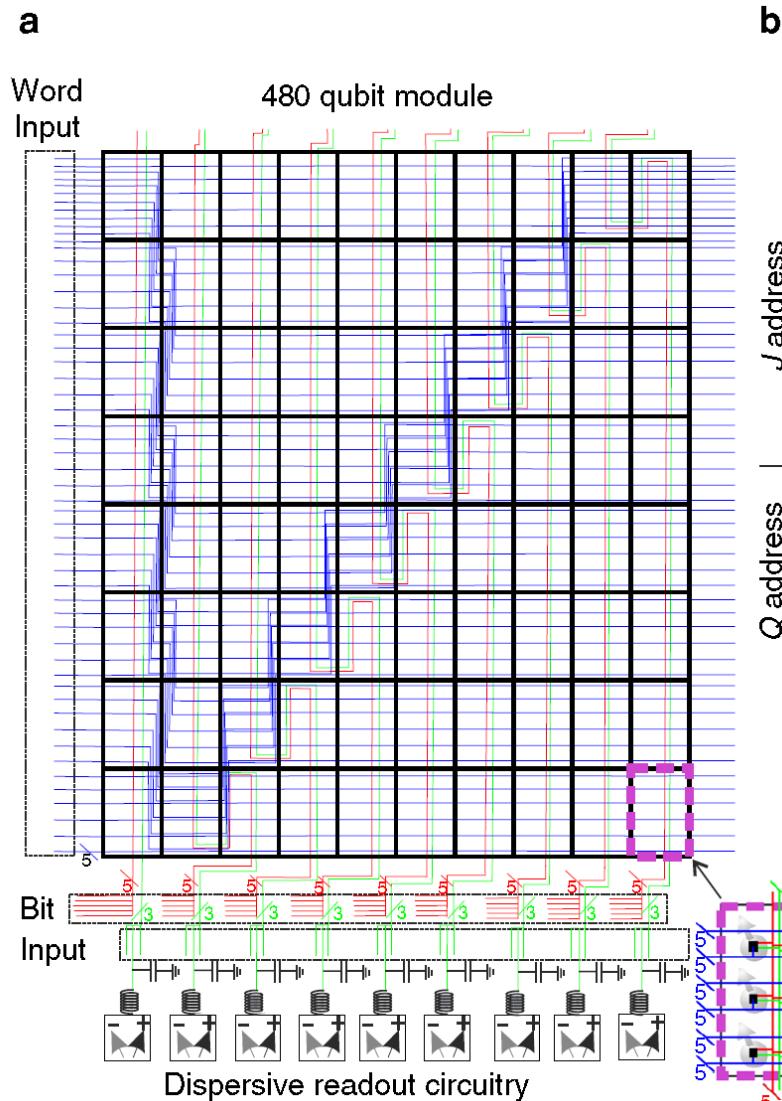
<sup>2</sup>*Materials Department, University of California, Santa Barbara, California 93106, USA*

(Received 17 October 2012; published 25 January 2013)

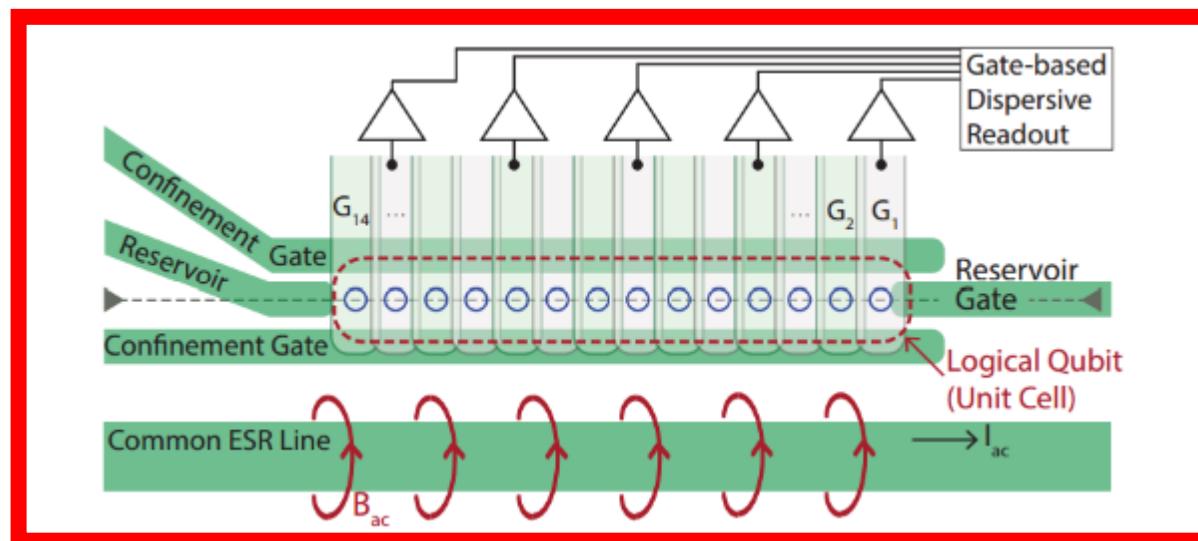
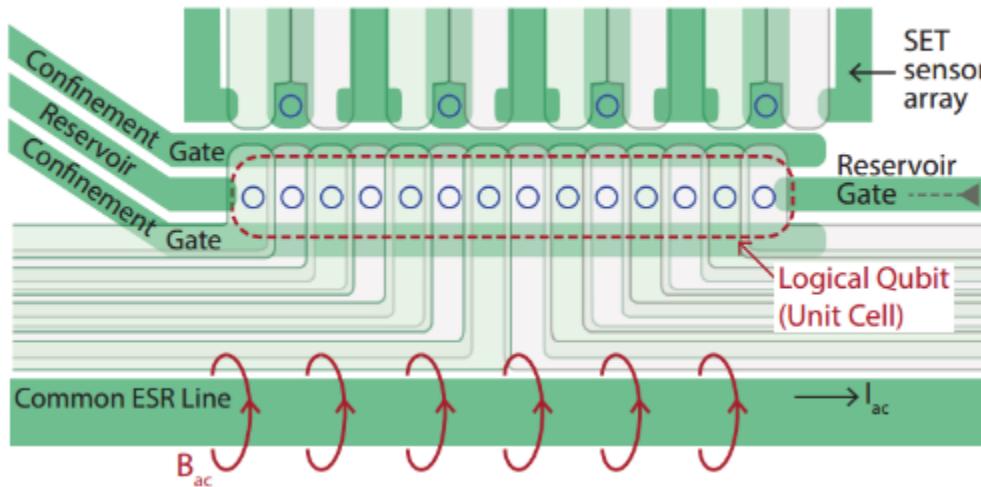
We report the dispersive charge-state readout of a double quantum dot in the few-electron regime using the *in situ* gate electrodes as sensitive detectors. We benchmark this gate sensing technique against the well established quantum point contact charge detector and find comparable performance with a bandwidth of  $\sim 10$  MHz and an equivalent charge sensitivity of  $\sim 6.3 \times 10^{-3} e/\sqrt{\text{Hz}}$ . Dispersive gate sensing alleviates the burden of separate charge detectors for quantum dot systems and promises to enable readout of qubits in scaled-up arrays.



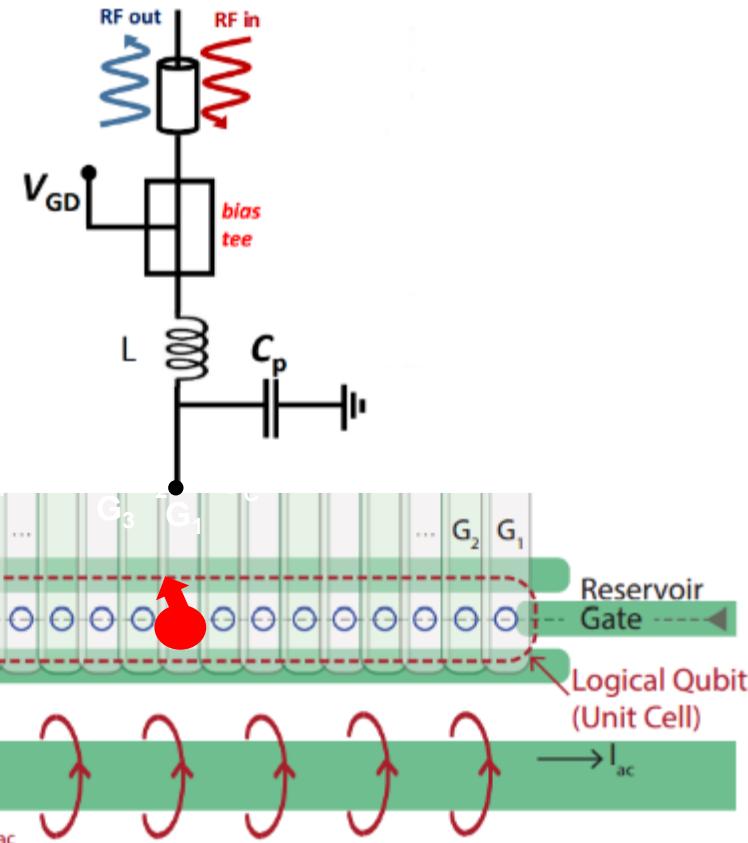
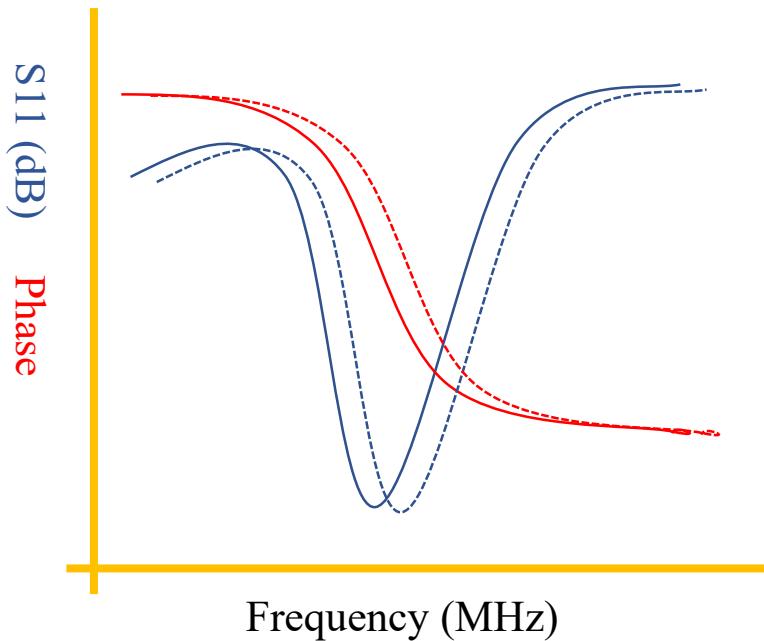
# CMOS QD Qubit Processor: Dispersive Readout



# Dispersive Gate Sensing: Compact, Scalable



# Electron tunnelling shifts resonant frequency



$$f = \frac{1}{2\pi(L(C_p + C_t))^{1/2}}$$

# Single-Shot Dispersive-Gate Spin Readout

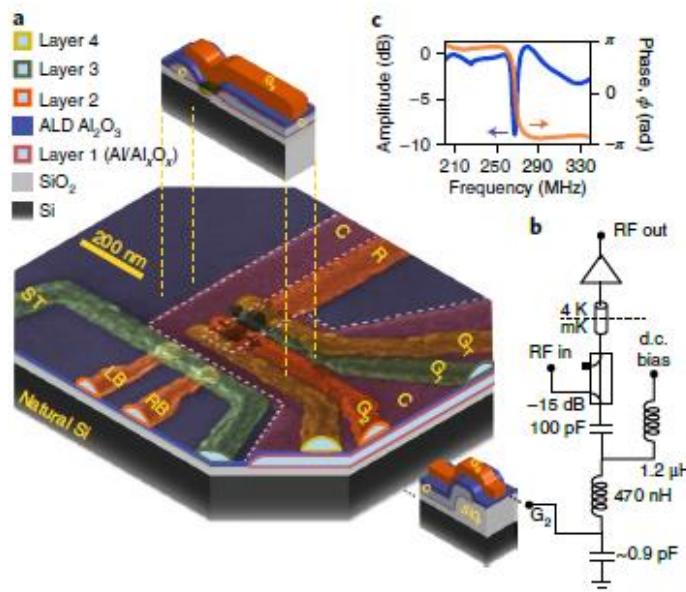
nature  
nanotechnology

LETTERS

<https://doi.org/10.1038/s41565-019-0400-7>

## Gate-based single-shot readout of spins in silicon

Anderson West<sup>1,6</sup>, Bas Hensen<sup>1,6\*</sup>, Alexis Jouan<sup>2</sup>, Tuomo Tanttu<sup>1</sup>, Chih-Hwan Yang<sup>1</sup>, Alessandro Rossi<sup>1</sup>, M. Fernando Gonzalez-Zalba<sup>4</sup>, Fay Hudson<sup>1</sup>, Andrea Morello<sup>1</sup>, David J. Reilly<sup>2,5</sup> and Andrew S. Dzurak<sup>1\*</sup>



Anderson West



Bas Hensen

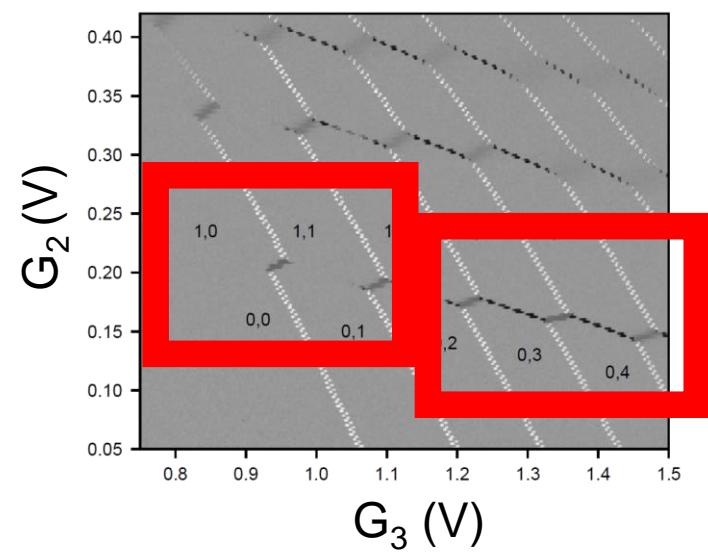
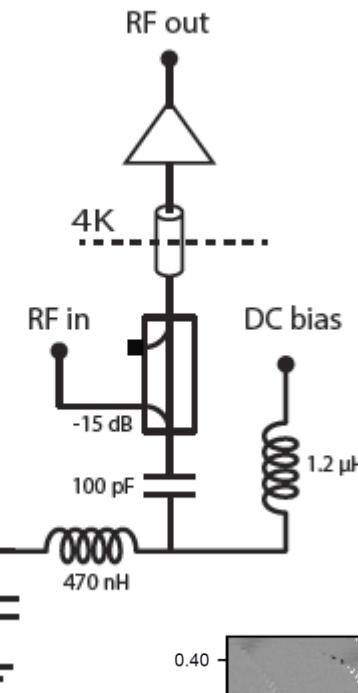
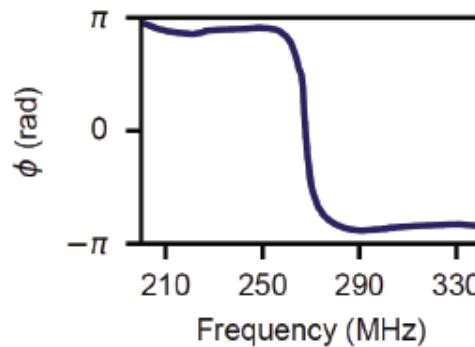
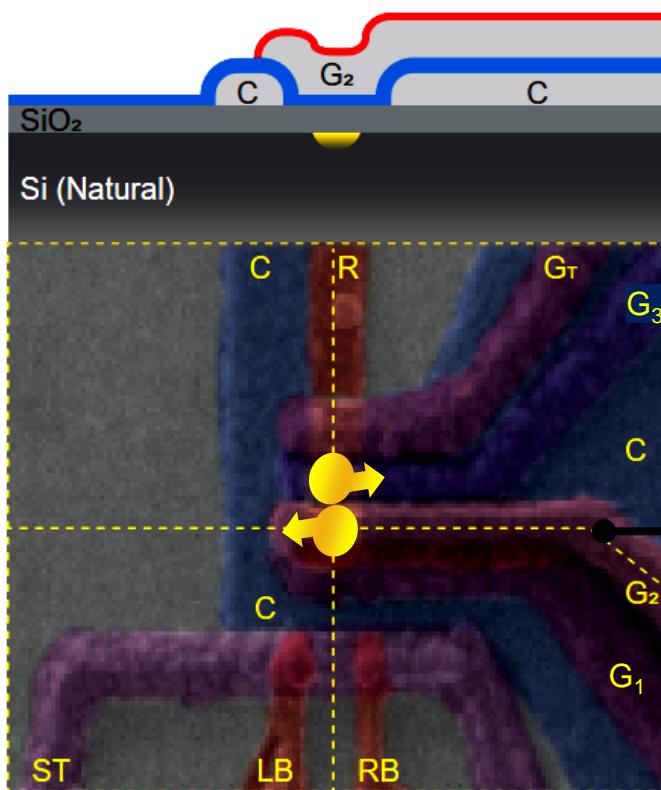


Alexis Jouan

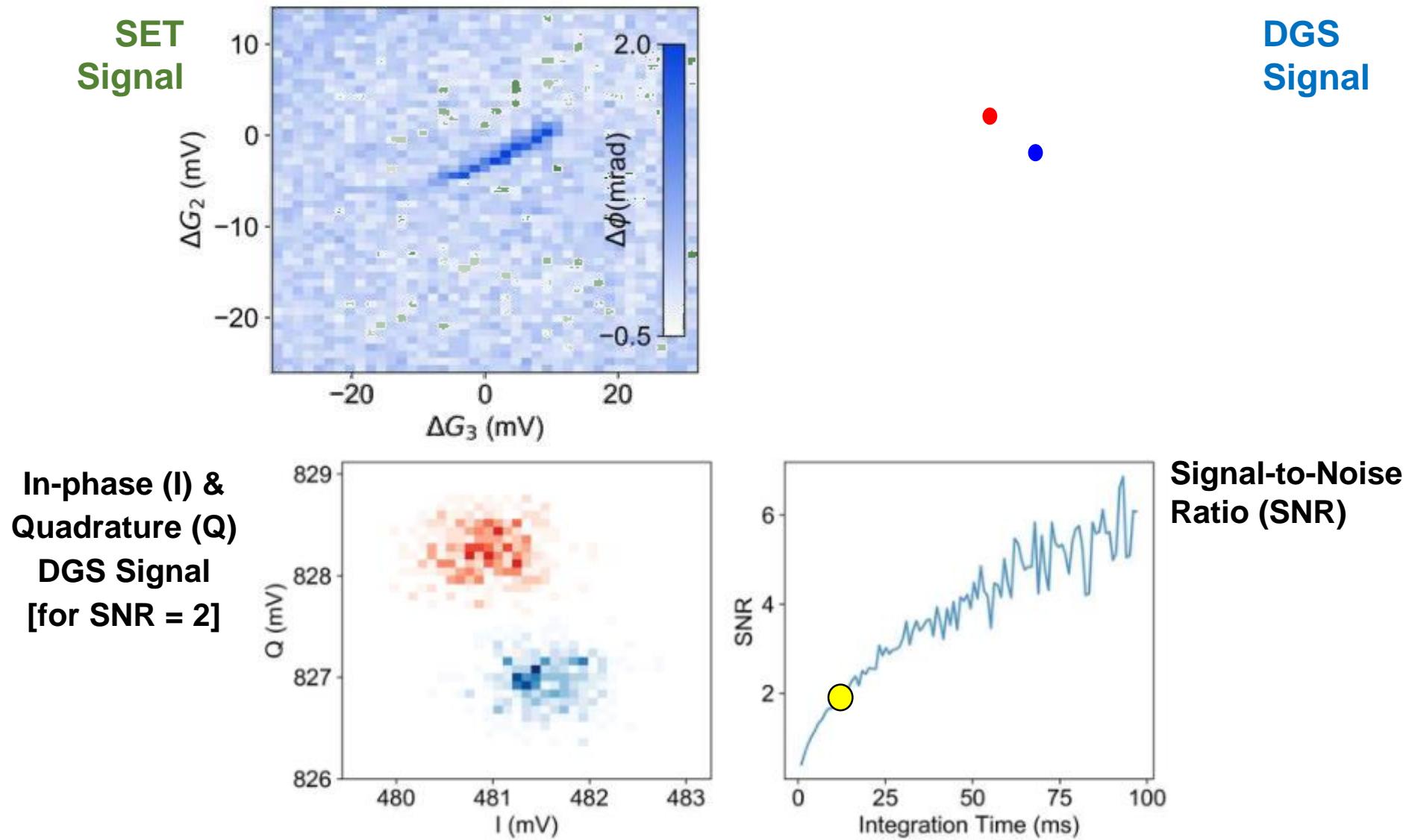


David Reilly

# Optimized Device for *Dispersive Gate Readout*

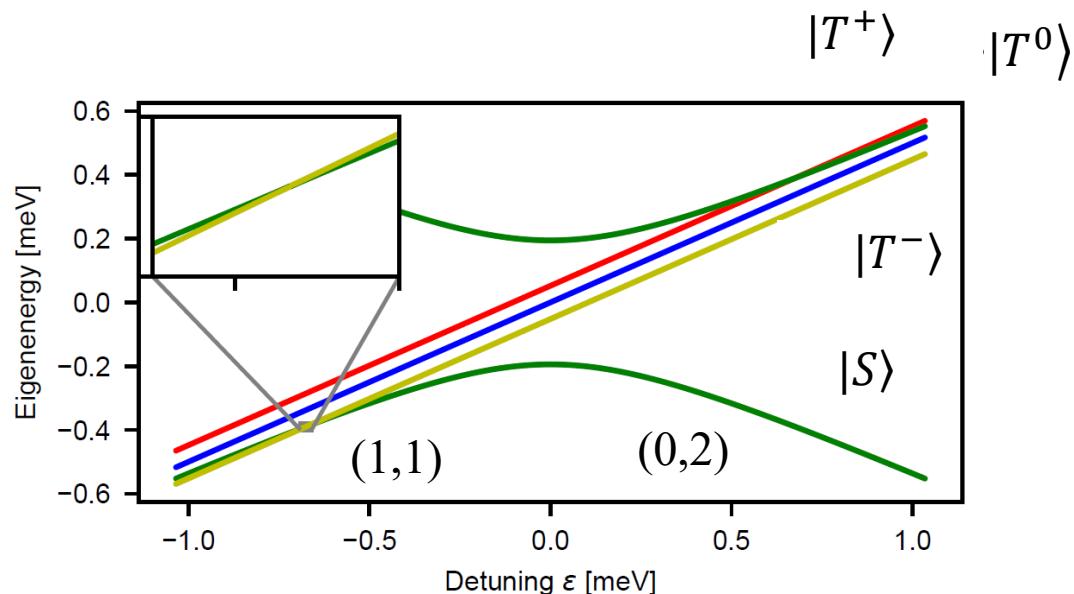
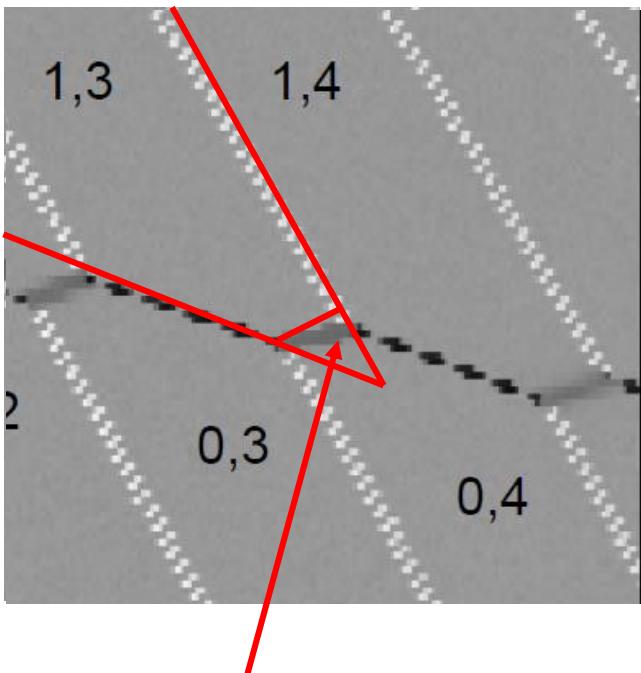


# Dispersive Gate *Charge* Sensing: (0,1) – (1,0)



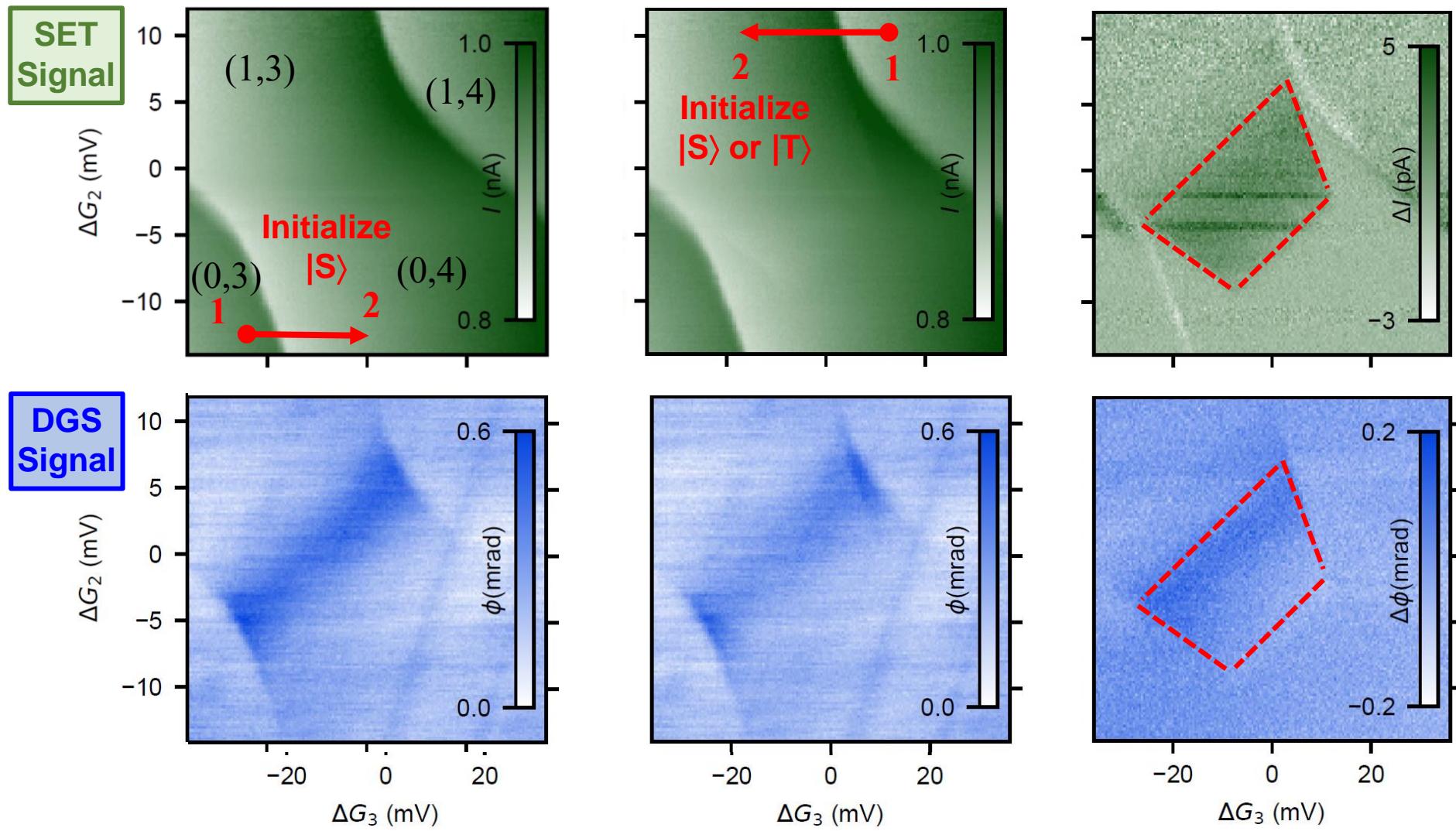
# Pauli Spin Blockade (PSB)

- We do not observe PSB at (1,1)-(0,2) transition  $\Rightarrow \Delta E_v < kT_e$ , or noise level
- We do observe PSB at (1,3)-(0,4) transition  $\Rightarrow \Delta E_{\text{orb}} > \Delta E_v$

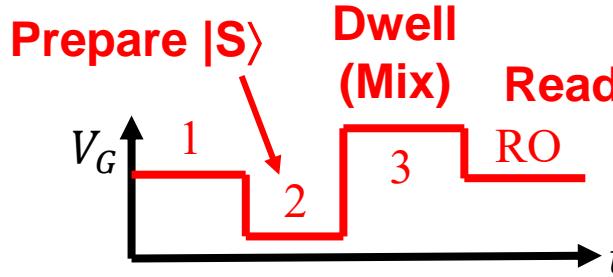
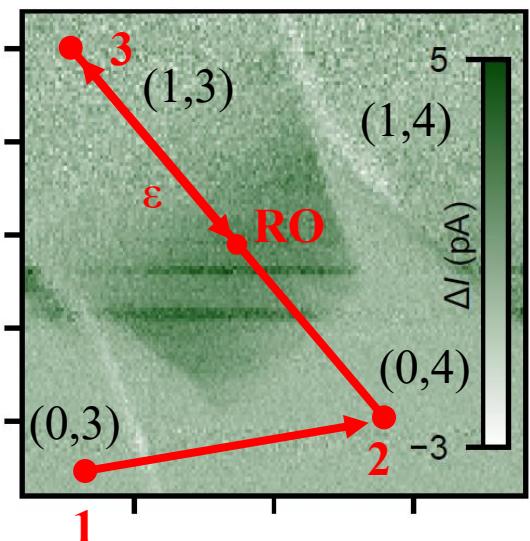


**Spin blockade region:**

- Spin triplet states are blocked from tunnelling from (1,1) to (0,2)
- No tunnelling  $\rightarrow$  No dispersive shift  
 $\rightarrow$  Enables gate-based dispersive spin readout

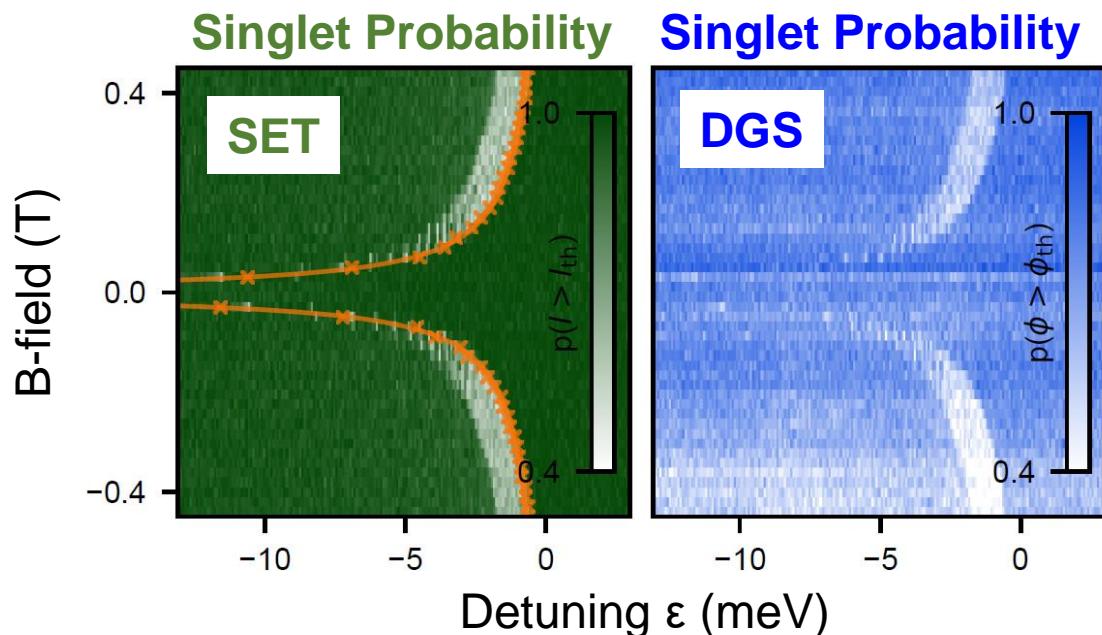
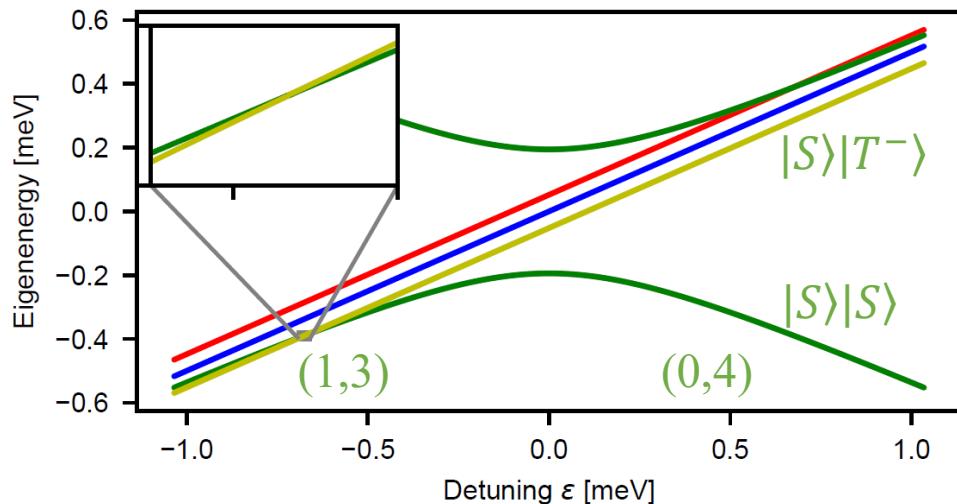


# $|S\rangle - |T^-\rangle$ Mixing: *Spin Funnel via DGS*



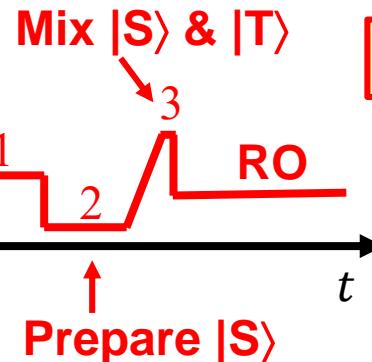
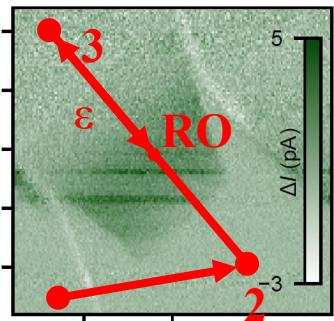
$^{nat}$ Si sample  
 $\rightarrow |S\rangle - |T^-\rangle$  mixing is hyperfine

Fit  $\rightarrow$  Large  $t_c \sim 50$  GHz

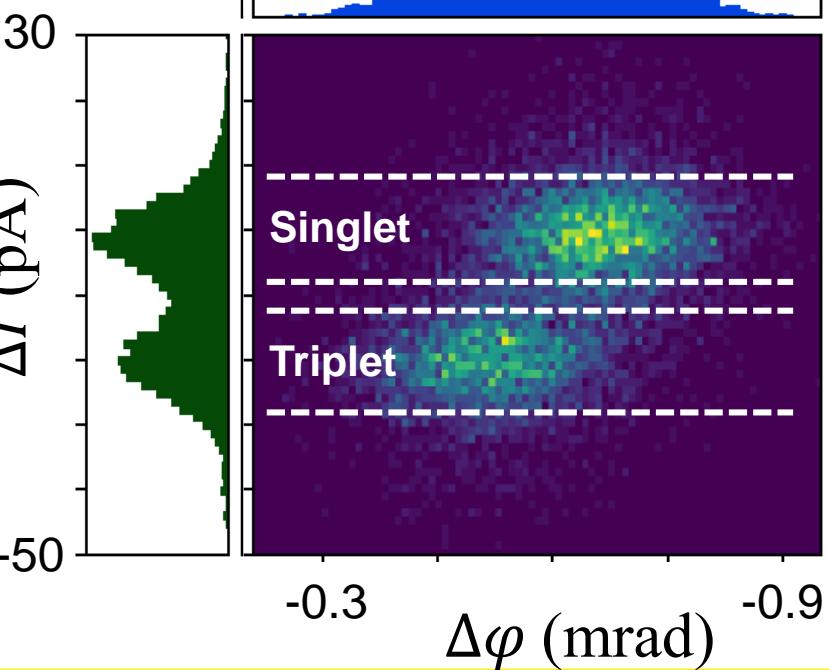


# S-T Single-Shot Spin Readout via DGS

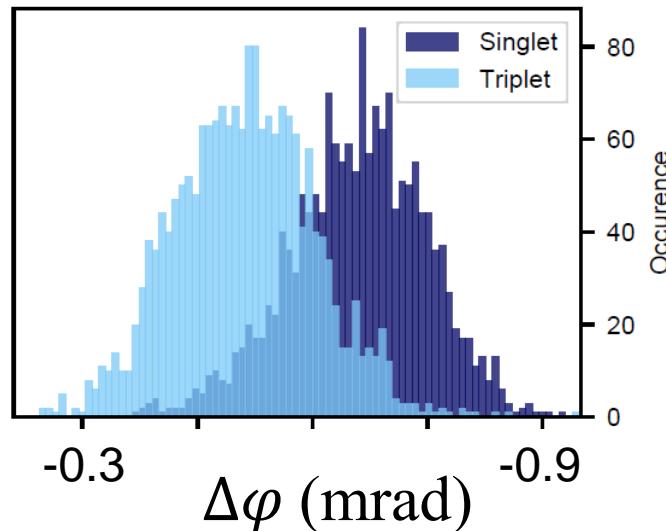
Calibrating signal levels  
with SET sensor to set  
DGS threshold:  
→ DGS RO Fidelity = 70%



DGS Signal



3 ms readout time  
per single-shot  
data point



# Single-Shot Gate-based Spin Readout in Silicon

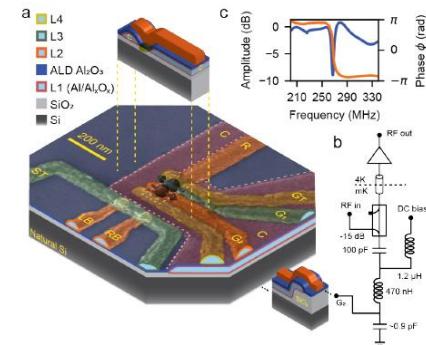
arXiv:1809.01864

## SiMOS QDs

Gate-based single-shot readout of spins in silicon

A. West,<sup>1,\*</sup> B. Hensen,<sup>1,\*</sup> A. Jouan,<sup>2</sup> T. Tanttu,<sup>1</sup> H. Yang,<sup>1</sup> A. Rossi,<sup>3</sup>  
M.F. Gonzalez-Zalba,<sup>4</sup> F.E. Hudson,<sup>1</sup> A. Morello,<sup>1</sup> D.J. Reilly,<sup>2,5</sup> and A.S. Dzurak<sup>1</sup>

<sup>1</sup>Centre for Quantum Computation and Communication Technology,  
School of Electrical Engineering and Telecommunications,  
The University of New South Wales, Sydney, New South Wales 2052, Australia  
<sup>2</sup>ARC Centre of Excellence for Engineered Quantum Systems,  
School of Physics, The University of Sydney, Sydney, NSW 2006, Australia

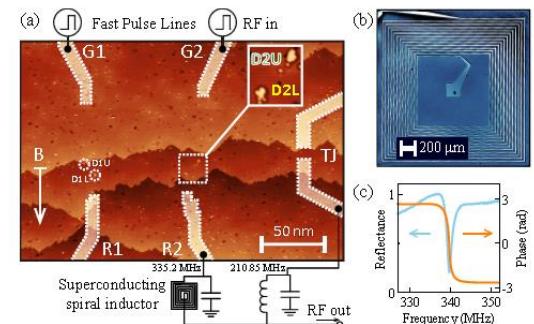


arXiv:1809.01802; PRX (2018)

## STM P-donor QDs

Single-shot single-gate RF spin readout in silicon

P. Pakkiam, A. V. Timofeev, M.G. House, M.R. Hogg, T. Kobayashi, M. Koch, S. Rogge, and M.Y. Simmons  
CQC2T, UNSW, Sydney, Australia  
(Dated: September 7, 2018)

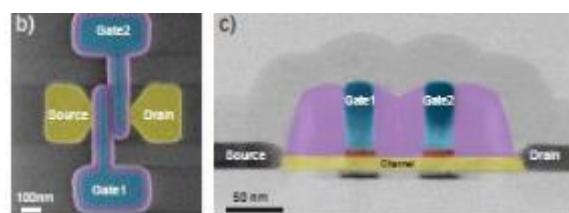


arXiv:1809.04584

## Si CMOS QDs

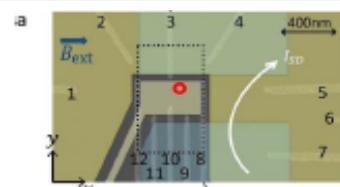
Gate-Based High Fidelity Spin Readout in a CMOS Device

Matias Urdampilleta,<sup>1,\*</sup> David J. Niegemann,<sup>1</sup> Emmanuel Charnier,<sup>1</sup> Baptiste Jadot,<sup>1</sup> Cameron Spence,<sup>1</sup> Pierre-André Mortemousque,<sup>1</sup> Christopher Bäuerle,<sup>1</sup> Louis Hutin,<sup>2</sup> Benoit Bertrand,<sup>2</sup> Sylvain Barraud,<sup>2</sup> Romain Maurand,<sup>3</sup> Marc Sanquer,<sup>3</sup> Xavier Jehl,<sup>3</sup> Silvano De Franceschi,<sup>3</sup> Maud Vinet,<sup>2</sup> and Tristan Meunier<sup>1,†</sup>



## Si/SiGe QDs

Zheng, Vandersypen et al. – TU Delft



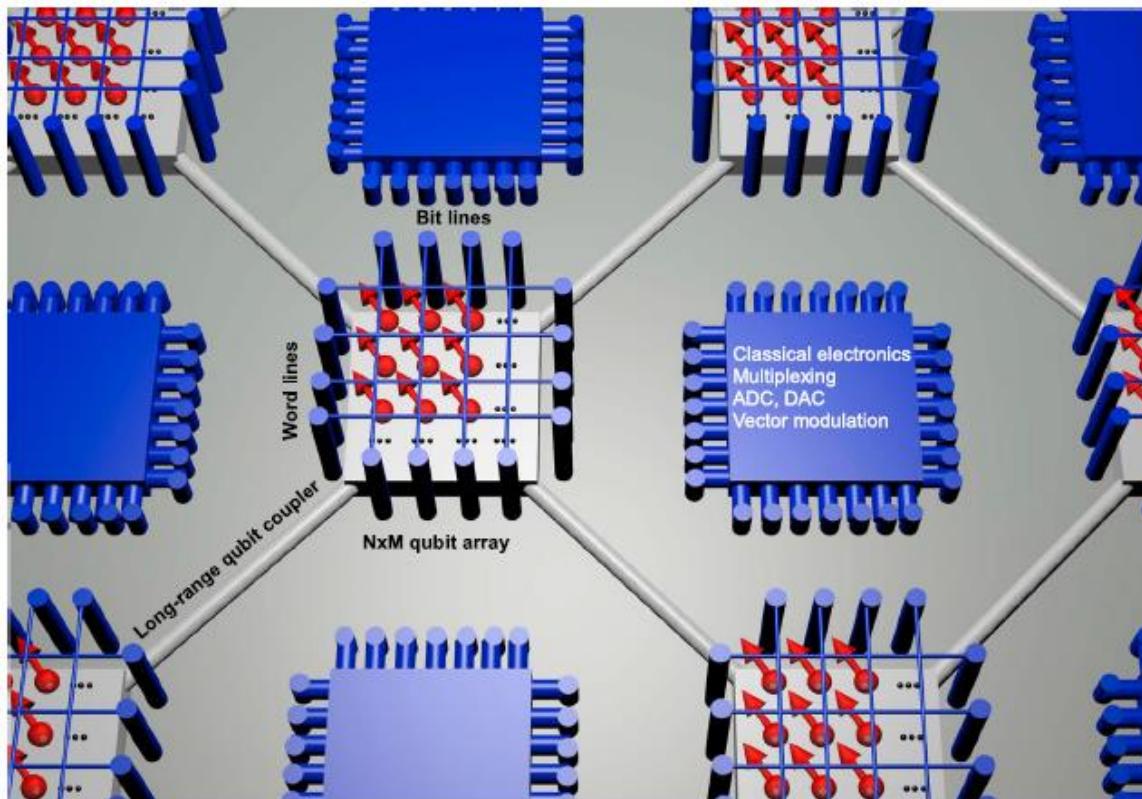
# Scalability – Wiring Fan-out: *Problems & Solutions*

REVIEW ARTICLE

OPEN

Interfacing spin qubits in quantum dots and donors—hot, dense, and coherent

L. M. K. Vandersypen<sup>1,2</sup>, H. Bluhm<sup>3</sup>, J. S. Clarke<sup>2</sup>, A. S. Dzurak<sup>4</sup>, R. Ishihara<sup>1</sup>, A. Morello<sup>4</sup>, D. J. Reilly<sup>5</sup>, L. R. Schreiber<sup>3</sup> and M. Veldhorst<sup>1</sup>



# Scalability: *Coupling Spin to Microwave Photons*

Strong coupling of a spin qubit to a superconducting stripline cavity

Xuedong Hu

*Department of Physics, University at Buffalo, SUNY, Buffalo, New York 14260-1500, USA*

Yu-xi Liu

*Institute of Microelectronics, Tsinghua University, Beijing 100084, China,*

*Tsinghua National Laboratory for Information Science and Technology (TNList), Tsinghua University, Beijing 100084, China,*

*and Advanced Science Institute, RIKEN, Wako, Saitama 351-0918, Japan*

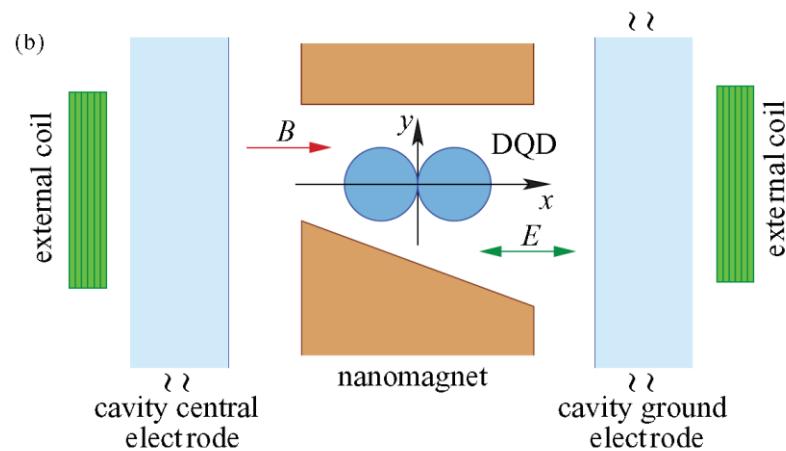
Franco Nori

*Advanced Science Institute, RIKEN, Saitama 351-0918, Japan and Department of Physics, University of Michigan, Ann Arbor, Michigan*

*48109-1040, USA*

(Received 2 May 2012; revised manuscript received 27 June 2012; published 16 July 2012)

We study electron-spin-photon coupling in a single-spin double quantum dot embedded in a superconducting stripline cavity. With an external magnetic field, we show that either a spin-orbit interaction (for InAs) or an inhomogeneous magnetic field (for Si and GaAs) could produce a strong spin-photon coupling, with a coupling strength of the order of 1 MHz. **With an isotopically purified Si double dot, which has a very long spin coherence time for the electron, it is possible to reach the strong-coupling limit between the spin and the cavity photon, as in cavity quantum electrodynamics.** The coupling strength and relaxation rates are calculated based on parameters of existing devices, making this proposal experimentally feasible.

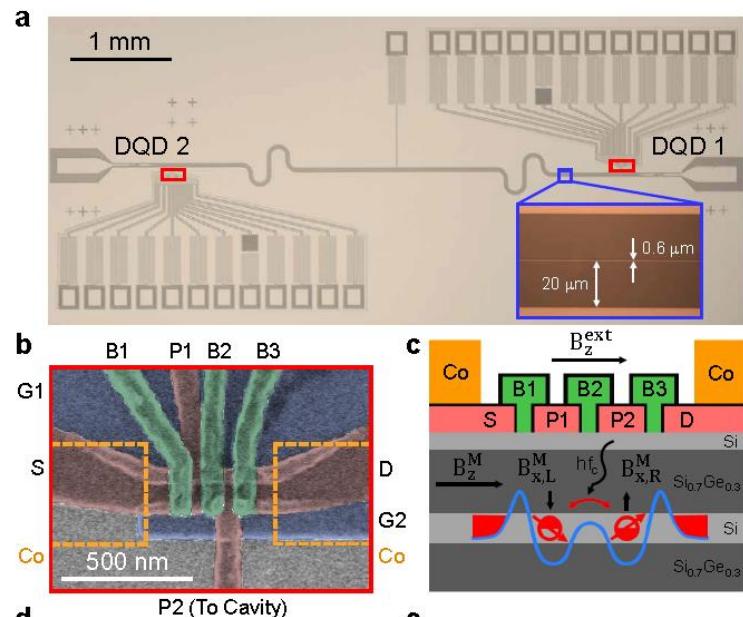
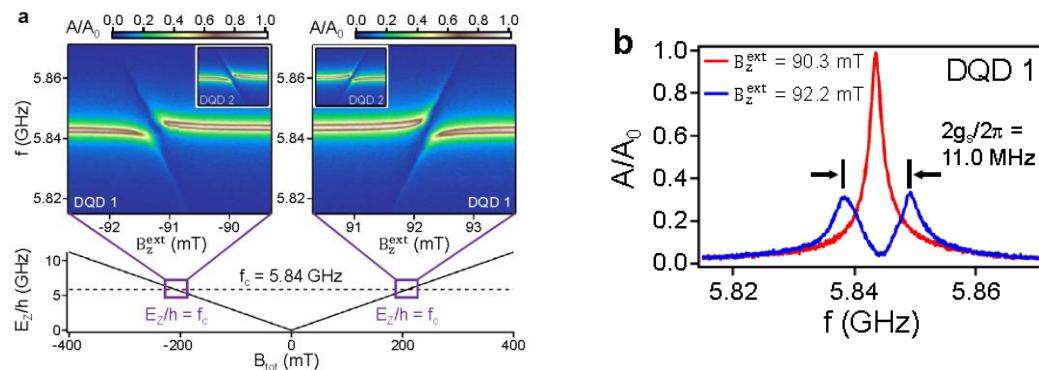


# Scalability: *Coupling Spin to Microwave Photons*

## A Coherent Spin-Photon Interface in Silicon

X. Mi,<sup>1</sup> M. Benito,<sup>2</sup> S. Putz,<sup>1</sup> D. M. Zajac,<sup>1</sup>

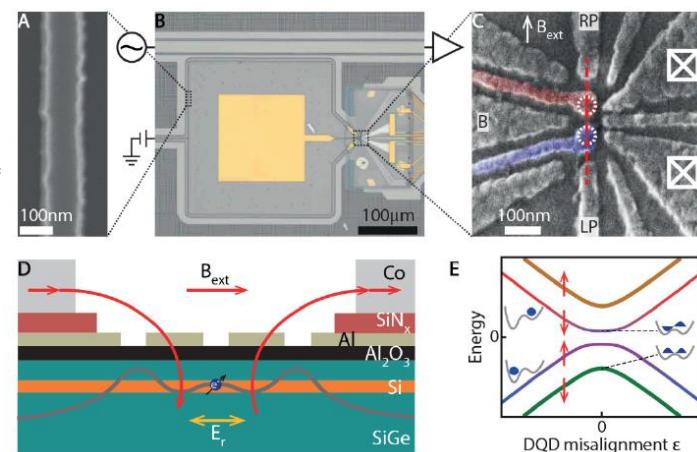
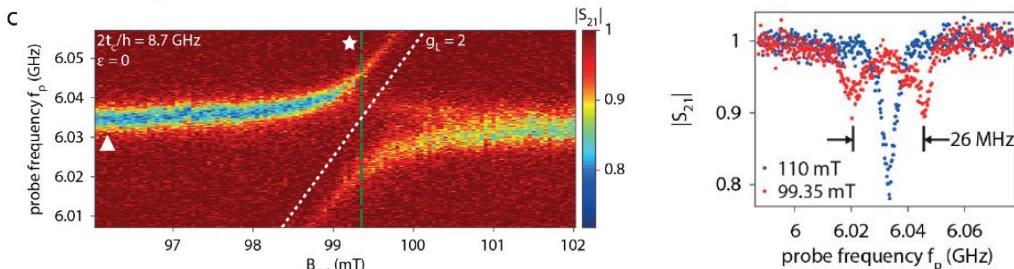
J. M. Taylor,<sup>3</sup> Guido Burkard,<sup>2</sup> and J. R. Petta<sup>1,\*</sup>



## Strong spin-photon coupling in silicon

N. Samkharadze,<sup>1,\*</sup> G. Zheng,<sup>1,\*</sup> N. Kalhor,<sup>1</sup> D. Brousse,<sup>2</sup> A. Sammak,<sup>2</sup> U. C. Mendes,<sup>3</sup> A. Blais,<sup>3,4</sup> G. Scappucci,<sup>1</sup> L. M. K. Vandersypen,<sup>1†</sup>

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# What about power dissipation?

ARTICLE

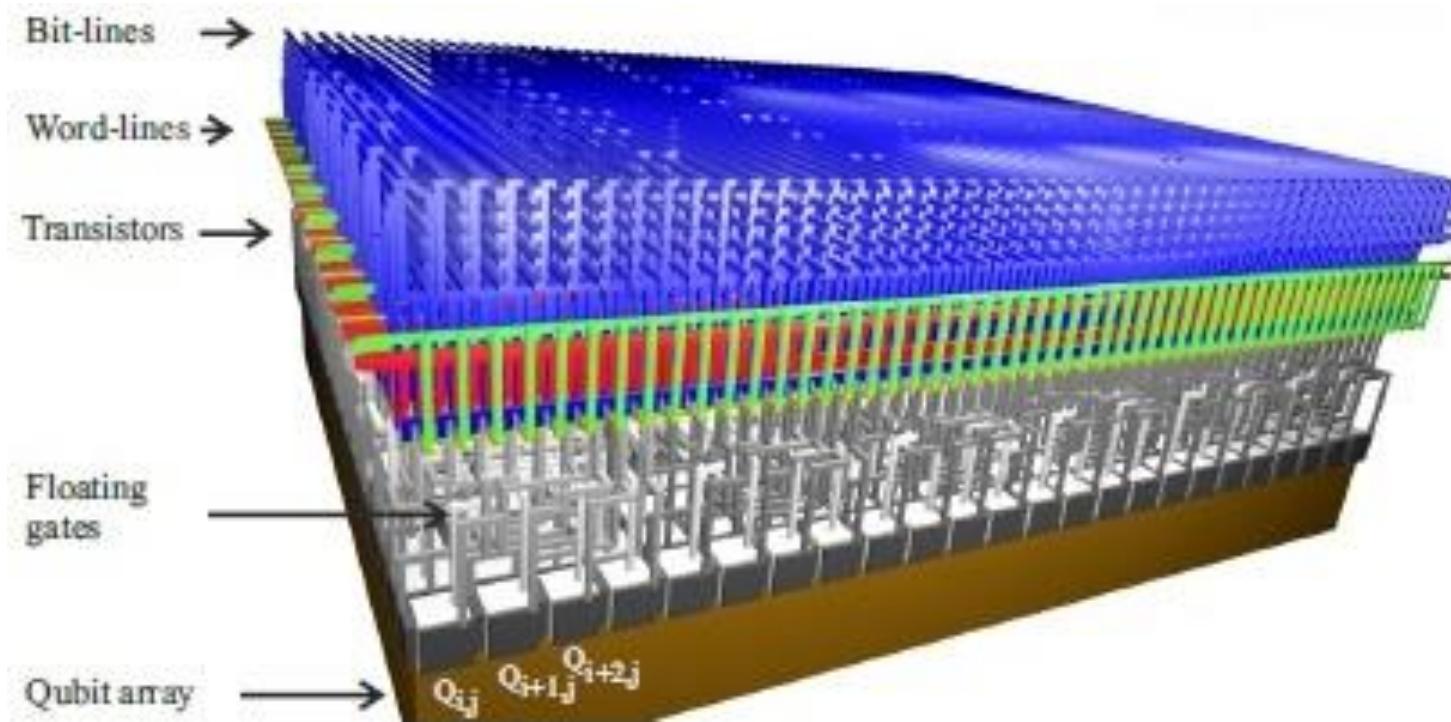
DOI: 10.1038/s41467-017-01905-6

OPEN



## Silicon CMOS architecture for a spin-based quantum computer

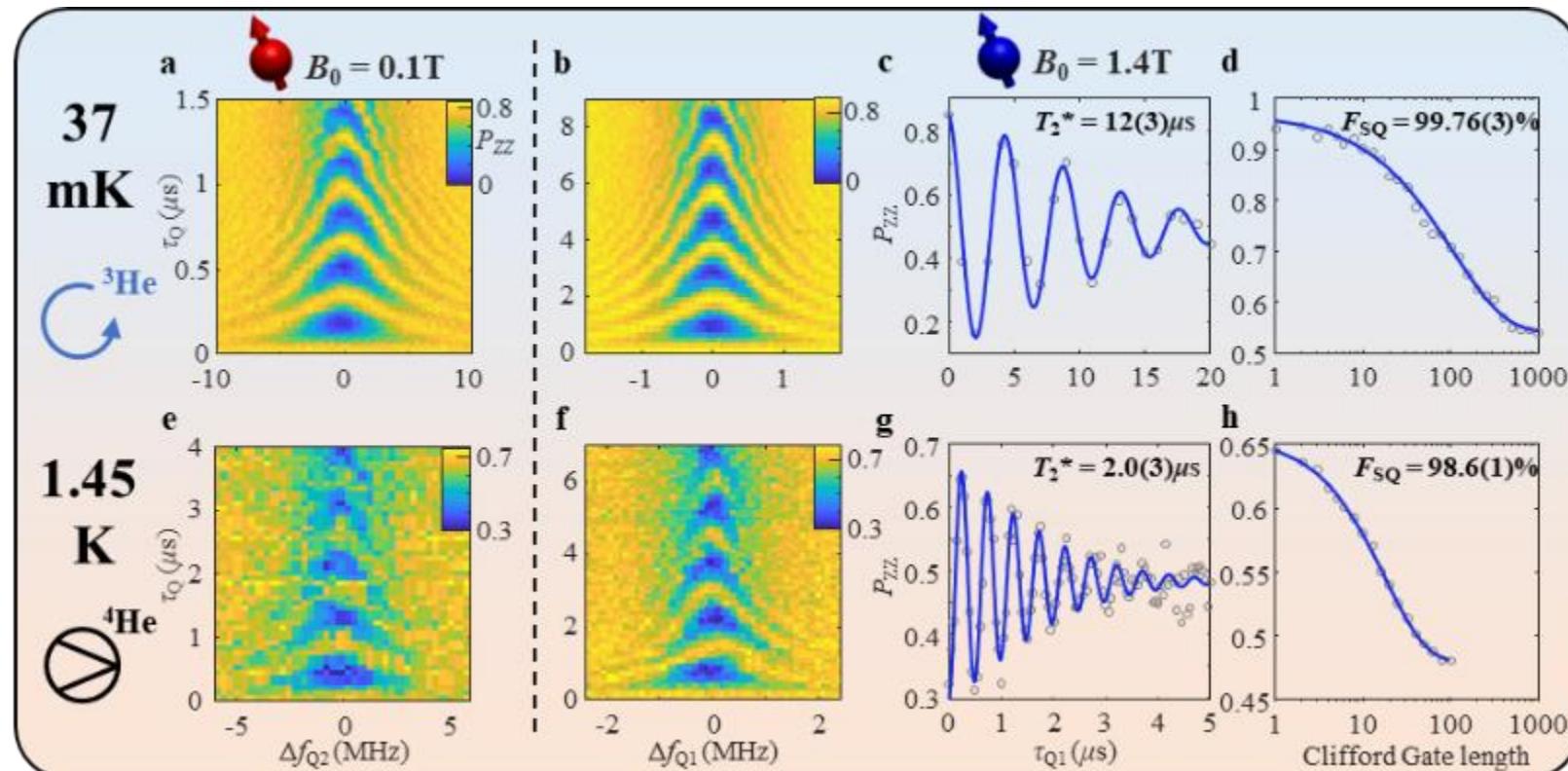
M. Veldhorst<sup>1,2</sup>, H.G.J. Eenink<sup>1,2</sup>, C.H. Yang<sup>2</sup> & A.S. Dzurak<sup>2</sup>



# SiMOS QD Qubit Operation at 1.5 Kelvin

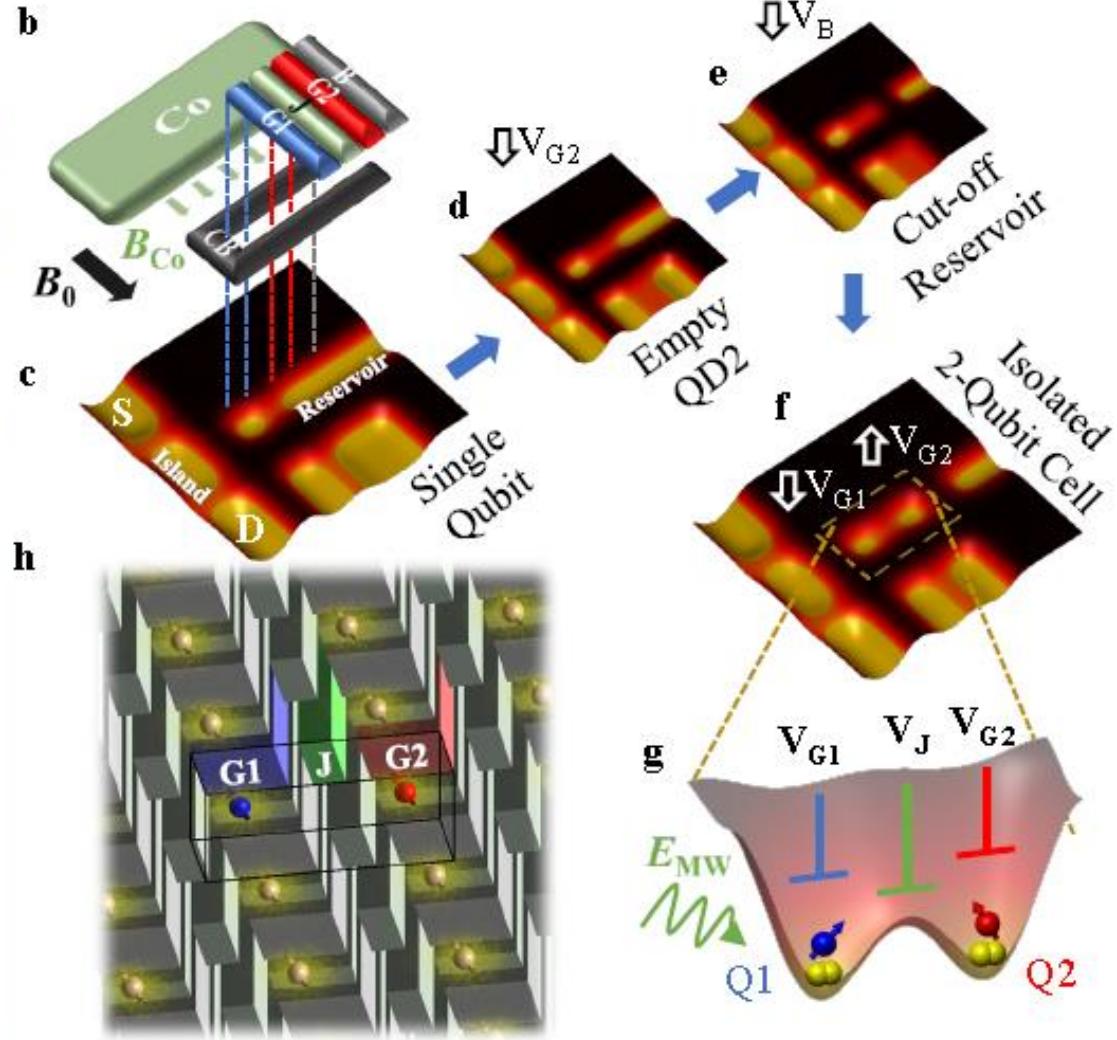
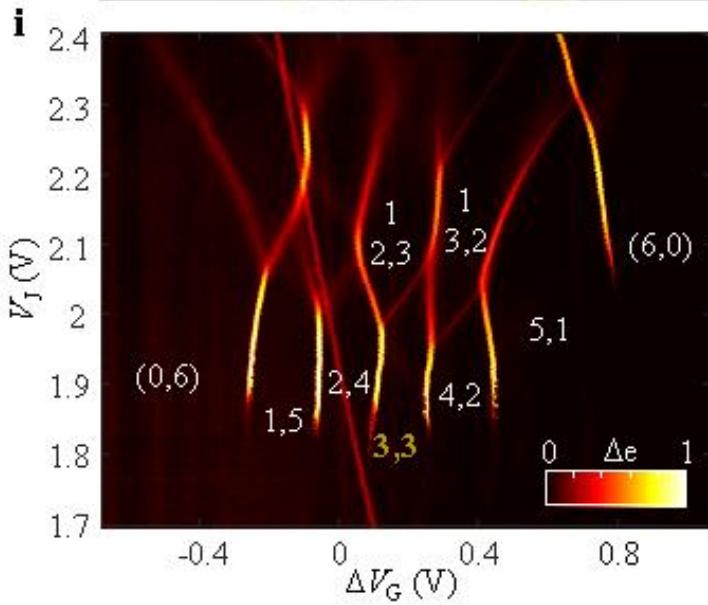
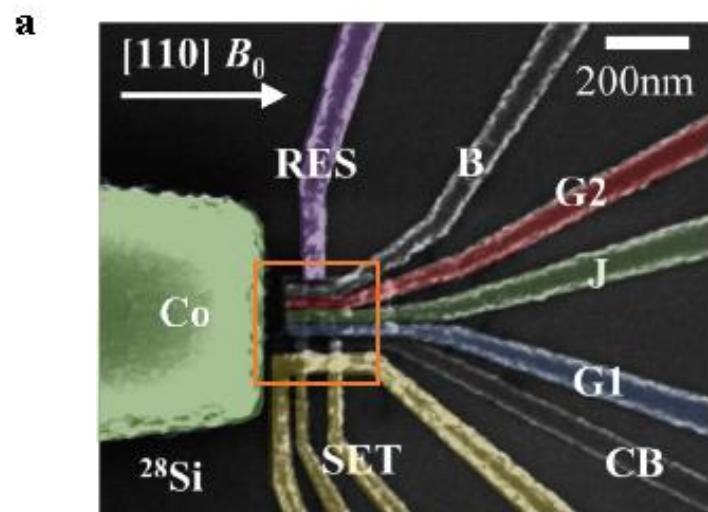
Silicon quantum processor unit cell operation above one Kelvin

C. H. Yang,<sup>1,\*</sup> R. C. C. Leon,<sup>1</sup> J. C. C. Hwang,<sup>1,†</sup> A. Saraiva,<sup>1</sup> T. Tanttu,<sup>1</sup> W. Huang,<sup>1</sup> J. Camirand Lemyre,<sup>2</sup> K. W. Chan,<sup>1,‡</sup> K. Y. Tan,<sup>1,‡</sup> F. E. Hudson,<sup>1</sup> K. M. Itoh,<sup>3</sup> A. Morello,<sup>1</sup> M. Pioro-Ladrière,<sup>2,4</sup> A. Laucht,<sup>1</sup> and A. S. Dzurak<sup>1,§</sup>

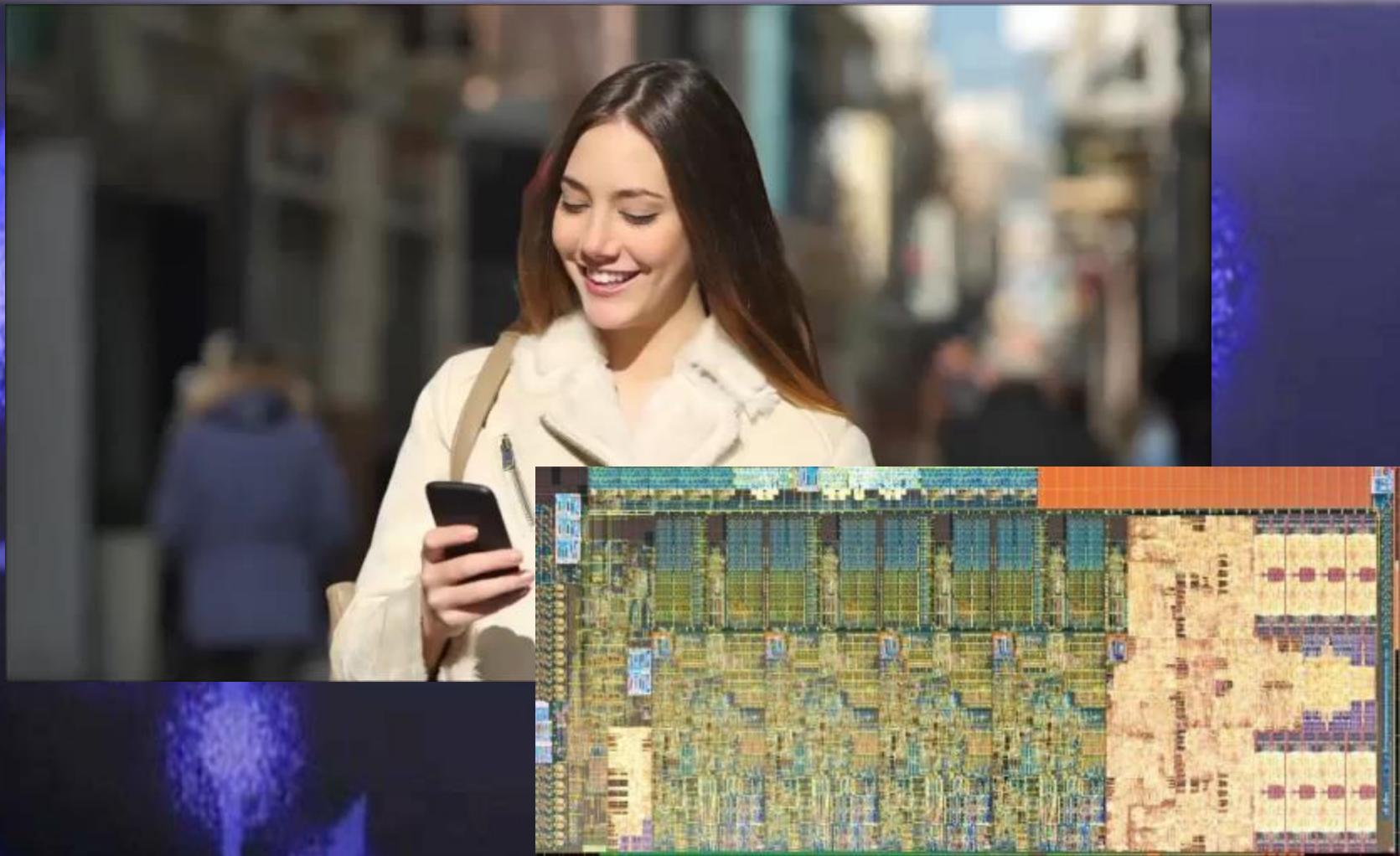


⇒ 1.5 K performance comparable to <sup>nat</sup>Si at 100 mK !

# SiMOS QD Qubit Operation at 1.5 Kelvin



# *Silicon Quantum Computing*



# Dzurak Research Group (UNSW)

## Postdoctoral Researchers



Henry Yang



Chris Escott



Tuomo Tanttu



Bas Hensen



Ruichen Zhao



Fay Hudson

## Graduate Students



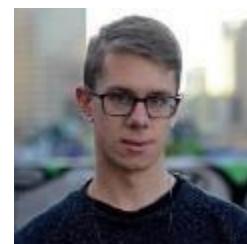
Wister Huang



Ross Leon



Anderson West



Will Gilbert

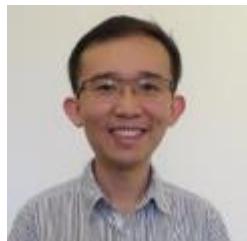


Alexis Shaw

## Former Team Members (2017-18)



Michael Fogarty (now UCL)



Kok Wai Chan (now Aalto U)



Jason Hwang (now ANFF-Sydney)

# Collaborators



Morello



Laucht



Culcer



Hamilton



**UNSW**  
SYDNEY



Bartlett



Flammia



Reilly



Gyure



Ladd



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**SYDNEY**



Rossi

UNIVERSITY OF  
CAMBRIDGE



Poirier-  
Ladrière

UNIVERSITÉ DE  
SHERBROOKE



Veldhorst

TU Delft

Ruskov  
&  
Tahan

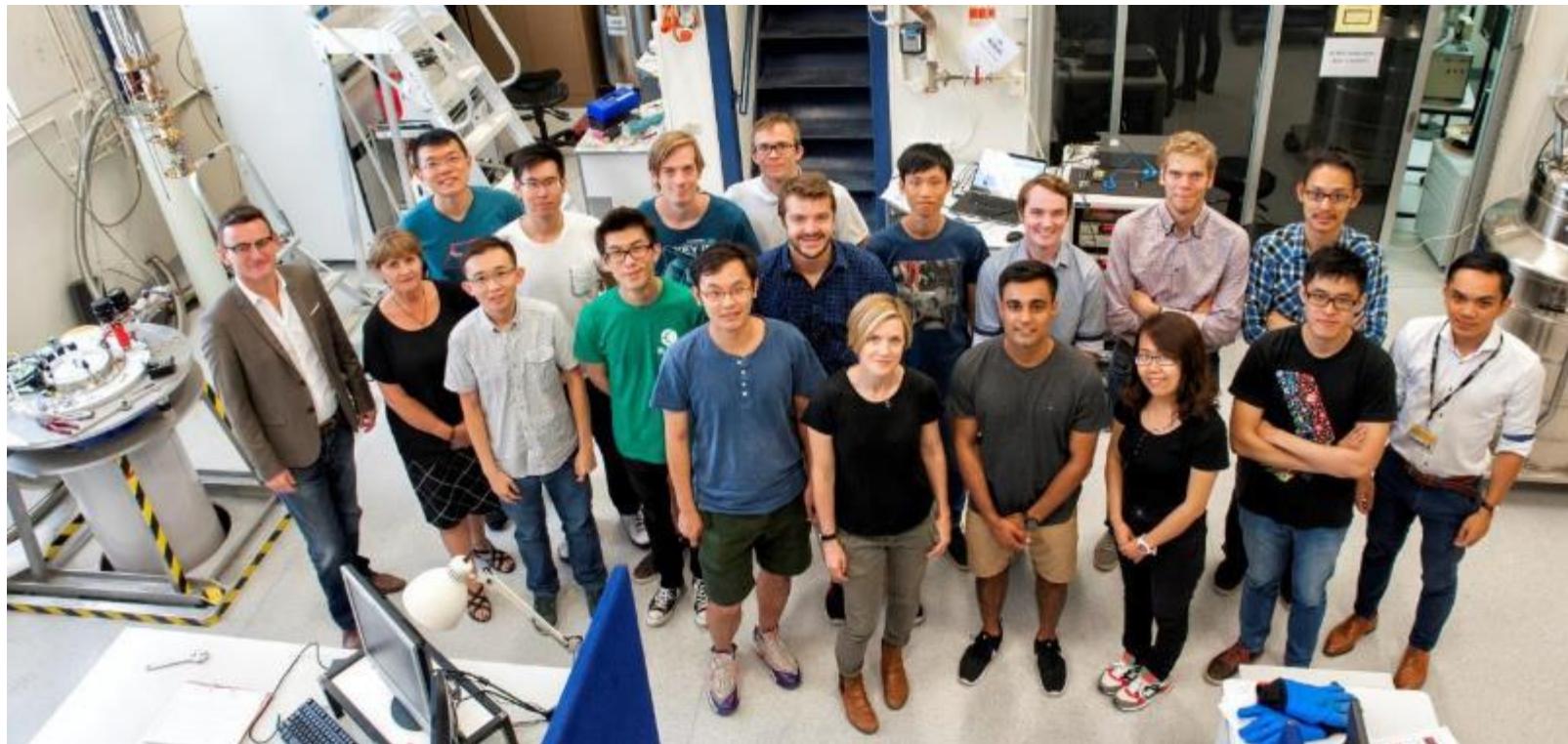
LPS  
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Itoh  
Keio University

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1858

# Acknowledgments



ANFF



Australian Government  
Australian Research Council



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Prof. Andrew Dzurak - Intl. W-S on Cryo-Electronics for Quantum Systems, Chicago, 17 June 2019

